

**M-7679 DNP 3.0
Communication Database
Rev. 1.2**



M-7679 DNP3.0 COMMUNICATION DATABASE

Specifications presented herein are thought to be accurate at the time of publication but are subject to change without notice.

No warranties of any kind are implied on the information contained in this document.

Rev	1.0	05/2023 (D-0347V03.35.04) Create new DNP document for M-7679
Rev	1.1	08/2023 (D-0347V03.35.04) Update Beckwith logo with registration mark
Rev	1.2	06/2024 (FW: D-0347V03.38.09 and SW: D-0348V10.39.10) Updated points

Beckwith Electric DNP3.0 Points Grouping

Introduction

The communication database profile in the M-7679 with the DNP3.0 protocol is grouped into five object types. These are:

- **Single-Bit Binary Inputs (Status)** (object 01, variation 01, 02):
These points are Report by Exception with time stamp. They are Considered class 1 data (an event with high priority).
- **Analog Output Block/Status (Setpoints)** (object 40, variation 01, 02/object 41, variation 01, 02):
With these points, you can write and read all the setpoints, configuration setpoints and the real-time clock setpoints.
- **Control Relay Output Block (Direct Control)** (object 12, variation 01, 02):
These points are Select Before Operate. Once the select command is received, the operate command will only be honored if it arrives within 20 seconds.
The Select Before Operate timeout can be configured using IPSCOM® DNP Editor.
- **Analog Inputs** (object 30, variation 01, 02, 03, 04):
These points represent all instantaneous measurements, demands and dates/times.
- **Binary Counters** (Object 20, Variation 01,02, 05, 06):
These points represents counters.

All points in the M-7679 are of static type, meaning that a class 0 poll will dump all data to the querying RTU (Via Object 60 Variation 0).

Index	Name	Value	Mask
429	HLT F50 Ground/Ground Sensiti...	TRUE	CLASS_ONE
430	HLT F50 Ground/Ground Sensiti...	TRUE	CLASS_ONE
431	HLT F51 phase A pickup status bit	TRUE	CLASS_ONE
432	HLT F51 phase A timeout status bit	TRUE	CLASS_ONE
433	HLT F51 phase B pickup status bit	TRUE	CLASS_ONE
434	HLT F51 phase B timeout status bit	TRUE	CLASS_ONE
435	HLT F51 phase C pickup status bit	TRUE	CLASS_ONE
436	HLT F51 phase C timeout status bit	TRUE	CLASS_ONE
437	HLT F51 Ground/Ground Sensiti...	TRUE	CLASS_ONE
438	HLT F51 Ground/Ground Sensiti...	TRUE	CLASS_ONE
439	Sectionalizer pickup status bit	TRUE	CLASS_ONE
440	Sectionalizer timeout status bit	TRUE	CLASS_ONE
441	Close Block Ph A pickup status bit	TRUE	CLASS_ONE
442	Close Block Ph A timeout status bit	TRUE	CLASS_ONE
443	Close Block Ph B pickup status bit	TRUE	CLASS_ONE
444	Close Block Ph B timeout status bit	TRUE	CLASS_ONE
445	Close Block Ph C pickup status bit	TRUE	CLASS_ONE
446	Close Block Ph C timeout status bit	TRUE	CLASS_ONE
447	Loop Scheme pickup status bit	TRUE	CLASS_ONE
448	Loop Scheme timeout status bit	TRUE	CLASS_ONE
449	F27#5 3 phases pickup status bit	TRUE	CLASS_ONE
450	F27#5 3 phases timeout status bit	TRUE	CLASS_ONE
451	F27#6 3 phases pickup status bit	TRUE	CLASS_ONE
452	F27#6 3 phases timeout status bit	TRUE	CLASS_ONE
453	F27#7 3 phases pickup status bit	TRUE	CLASS_ONE
454	F27#7 3 phases timeout status bit	TRUE	CLASS_ONE
455	F27#8 3 phases pickup status bit	TRUE	CLASS_ONE

Index	Name	Value	Mask
0	Input 1 Status	TRUE	CLASS_ONE
1	Reclose State 5 for Phase-A	TRUE	CLASS_ONE
2	F51N#1 pickup status bit	TRUE	CLASS_ONE
3	IPSLOGIC#1 pickup status bit	TRUE	CLASS_ONE
4	Close Block Ph A timeout status bit	TRUE	CLASS_ONE
5	Close Block Ph B timeout status bit	TRUE	CLASS_ONE
6	Close Block Ph C timeout status bit	TRUE	CLASS_ONE

The Value field can have the following attributes.

1. **TRUE.** This means that the point in the object will return 1 for active, true, yes or on and 0 for inactive, false, no or off.
2. **FALSE.** The point in the object will return 0 for active, true, yes or on and 1 for inactive, false, no or off.
3. **ONE.** The point in the object will always return 1.
4. **ZERO.** The point in the object will always return 0.

■ **NOTE:** The RANGE value column in the following tables indicates the return value of each point when the Value field is configured to **TRUE**.

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
0	–	2253	N/A	Input 2 Status	0 = inactive 1 = active	–
1	–	2253	N/A	Input 3 Status	0 = inactive 1 = active	–
2	–	2253	N/A	Input 4 Status	0 = inactive 1 = active	–
3	–	2253	N/A	Input 5 Status	0 = inactive 1 = active	–
4	–	2253	N/A	Input 6 Status	0 = inactive 1 = active	–
5	–	2253	N/A	Input 7 Status	0 = inactive 1 = active	–
6	–	2253	N/A	Input 8 Status	0 = inactive 1 = active	–
7	–	2253	N/A	Input 9 Status	0 = inactive 1 = active	–
8	–	2253	N/A	Input 10 Status	0 = inactive 1 = active	–
9	–	2253	N/A	Input 11 Status	0 = inactive 1 = active	–
10	–	2253	N/A	Input 12 Status	0 = inactive 1 = active	–
11	–	2253	N/A	Input FL Status	0 = inactive 1 = active	–
12	–	2254	N/A	Output 1 Status	0 = inactive 1 = active	–
13	–	2254	N/A	Output 2 Status	0 = inactive 1 = active	–
14	–	2254	N/A	Output 3 Status	0 = inactive 1 = active	–
15	–	2254	N/A	Output 4 Status	0 = inactive 1 = active	–
16	–	2254	N/A	Output 5 Status	0 = inactive 1 = active	–
17	–	2254	N/A	Output 6 Status	0 = inactive 1 = active	–
18	–	2254	N/A	Output 7 Status	0 = inactive 1 = active	–
19	–	2254	N/A	Output 8 Status	0 = inactive 1 = active	–
20	–	2254	N/A	Output 9 Status	0 = inactive 1 = active	–
21	–	2254	N/A	Output 10 Status	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
22	–	2254	N/A	Output 11 Status	0 = inactive 1 = active	–
23	–	2254	N/A	Output 12 Status	0 = inactive 1 = active	–
24	–	20	N/A	Profile 1 Active Status	0 = inactive 1 = active	–
25	–	20	N/A	Profile 2 Active Status	0 = inactive 1 = active	–
26	–	20	N/A	Profile 3 Active Status	0 = inactive 1 = active	–
27	–	20	N/A	Profile 4 Active Status	0 = inactive 1 = active	–
28	–	20	N/A	Profile 5 Active Status	0 = inactive 1 = active	–
29	–	20	N/A	Profile 6 Active Status	0 = inactive 1 = active	–
30	–	20	N/A	Profile 7 Active Status	0 = inactive 1 = active	–
31	–	20	N/A	Profile 8 Active Status	0 = inactive 1 = active	–
32	1	2508	N/A	Reclose State 0 for 3-Phase	0 = inactive 1 = active	–
33	1	2508	N/A	Reclose State 1 for 3-Phase	0 = inactive 1 = active	–
34	1	2508	N/A	Reclose State 2 for 3-Phase	0 = inactive 1 = active	–
35	1	2508	N/A	Reclose State 3 for 3-Phase	0 = inactive 1 = active	–
36	1	2508	N/A	Reclose State 4 for 3-Phase	0 = inactive 1 = active	–
37	1	2508	N/A	Reclose State 5 for 3-Phase	0 = inactive 1 = active	–
38	1	2508	N/A	Reclose State 6 for 3-Phase	0 = inactive 1 = active	–
39	1	2508	N/A	Reclose State 7 for 3-Phase	0 = inactive 1 = active	–
40	1	2508	N/A	Reclose State 8 for 3-Phase	0 = inactive 1 = active	–
41	1	2508	N/A	Reclose State 9 for 3-Phase	0 = inactive 1 = active	–
42	1	2508	N/A	Reclose State 10 for 3-Phase	0 = inactive 1 = active	–
43	1	2508	N/A	Control Lockout for 3-Phase	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
44	1	4552	N/A	Reclose State 0 for Phase-A	0 = inactive 1 = active	—
45	1	4552	N/A	Reclose State 1 for Phase-A	0 = inactive 1 = active	—
46	1	4552	N/A	Reclose State 2 for Phase-A	0 = inactive 1 = active	—
47	1	4552	N/A	Reclose State 3 for Phase-A	0 = inactive 1 = active	—
48	1	4552	N/A	Reclose State 4 for Phase-A	0 = inactive 1 = active	—
49	1	4552	N/A	Reclose State 5 for Phase-A	0 = inactive 1 = active	—
50	1	4552	N/A	Reclose State 6 for Phase-A	0 = inactive 1 = active	—
51	1	4552	N/A	Reclose State 7 for Phase-A	0 = inactive 1 = active	—
52	1	4552	N/A	Reclose State 8 for Phase-A	0 = inactive 1 = active	—
53	1	4552	N/A	Reclose State 9 for Phase-A	0 = inactive 1 = active	—
54	1	4552	N/A	Reclose State 10 for Phase-A	0 = inactive 1 = active	—
55	1	4552	N/A	Control Lockout for Phase-A	0 = inactive 1 = active	—
56	1	4567	N/A	Reclose State 0 for Phase-B	0 = inactive 1 = active	—
57	1	4567	N/A	Reclose State 1 for Phase-B	0 = inactive 1 = active	—
58	1	4567	N/A	Reclose State 2 for Phase-B	0 = inactive 1 = active	—
59	1	4567	N/A	Reclose State 3 for Phase-B	0 = inactive 1 = active	—
60	1	4567	N/A	Reclose State 4 for Phase-B	0 = inactive 1 = active	—
61	1	4567	N/A	Reclose State 5 for Phase-B	0 = inactive 1 = active	—
62	1	4567	N/A	Reclose State 6 for Phase-B	0 = inactive 1 = active	—
63	1	4567	N/A	Reclose State 7 for Phase-B	0 = inactive 1 = active	—
64	1	4567	N/A	Reclose State 8 for Phase-B	0 = inactive 1 = active	—
65	1	4567	N/A	Reclose State 9 for Phase-B	0 = inactive 1 = active	—

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
66	1	4567	N/A	Reclose State 10 for Phase-B	0 = inactive 1 = active	—
67	1	4567	N/A	Control Lockout for Phase-B	0 = inactive 1 = active	—
68	1	4582	N/A	Reclose State 0 for Phase-C	0 = inactive 1 = active	—
69	1	4582	N/A	Reclose State 1 for Phase-C	0 = inactive 1 = active	—
70	1	4582	N/A	Reclose State 2 for Phase-C	0 = inactive 1 = active	—
71	1	4582	N/A	Reclose State 3 for Phase-C	0 = inactive 1 = active	—
72	1	4582	N/A	Reclose State 4 for Phase-C	0 = inactive 1 = active	—
73	1	4582	N/A	Reclose State 5 for Phase-C	0 = inactive 1 = active	—
74	1	4582	N/A	Reclose State 6 for Phase-C	0 = inactive 1 = active	—
75	1	4582	N/A	Reclose State 7 for Phase-C	0 = inactive 1 = active	—
76	1	4582	N/A	Reclose State 8 for Phase-C	0 = inactive 1 = active	—
77	1	4582	N/A	Reclose State 9 for Phase-C	0 = inactive 1 = active	—
78	1	4582	N/A	Reclose State 10 for Phase-C	0 = inactive 1 = active	—
79	1	4582	N/A	Control Lockout for Phase-C	0 = inactive 1 = active	—
80	—	2255	N/A	F25 pickup status bit	0 = inactive 1 = active	—
81	—	2265	N/A	F25 timeout status bit	0 = inactive 1 = active	—
82	—	2255	N/A	F27#1 phase A pickup status bit	0 = inactive 1 = active	—
83	—	2265	N/A	F27#1 phase A timeout status bit	0 = inactive 1 = active	—
84	—	2255	N/A	F27#1 phase B pickup status bit	0 = inactive 1 = active	—
85	—	2265	N/A	F27#1 phase B timeout status bit	0 = inactive 1 = active	—
86	—	2255	N/A	F27#1 phase C pickup status bit	0 = inactive 1 = active	—
87	—	2265	N/A	F27#1 phase C timeout status bit	0 = inactive 1 = active	—

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
88	–	2255	N/A	F27#2 phase A pickup status bit	0 = inactive 1 = active	–
89	–	2265	N/A	F27#2 phase A timeout status bit	0 = inactive 1 = active	–
90	–	2255	N/A	F27#2 phase B pickup status bit	0 = inactive 1 = active	–
91	–	2265	N/A	F27#2 phase B timeout status bit	0 = inactive 1 = active	–
92	–	2255	N/A	F27#2 phase C pickup status bit	0 = inactive 1 = active	–
93	–	2265	N/A	F27#2 phase C timeout status bit	0 = inactive 1 = active	–
94	–	2255	N/A	F27#3 phase A pickup status bit	0 = inactive 1 = active	–
95	–	2265	N/A	F27#3 phase A timeout status bit	0 = inactive 1 = active	–
96	–	2255	N/A	F27#3 phase B pickup status bit	0 = inactive 1 = active	–
97	–	2265	N/A	F27#3 phase B timeout status bit	0 = inactive 1 = active	–
98	–	2255	N/A	F27#3 phase C pickup status bit	0 = inactive 1 = active	–
99	–	2265	N/A	F27#3 phase C timeout status bit	0 = inactive 1 = active	–
100	–	2265	N/A	F27#3 phase C timeout status bit	0 = inactive 1 = active	–
101	–	2255	N/A	F27#4 phase A pickup status bit	0 = inactive 1 = active	–
102	–	2265	N/A	F27#4 phase A timeout status bit	0 = inactive 1 = active	–
103	–	2255	N/A	F27#4 phase B pickup status bit	0 = inactive 1 = active	–
104	–	2265	N/A	F27#4 phase B timeout status bit	0 = inactive 1 = active	–
105	–	2255	N/A	F27#4 phase C pickup status bit	0 = inactive 1 = active	–
106	–	2265	N/A	F27#4 phase C timeout status bit	0 = inactive 1 = active	–
107	–	2255	N/A	F27PP phases AB pickup status bit	0 = inactive 1 = active	–
108	–	2265	N/A	F27PP phases AB timeout status bit	0 = inactive 1 = active	–
109	–	2255	N/A	F27PP phases BC pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
110	–	2265	N/A	F27PP phases BC timeout status bit	0 = inactive 1 = active	–
111	–	2255	N/A	F27PP phases CA pickup status bit	0 = inactive 1 = active	–
112	–	2265	N/A	F27PP phases CA timeout status bit	0 = inactive 1 = active	–
113	–	2256	N/A	F27Vz1 pickup status bit	0 = inactive 1 = active	–
114	–	2266	N/A	F27Vz1 timeout status bit	0 = inactive 1 = active	–
115	–	2256	N/A	F32#1 phase A pickup status bit	0 = inactive 1 = active	–
116	–	2266	N/A	F32#1 phase A timeout status bit	0 = inactive 1 = active	–
117	–	2256	N/A	F32#1 phase B pickup status bit	0 = inactive 1 = active	–
118	–	2266	N/A	F32#1 phase B timeout status bit	0 = inactive 1 = active	–
119	–	2256	N/A	F32#1 phase C pickup status bit	0 = inactive 1 = active	–
120	–	2266	N/A	F32#1 phase C timeout status bit	0 = inactive 1 = active	–
121	–	2256	N/A	F32#2 phase A pickup status bit	0 = inactive 1 = active	–
122	–	2266	N/A	F32#2 phase A timeout status bit	0 = inactive 1 = active	–
123	–	2256	N/A	F32#2 phase B pickup status bit	0 = inactive 1 = active	–
124	–	2266	N/A	F32#2 phase B timeout status bit	0 = inactive 1 = active	–
125	–	2256	N/A	F32#2 phase C pickup status bit	0 = inactive 1 = active	–
126	–	2266	N/A	F32#2 phase C timeout status bit	0 = inactive 1 = active	–
127	–	2256	N/A	F32#3 phase A pickup status bit	0 = inactive 1 = active	–
128	–	2266	N/A	F32#3 phase A timeout status bit	0 = inactive 1 = active	–
129	–	2256	N/A	F32#3 phase B pickup status bit	0 = inactive 1 = active	–
130	–	2266	N/A	F32#3 phase B timeout status bit	0 = inactive 1 = active	–
131	–	2256	N/A	F32#3 phase C pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
132	–	2266	N/A	F32#3 phase C timeout status bit	0 = inactive 1 = active	–
133	–	2256	N/A	F32#4 phase A pickup status bit	0 = inactive 1 = active	–
134	–	2266	N/A	F32#4 phase A timeout status bit	0 = inactive 1 = active	–
135	–	2256	N/A	F32#4 phase B pickup status bit	0 = inactive 1 = active	–
136	–	2266	N/A	F32#4 phase B timeout status bit	0 = inactive 1 = active	–
137	–	2256	N/A	F32#4 phase C pickup status bit	0 = inactive 1 = active	–
138	–	2266	N/A	F32#4 phase C timeout status bit	0 = inactive 1 = active	–
139	–	2256	N/A	F50P#1 phase A pickup status bit	0 = inactive 1 = active	–
140	–	2266	N/A	F50P#1 phase A timeout status bit	0 = inactive 1 = active	–
141	–	2256	N/A	F50P#1 phase B pickup status bit	0 = inactive 1 = active	–
142	–	2266	N/A	F50P#1 phase B timeout status bit	0 = inactive 1 = active	–
143	–	2256	N/A	F50P#1 phase C pickup status bit	0 = inactive 1 = active	–
144	–	2266	N/A	F50P#1 phase C timeout status bit	0 = inactive 1 = active	–
145	–	2257	N/A	F50P#2 phase A pickup status bit	0 = inactive 1 = active	–
146	–	2267	N/A	F50P#2 phase A timeout status bit	0 = inactive 1 = active	–
147	–	2257	N/A	F50P#2 phase B pickup status bit	0 = inactive 1 = active	–
148	–	2267	N/A	F50P#2 phase B timeout status bit	0 = inactive 1 = active	–
149	–	2257	N/A	F50P#2 phase C pickup status bit	0 = inactive 1 = active	–
150	–	2267	N/A	F50P#2 phase C timeout status bit	0 = inactive 1 = active	–
151	–	2257	N/A	F50P#3 phase A pickup status bit	0 = inactive 1 = active	–
152	–	2267	N/A	F50P#3 phase A timeout status bit	0 = inactive 1 = active	–
153	–	2257	N/A	F50P#3 phase B pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
154	–	2267	N/A	F50P#3 phase B timeout status bit	0 = inactive 1 = active	–
155	–	2257	N/A	F50P#3 phase C pickup status bit	0 = inactive 1 = active	–
156	–	2267	N/A	F50P#3 phase C timeout status bit	0 = inactive 1 = active	–
157	–	2257	N/A	F50P#4 phase A pickup status bit	0 = inactive 1 = active	–
158	–	2267	N/A	F50P#4 phase A timeout status bit	0 = inactive 1 = active	–
159	–	2257	N/A	F50P#4 phase B pickup status bit	0 = inactive 1 = active	–
160	–	2267	N/A	F50P#4 phase B timeout status bit	0 = inactive 1 = active	–
161	–	2257	N/A	F50P#4 phase C pickup status bit	0 = inactive 1 = active	–
162	–	2267	N/A	F50P#4 phase C timeout status bit	0 = inactive 1 = active	–
163	–	2257	N/A	F50P#5 phase A pickup status bit	0 = inactive 1 = active	–
164	–	2267	N/A	F50P#5 phase A timeout status bit	0 = inactive 1 = active	–
165	–	2257	N/A	F50P#5 phase B pickup status bit	0 = inactive 1 = active	–
166	–	2267	N/A	F50P#5 phase B timeout status bit	0 = inactive 1 = active	–
167	–	2257	N/A	F50P#5 phase C pickup status bit	0 = inactive 1 = active	–
168	–	2267	N/A	F50P#5 phase C timeout status bit	0 = inactive 1 = active	–
169	–	2257	N/A	F50N#1 pickup status bit	0 = inactive 1 = active	–
170	–	2267	N/A	F50N#1 timeout status bit	0 = inactive 1 = active	–
171	–	2257	N/A	F50N#2 pickup status bit	0 = inactive 1 = active	–
172	–	2267	N/A	F50N#2 timeout status bit	0 = inactive 1 = active	–
173	–	2257	N/A	F50N#3 pickup status bit	0 = inactive 1 = active	–
174	–	2267	N/A	F50N#3 timeout status bit	0 = inactive 1 = active	–
175	–	2257	N/A	F50N#4 pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
176	–	2267	N/A	F50N#4 timeout status bit	0 = inactive 1 = active	–
177	–	2258	N/A	F50N#5 pickup status bit	0 = inactive 1 = active	–
178	–	2268	N/A	F50N#5 timeout status bit	0 = inactive 1 = active	–
179	–	2258	N/A	F50G#1 pickup status bit	0 = inactive 1 = active	–
180	–	2268	N/A	F50G#1 timeout status bit	0 = inactive 1 = active	–
181	–	2258	N/A	F50G#2 pickup status bit	0 = inactive 1 = active	–
182	–	2268	N/A	F50G#2 timeout status bit	0 = inactive 1 = active	–
183	–	2258	N/A	F50G#3 pickup status bit	0 = inactive 1 = active	–
184	–	2268	N/A	F50G#3 timeout status bit	0 = inactive 1 = active	–
185	–	2258	N/A	F50G#4 pickup status bit	0 = inactive 1 = active	–
186	–	2268	N/A	F50G#4 timeout status bit	0 = inactive 1 = active	–
187	–	2258	N/A	F50G#5 pickup status bit	0 = inactive 1 = active	–
188	–	2268	N/A	F50G#5 timeout status bit	0 = inactive 1 = active	–
189	–	2258	N/A	F46DT#1 pickup status bit	0 = inactive 1 = active	–
190	–	2268	N/A	F46DT#1 timeout status bit	0 = inactive 1 = active	–
191	–	2258	N/A	F46DT#2 pickup status bit	0 = inactive 1 = active	–
192	–	2268	N/A	F46DT#2 timeout status bit	0 = inactive 1 = active	–
193	–	2258	N/A	F46DT#3 pickup status bit	0 = inactive 1 = active	–
194	–	2268	N/A	F46DT#3 timeout status bit	0 = inactive 1 = active	–
195	–	2258	N/A	F46DT#4 pickup status bit	0 = inactive 1 = active	–
196	–	2268	N/A	F46DT#3 timeout status bit	0 = inactive 1 = active	–
197	–	2258	N/A	F46DT#5 pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
198	–	2268	N/A	F46DT#5 timeout status bit	0 = inactive 1 = active	–
199	–	2258	N/A	F50BF pickup status bit	0 = inactive 1 = active	–
200	–	2268	N/A	F50BF timeout status bit	0 = inactive 1 = active	–
201	–	2258	N/A	F51P#1 phase A pickup status bit	0 = inactive 1 = active	–
202	–	2268	N/A	F51P#1 phase A timeout status bit	0 = inactive 1 = active	–
203	–	2258	N/A	F51P#1 phase B pickup status bit	0 = inactive 1 = active	–
204	–	2268	N/A	F51P#1 phase B timeout status bit	0 = inactive 1 = active	–
205	–	2258	N/A	F51P#1 phase C pickup status bit	0 = inactive 1 = active	–
206	–	2268	N/A	F51P#1 phase C timeout status bit	0 = inactive 1 = active	–
207	–	2258	N/A	F51P#2 phase A pickup status bit	0 = inactive 1 = active	–
208	–	2268	N/A	F51P#2 phase A timeout status bit	0 = inactive 1 = active	–
209	–	2259	N/A	F51P#2 phase B pickup status bit	0 = inactive 1 = active	–
210	–	2269	N/A	F51P#2 phase B timeout status bit	0 = inactive 1 = active	–
211	–	2259	N/A	F51P#2 phase C pickup status bit	0 = inactive 1 = active	–
212	–	2269	N/A	F51P#2 phase C timeout status bit	0 = inactive 1 = active	–
213	–	2259	N/A	F51P#3 phase A pickup status bit	0 = inactive 1 = active	–
214	–	2269	N/A	F51P#3 phase A timeout status bit	0 = inactive 1 = active	–
215	–	2259	N/A	F51P#3 phase B pickup status bit	0 = inactive 1 = active	–
216	–	2269	N/A	F51P#3 phase B timeout status bit	0 = inactive 1 = active	–
217	–	2259	N/A	F51P#3 phase C pickup status bit	0 = inactive 1 = active	–
218	–	2269	N/A	F51P#3 phase C timeout status bit	0 = inactive 1 = active	–
219	–	2259	N/A	F51P#4 phase A pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
220	–	2269	N/A	F51P#4 phase A timeout status bit	0 = inactive 1 = active	–
221	–	2259	N/A	F51P#4 phase B pickup status bit	0 = inactive 1 = active	–
222	–	2269	N/A	F51P#4 phase B timeout status bit	0 = inactive 1 = active	–
223	–	2259	N/A	F51P#4 phase C pickup status bit	0 = inactive 1 = active	–
224	–	2269	N/A	F51P#4 phase C timeout status bit	0 = inactive 1 = active	–
225	–	2259	N/A	F51P#5 phase A pickup status bit	0 = inactive 1 = active	–
226	–	2269	N/A	F51P#5 phase A timeout status bit	0 = inactive 1 = active	–
227	–	2259	N/A	F51P#5 phase B pickup status bit	0 = inactive 1 = active	–
228	–	2269	N/A	F51P#5 phase B timeout status bit	0 = inactive 1 = active	–
229	–	2259	N/A	F51P#5 phase C pickup status bit	0 = inactive 1 = active	–
230	–	2269	N/A	F51P#5 phase C timeout status bit	0 = inactive 1 = active	–
231	–	2259	N/A	F51N#1 pickup status bit	0 = inactive 1 = active	–
232	–	2269	N/A	F51N#1 timeout status bit	0 = inactive 1 = active	–
233	–	2259	N/A	F51N#2 pickup status bit	0 = inactive 1 = active	–
234	–	2269	N/A	F51N#2 timeout status bit	0 = inactive 1 = active	–
235	–	2259	N/A	F51N#3 pickup status bit	0 = inactive 1 = active	–
236	–	2269	N/A	F51N#3 timeout status bit	0 = inactive 1 = active	–
237	–	2259	N/A	F51N#4 pickup status bit	0 = inactive 1 = active	–
238	–	2269	N/A	F51N#4 timeout status bit	0 = inactive 1 = active	–
239	–	2259	N/A	F51N#5 pickup status bit	0 = inactive 1 = active	–
240	–	2269	N/A	F51N#5 timeout status bit	0 = inactive 1 = active	–
241	–	2260	N/A	F51G#1 pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
242	–	2270	N/A	F51G#1 timeout status bit	0 = inactive 1 = active	–
243	–	2260	N/A	F51G#2 pickup status bit	0 = inactive 1 = active	–
244	–	2270	N/A	F51G#2 timeout status bit	0 = inactive 1 = active	–
245	–	2260	N/A	F51G#3 pickup status bit	0 = inactive 1 = active	–
246	–	2270	N/A	F51G#3 timeout status bit	0 = inactive 1 = active	–
247	–	2260	N/A	F51G#4 pickup status bit	0 = inactive 1 = active	–
248	–	2270	N/A	F51G#4 timeout status bit	0 = inactive 1 = active	–
249	–	2260	N/A	F51G#5 pickup status bit	0 = inactive 1 = active	–
250	–	2270	N/A	F51G#5 timeout status bit	0 = inactive 1 = active	–
251	–	2260	N/A	F46IT#1 pickup status bit	0 = inactive 1 = active	–
252	–	2270	N/A	F46IT#1 timeout status bit	0 = inactive 1 = active	–
253	–	2260	N/A	F46IT#2 pickup status bit	0 = inactive 1 = active	–
254	–	2270	N/A	F46IT#2 timeout status bit	0 = inactive 1 = active	–
255	–	2260	N/A	F46IT#3 pickup status bit	0 = inactive 1 = active	–
256	–	2270	N/A	F46IT#3 timeout status bit	0 = inactive 1 = active	–
257	–	2260	N/A	F46IT#4 pickup status bit	0 = inactive 1 = active	–
258	–	2270	N/A	F46IT#4 timeout status bit	0 = inactive 1 = active	–
259	–	2260	N/A	F46IT#5 pickup status bit	0 = inactive 1 = active	–
260	–	2270	N/A	F46IT#5 timeout status bit	0 = inactive 1 = active	–
261	–	2260	N/A	F59#1 phase A pickup status bit	0 = inactive 1 = active	–
262	–	2270	N/A	F59#1 phase A timeout status bit	0 = inactive 1 = active	–
263	–	2260	N/A	F59#1 phase B pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
264	–	2270	N/A	F59#1 phase B timeout status bit	0 = inactive 1 = active	–
265	–	2260	N/A	F59#1 phase C pickup status bit	0 = inactive 1 = active	–
266	–	2270	N/A	F59#1 phase C timeout status bit	0 = inactive 1 = active	–
267	–	2260	N/A	F59#2 phase A pickup status bit	0 = inactive 1 = active	–
268	–	2270	N/A	F59#2 phase A timeout status bit	0 = inactive 1 = active	–
269	–	2260	N/A	F59#2 phase B pickup status bit	0 = inactive 1 = active	–
270	–	2270	N/A	F59#2 phase B timeout status bit	0 = inactive 1 = active	–
271	–	2260	N/A	F59#2 phase C pickup status bit	0 = inactive 1 = active	–
272	–	2270	N/A	F59#2 phase C timeout status bit	0 = inactive 1 = active	–
273	–	2261	N/A	F59#3 phase A pickup status bit	0 = inactive 1 = active	–
274	–	2271	N/A	F59#3 phase A timeout status bit	0 = inactive 1 = active	–
275	–	2261	N/A	F59#3 phase B pickup status bit	0 = inactive 1 = active	–
276	–	2271	N/A	F59#3 phase B timeout status bit	0 = inactive 1 = active	–
277	–	2261	N/A	F59#3 phase C pickup status bit	0 = inactive 1 = active	–
278	–	2271	N/A	F59#3 phase C timeout status bit	0 = inactive 1 = active	–
279	–	2261	N/A	F59#4 phase A pickup status bit	0 = inactive 1 = active	–
280	–	2271	N/A	F59#4 phase A timeout status bit	0 = inactive 1 = active	–
281	–	2261	N/A	F59#4 phase B pickup status bit	0 = inactive 1 = active	–
282	–	2271	N/A	F59#4 phase B timeout status bit	0 = inactive 1 = active	–
283	–	2261	N/A	F59#4 phase C pickup status bit	0 = inactive 1 = active	–
284	–	2271	N/A	F59#4 phase C timeout status bit	0 = inactive 1 = active	–
285	–	2261	N/A	F59PP phase AB pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
286	–	2271	N/A	F59PP phase AB timeout status bit	0 = inactive 1 = active	–
287	–	2261	N/A	F59PP phase BC pickup status bit	0 = inactive 1 = active	–
288	–	2271	N/A	F59PP phase BC timeout status bit	0 = inactive 1 = active	–
289	–	2261	N/A	F59PP phase CA pickup status bit	0 = inactive 1 = active	–
290	–	2271	N/A	F59PP phase CA timeout status bit	0 = inactive 1 = active	–
291	–	2261	N/A	F59N#1 pickup status bit	0 = inactive 1 = active	–
292	–	2271	N/A	F59N#1 timeout status bit	0 = inactive 1 = active	–
293	–	2085	N/A	F59N#2 pickup status bit	0 = inactive 1 = active	–
294	–	2086	N/A	F59N#2 timeout status bit	0 = inactive 1 = active	–
295	–	2261	N/A	F59Vz1 pickup status bit	0 = inactive 1 = active	–
296	–	2271	N/A	F59Vz1 timeout status bit	0 = inactive 1 = active	–
297	–	2261	N/A	F47 pickup status bit	0 = inactive 1 = active	–
298	–	2271	N/A	F47 timeout status bit	0 = inactive 1 = active	–
299	–	2261	N/A	F67#1 phase A pickup status bit	0 = inactive 1 = active	–
300	–	2271	N/A	F67#1 phase A timeout status bit	0 = inactive 1 = active	–
301	–	2261	N/A	F67#1 phase B pickup status bit	0 = inactive 1 = active	–
302	–	2271	N/A	F67#1 phase B timeout status bit	0 = inactive 1 = active	–
303	–	2261	N/A	F67#1 phase C pickup status bit	0 = inactive 1 = active	–
304	–	2271	N/A	F67#1 phase C timeout status bit	0 = inactive 1 = active	–
305	–	2261	N/A	F67#2 phase A pickup status bit	0 = inactive 1 = active	–
306	–	2271	N/A	F67#2 phase A timeout status bit	0 = inactive 1 = active	–
307	–	2262	N/A	F67#2 phase B pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
308	–	2272	N/A	F67#2 phase B timeout status bit	0 = inactive 1 = active	–
309	–	2262	N/A	F67#2 phase C pickup status bit	0 = inactive 1 = active	–
310	–	2272	N/A	F67#2 phase C timeout status bit	0 = inactive 1 = active	–
311	–	2262	N/A	F67#3 phase A pickup status bit	0 = inactive 1 = active	–
312	–	2272	N/A	F67#3 phase A timeout status bit	0 = inactive 1 = active	–
313	–	2262	N/A	F67#3 phase B pickup status bit	0 = inactive 1 = active	–
314	–	2272	N/A	F67#3 phase B timeout status bit	0 = inactive 1 = active	–
315	–	2262	N/A	F67#3 phase C pickup status bit	0 = inactive 1 = active	–
316	–	2272	N/A	F67#3 phase C timeout status bit	0 = inactive 1 = active	–
317	–	2262	N/A	F67#4 phase A pickup status bit	0 = inactive 1 = active	–
318	–	2272	N/A	F67#4 phase A timeout status bit	0 = inactive 1 = active	–
319	–	2262	N/A	F67#4 phase B pickup status bit	0 = inactive 1 = active	–
320	–	2272	N/A	F67#4 phase B timeout status bit	0 = inactive 1 = active	–
321	–	2262	N/A	F67#4 phase C pickup status bit	0 = inactive 1 = active	–
322	–	2272	N/A	F67#4 phase C timeout status bit	0 = inactive 1 = active	–
323	–	2262	N/A	F67#5 phase A pickup status bit	0 = inactive 1 = active	–
324	–	2272	N/A	F67#5 phase A timeout status bit	0 = inactive 1 = active	–
325	–	2262	N/A	F67#5 phase B pickup status bit	0 = inactive 1 = active	–
326	–	2272	N/A	F67#5 phase B timeout status bit	0 = inactive 1 = active	–
327	–	2262	N/A	F67#5 phase C pickup status bit	0 = inactive 1 = active	–
328	–	2272	N/A	F67#5 phase C timeout status bit	0 = inactive 1 = active	–
329	–	2262	N/A	F67N#1 pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
330	–	2272	N/A	F67N#1 timeout status bit	0 = inactive 1 = active	–
331	–	2262	N/A	F67N#2 pickup status bit	0 = inactive 1 = active	–
332	–	2272	N/A	F67N#2 timeout status bit	0 = inactive 1 = active	–
333	–	2262	N/A	F67N#3 pickup status bit	0 = inactive 1 = active	–
334	–	2272	N/A	F67N#3 timeout status bit	0 = inactive 1 = active	–
335	–	2262	N/A	F67N#4 pickup status bit	0 = inactive 1 = active	–
336	–	2272	N/A	F67N#4 timeout status bit	0 = inactive 1 = active	–
337	–	2262	N/A	F67N#5 pickup status bit	0 = inactive 1 = active	–
338	–	2272	N/A	F67N#5 timeout status bit	0 = inactive 1 = active	–
339	–	2263	N/A	F67G#1 pickup status bit	0 = inactive 1 = active	–
340	–	2273	N/A	F67G#1 timeout status bit	0 = inactive 1 = active	–
341	–	2263	N/A	F67G#2 pickup status bit	0 = inactive 1 = active	–
342	–	2273	N/A	F67G#2 timeout status bit	0 = inactive 1 = active	–
343	–	2263	N/A	F67G#3 pickup status bit	0 = inactive 1 = active	–
344	–	2273	N/A	F67G#3 timeout status bit	0 = inactive 1 = active	–
345	–	2263	N/A	F67G#4 pickup status bit	0 = inactive 1 = active	–
346	–	2273	N/A	F67G#4 timeout status bit	0 = inactive 1 = active	–
347	–	2263	N/A	F67G#5 pickup status bit	0 = inactive 1 = active	–
348	–	2273	N/A	F67G#5 timeout status bit	0 = inactive 1 = active	–
349	–	2263	N/A	F67Q#1 pickup status bit	0 = inactive 1 = active	–
350	–	2273	N/A	F67Q#1 timeout status bit	0 = inactive 1 = active	–
351	–	2263	N/A	F67Q#2 pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
352	–	2273	N/A	F67Q#2 timeout status bit	0 = inactive 1 = active	–
353	–	2263	N/A	F67Q#3 pickup status bit	0 = inactive 1 = active	–
354	–	2273	N/A	F67Q#3 timeout status bit	0 = inactive 1 = active	–
355	–	2263	N/A	F67Q#4 pickup status bit	0 = inactive 1 = active	–
356	–	2273	N/A	F67Q#4 timeout status bit	0 = inactive 1 = active	–
357	–	2263	N/A	F67Q#5 pickup status bit	0 = inactive 1 = active	–
358	–	2273	N/A	F67Q#5 timeout status bit	0 = inactive 1 = active	–
359	–	2263	N/A	TCM pickup status bit	0 = inactive 1 = active	–
360	–	2273	N/A	TCM timeout status bit	0 = inactive 1 = active	–
361	–	2263	N/A	CCM pickup status bit	0 = inactive 1 = active	–
362	–	2273	N/A	CCM timeout status bit	0 = inactive 1 = active	–
363	–	2263	N/A	F81#1 pickup status bit	0 = inactive 1 = active	–
364	–	2273	N/A	F81#1 timeout status bit	0 = inactive 1 = active	–
365	–	2263	N/A	F81#2 pickup status bit	0 = inactive 1 = active	–
366	–	2273	N/A	F81#2 timeout status bit	0 = inactive 1 = active	–
367	–	2263	N/A	F81#3 pickup status bit	0 = inactive 1 = active	–
368	–	2273	N/A	F81#3 timeout status bit	0 = inactive 1 = active	–
369	–	2264	N/A	F81#4 pickup status bit	0 = inactive 1 = active	–
370	–	2274	N/A	F81#4 timeout status bit	0 = inactive 1 = active	–
371	–	2264	N/A	F81R#1 pickup status bit	0 = inactive 1 = active	–
372	–	2274	N/A	F81R#1 timeout status bit	0 = inactive 1 = active	–
373	–	2264	N/A	F81R#2 pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
374	–	2274	N/A	F81R#2 timeout status bit	0 = inactive 1 = active	–
375	–	2264	N/A	IPSLOGIC#1 pickup status bit	0 = inactive 1 = active	–
376	–	2274	N/A	IPSLOGIC#1 timeout status bit	0 = inactive 1 = active	–
377	–	2264	N/A	IPSLOGIC#2 pickup status bit	0 = inactive 1 = active	–
378	–	2274	N/A	IPSLOGIC#2 timeout status bit	0 = inactive 1 = active	–
379	–	2264	N/A	IPSLOGIC#3 pickup status bit	0 = inactive 1 = active	–
380	–	2274	N/A	IPSLOGIC#3 timeout status bit	0 = inactive 1 = active	–
381	–	2264	N/A	IPSLOGIC#4 pickup status bit	0 = inactive 1 = active	–
382	–	2274	N/A	IPSLOGIC#4 timeout status bit	0 = inactive 1 = active	–
383	–	2264	N/A	IPSLOGIC#5 pickup status bit	0 = inactive 1 = active	–
384	–	2274	N/A	IPSLOGIC#5 timeout status bit	0 = inactive 1 = active	–
385	–	2264	N/A	IPSLOGIC#6 pickup status bit	0 = inactive 1 = active	–
386	–	2274	N/A	IPSLOGIC#6 timeout status bit	0 = inactive 1 = active	–
387	–	2264	N/A	IPSLOGIC#7 pickup status bit	0 = inactive 1 = active	–
388	–	2274	N/A	IPSLOGIC#7 timeout status bit	0 = inactive 1 = active	–
389	–	2264	N/A	IPSLOGIC#8 pickup status bit	0 = inactive 1 = active	–
390	–	2274	N/A	IPSLOGIC#8 timeout status bit	0 = inactive 1 = active	–
391	–	2264	N/A	PSM pickup status bit	0 = inactive 1 = active	–
392	–	2274	N/A	PSM timeout status bit	0 = inactive 1 = active	–
393	–	2264	N/A	F60FL pickup status bit	0 = inactive 1 = active	–
394	–	2274	N/A	F60FL timeout status bit	0 = inactive 1 = active	–
395	–	2264	N/A	FTHD/TDD #1 phase A pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
396	–	2274	N/A	FTHD/TDD #1 phase A timeout status bit	0 = inactive 1 = active	–
397	–	1935	N/A	FTHD/TDD #1 phase B pickup status bit	0 = inactive 1 = active	–
398	–	1936	N/A	FTHD/TDD #1 phase B timeout status bit	0 = inactive 1 = active	–
399	–	1935	N/A	FTHD/TDD #1 phase C pickup status bit	0 = inactive 1 = active	–
400	–	1936	N/A	FTHD/TDD #1 phase C timeout status bit	0 = inactive 1 = active	–
401	–	1935	N/A	FTHD/TDD #2 phase A pickup status bit	0 = inactive 1 = active	–
402	–	1936	N/A	FTHD/TDD #2 phase A timeout status bit	0 = inactive 1 = active	–
403	–	1935	N/A	FTHD/TDD #2 phase B pickup status bit	0 = inactive 1 = active	–
404	–	1936	N/A	FTHD/TDD #2 phase B timeout status bit	0 = inactive 1 = active	–
405	–	1935	N/A	FTHD/TDD #2 phase C pickup status bit	0 = inactive 1 = active	–
406	–	1936	N/A	FTHD/TDD #2 phase C timeout status bit	0 = inactive 1 = active	–
407	–	1935	N/A	F59I phase A pickup status bit	0 = inactive 1 = active	–
408	–	1936	N/A	F59I phase A timeout status bit	0 = inactive 1 = active	–
409	–	1935	N/A	F59I phase B pickup status bit	0 = inactive 1 = active	–
410	–	1936	N/A	F59I phase B timeout status bit	0 = inactive 1 = active	–
411	–	1935	N/A	F59I phase C pickup status bit	0 = inactive 1 = active	–
412	–	1936	N/A	F59I phase C timeout status bit	0 = inactive 1 = active	–
413	–	1935	N/A	F40 #1 pickup status bit	0 = inactive 1 = active	–
414	–	1936	N/A	F40 #1 timeout status bit	0 = inactive 1 = active	–
415	–	1935	N/A	F40 #2 pickup status bit	0 = inactive 1 = active	–
416	–	1936	N/A	F40 #2 timeout status bit	0 = inactive 1 = active	–
417	–	1935	N/A	F40VC #1 pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
418	–	1936	N/A	F40VC #1 timeout status bit	0 = inactive 1 = active	–
419	–	1935	N/A	F40VC #2 pickup status bit	0 = inactive 1 = active	–
420	–	1936	N/A	F40VC #2 timeout status bit	0 = inactive 1 = active	–
421	–	1935	N/A	FBM #1 pickup status bit	0 = inactive 1 = active	–
422	–	1936	N/A	FBM #1 timeout status bit	0 = inactive 1 = active	–
423	–	1996	N/A	HLT F50P phase A pickup status bit	0 = inactive 1 = active	–
424	–	1997	N/A	HLT F50P phase A timeout status bit	0 = inactive 1 = active	–
425	–	1996	N/A	HLT F50P phase B pickup status bit	0 = inactive 1 = active	–
426	–	1997	N/A	HLT F50P phase B timeout status bit	0 = inactive 1 = active	–
427	–	1996	N/A	HLT F50P phase C pickup status bit	0 = inactive 1 = active	–
428	–	1997	N/A	HLT F50P phase C timeout status bit	0 = inactive 1 = active	–
429	–	1996	N/A	HLT F50 Ground/Ground Sensitive pickup status bit	0 = inactive 1 = active	–
430	–	1997	N/A	HLT F50 Ground/Ground Sensitive timeout status bit	0 = inactive 1 = active	–
431	–	1996	N/A	HLT F51P phase A pickup status bit	0 = inactive 1 = active	–
432	–	1997	N/A	HLT F51P phase A timeout status bit	0 = inactive 1 = active	–
433	–	1996	N/A	HLT F51P phase B pickup status bit	0 = inactive 1 = active	–
434	–	1997	N/A	HLT F51P phase B timeout status bit	0 = inactive 1 = active	–
435	–	1996	N/A	HLT F51P phase C pickup status bit	0 = inactive 1 = active	–
436	–	1997	N/A	HLT F51P phase C timeout status bit	0 = inactive 1 = active	–
437	–	1996	N/A	HLT F51 Ground/Ground Sensitive pickup status bit	0 = inactive 1 = active	–
438	–	1997	N/A	HLT F51 Ground/Ground Sensitive timeout status bit	0 = inactive 1 = active	–
439	–	1996	N/A	Sectionalizer pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
440	–	1997	N/A	Sectionalizer timeout status bit	0 = inactive 1 = active	–
441	–	1996	N/A	Close Block Ph A pickup status bit	0 = inactive 1 = active	–
442	–	1997	N/A	Close Block Ph A timeout status bit	0 = inactive 1 = active	–
443	–	2085	N/A	Close Block Ph B pickup status bit	0 = inactive 1 = active	–
444	–	2086	N/A	Close Block Ph B timeout status bit	0 = inactive 1 = active	–
445	–	2085	N/A	Close Block Ph C pickup status bit	0 = inactive 1 = active	–
446	–	2086	N/A	Close Block Ph C timeout status bit	0 = inactive 1 = active	–
447	–	2085	N/A	Loop Scheme pickup status bit	0 = inactive 1 = active	–
448	–	2086	N/A	Loop Scheme timeout status bit	0 = inactive 1 = active	–
449	–	2085	N/A	F27#5 3 phases pickup status bit	0 = inactive 1 = active	–
450	–	2086	N/A	F27#5 3 phases timeout status bit	0 = inactive 1 = active	–
451	–	2085	N/A	F27#6 3 phases pickup status bit	0 = inactive 1 = active	–
452	–	2086	N/A	F27#6 3 phases timeout status bit	0 = inactive 1 = active	–
453	–	4702	N/A	F27#7 3 phases pickup status bit	0 = inactive 1 = active	–
454	–	4703	N/A	F27#7 3 phases timeout status bit	0 = inactive 1 = active	–
455	–	4702	N/A	F27#8 3 phases pickup status bit	0 = inactive 1 = active	–
456	–	4703	N/A	F27#8 3 phases timeout status bit	0 = inactive 1 = active	–
457	–	46373	N/A	F59#5 3 phases pickup status bit	0 = inactive 1 = active	–
458	–	46374	N/A	F59#5 3 phases timeout status bit	0 = inactive 1 = active	–
459	–	46378	N/A	F59#6 3 phases pickup status bit	0 = inactive 1 = active	–
460	–	46379	N/A	F59#6 3 phases timeout status bit	0 = inactive 1 = active	–
461	–	46383	N/A	F59#7 3 phases pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
462	–	46384	N/A	F59#7 3 phases timeout status bit	0 = inactive 1 = active	–
463	–	46388	N/A	F59#8 3 phases pickup status bit	0 = inactive 1 = active	–
464	–	46389	N/A	F59#8 3 phases timeout status bit	0 = inactive 1 = active	–
465	–	4702	N/A	HLT F50 Neutral pickup status bit	0 = inactive 1 = active	–
466	–	4703	N/A	HLT F50 Neutral timeout status bit	0 = inactive 1 = active	–
467	–	4702	N/A	HLT F51 Neutral pickup status bit	0 = inactive 1 = active	–
468	–	4703	N/A	HLT F51 Neutral timeout status bit	0 = inactive 1 = active	–
469	–	4804	N/A	F81#5 pickup status bit	0 = inactive 1 = active	–
470	–	4806	N/A	F81#5 timeout status bit	0 = inactive 1 = active	–
471	–	4805	N/A	F81#6 pickup status bit	0 = inactive 1 = active	–
472	–	4807	N/A	F81#6 timeout status bit	0 = inactive 1 = active	–
473	–	4703	N/A	HCL A pickup status bit	0 = inactive 1 = active	–
474	–	4703	N/A	HCL A pickup timeout bit	0 = inactive 1 = active	–
475	–	4703	N/A	HCL B pickup status bit	0 = inactive 1 = active	–
476	–	4703	N/A	HCL B pickup timeout bit	0 = inactive 1 = active	–
477	–	4703	N/A	HCL C pickup status bit	0 = inactive 1 = active	–
478	–	4703	N/A	HCL C pickup timeout bit	0 = inactive 1 = active	–
479	–	4805	N/A	HCL G pickup status bit	0 = inactive 1 = active	–
480	–	4807	N/A	HCL G timeout status bit	0 = inactive 1 = active	–
481	–	4805	N/A	F81#7 pickup status bit	0 = inactive 1 = active	–
482	–	4807	N/A	F81#7 timeout status bit	0 = inactive 1 = active	–
483	–	4805	N/A	F81#8 pickup status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
484	–	4807	N/A	F81#8 timeout status bit	0 = inactive 1 = active	–
485	–	4805	N/A	F81R#3 pickup status bit	0 = inactive 1 = active	–
486	–	4807	N/A	F81R#3 timeout status bit	0 = inactive 1 = active	–
487	–	4847	N/A	F81R#4 pickup status bit	0 = inactive 1 = active	–
488	–	4849	N/A	F81R#4 timeout status bit	0 = inactive 1 = active	–
489	–	4847	N/A	F81R#5 pickup status bit	0 = inactive 1 = active	–
490	–	4849	N/A	F81R#5 timeout status bit	0 = inactive 1 = active	–
491	–	4847	N/A	F81R#6 pickup status bit	0 = inactive 1 = active	–
492	–	4849	N/A	F81R#6 timeout status bit	0 = inactive 1 = active	–
493	–	4847	N/A	F81R#7 pickup status bit	0 = inactive 1 = active	–
494	–	4849	N/A	F81R#7 timeout status bit	0 = inactive 1 = active	–
495	–	4847	N/A	F81R#8 pickup status bit	0 = inactive 1 = active	–
496	–	4849	N/A	F81R#8 timeout status bit	0 = inactive 1 = active	–
497	–	4847	N/A	F46BC pickup status bit	0 = inactive 1 = active	–
498	–	4849	N/A	F46BC timeout status bit	0 = inactive 1 = active	–
499	–	4847	N/A	FBM #2 pickup status bit	0 = inactive 1 = active	–
500	–	4849	N/A	FBM #2 timeout status bit	0 = inactive 1 = active	–
501	–	3780	N/A	F25 timeout target status bit	0 = inactive 1 = active	–
502	–	3780	N/A	F27#1 phase A timeout target status bit	0 = inactive 1 = active	–
503	–	3780	N/A	F27#1 phase B timeout target status bit	0 = inactive 1 = active	–
504	–	3780	N/A	F27#1 phase C timeout target status bit	0 = inactive 1 = active	–
505	–	3780	N/A	F27#2 phase A timeout target status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
506	–	3780	N/A	F27#2 phase B timeout target status bit	0 = inactive 1 = active	–
507	–	3780	N/A	F27#2 phase C timeout target status bit	0 = inactive 1 = active	–
508	–	3780	N/A	F27#3 phase A timeout target status bit	0 = inactive 1 = active	–
509	–	3780	N/A	F27#3 phase B timeout target status bit	0 = inactive 1 = active	–
510	–	3780	N/A	F27#3 phase C timeout target status bit	0 = inactive 1 = active	–
511	–	3780	N/A	F27#4 phase A timeout target status bit	0 = inactive 1 = active	–
512	–	3780	N/A	F27#4 phase B timeout target status bit	0 = inactive 1 = active	–
513	–	3780	N/A	F27#4 phase C timeout target status bit	0 = inactive 1 = active	–
514	–	3780	N/A	F27PP phase A timeout target status bit	0 = inactive 1 = active	–
515	–	3780	N/A	F27PP phase B timeout target status bit	0 = inactive 1 = active	–
516	–	3780	N/A	F27PP phase C timeout target status bit	0 = inactive 1 = active	–
517	–	3781	N/A	F27Vz1 timeout target status bit	0 = inactive 1 = active	–
518	–	3781	N/A	F32#1 phase A timeout target status bit	0 = inactive 1 = active	–
519	–	3781	N/A	F32#1 phase B timeout target status bit	0 = inactive 1 = active	–
520	–	3781	N/A	F32#1 phase C timeout target status bit	0 = inactive 1 = active	–
521	–	3781	N/A	F32#2 phase A timeout target status bit	0 = inactive 1 = active	–
522	–	3781	N/A	F32#2 phase B timeout target status bit	0 = inactive 1 = active	–
523	–	3781	N/A	F32#2 phase C timeout target status bit	0 = inactive 1 = active	–
524	–	3781	N/A	F32#3 phase A timeout target status bit	0 = inactive 1 = active	–
525	–	3781	N/A	F32#3 phase B timeout target status bit	0 = inactive 1 = active	–
526	–	3781	N/A	F32#3 phase C timeout target status bit	0 = inactive 1 = active	–
527	–	3781	N/A	F32#4 phase A timeout target status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
528	–	3781	N/A	F32#4 phase B timeout target status bit	0 = inactive 1 = active	–
529	–	3781	N/A	F32#4 phase C timeout target status bit	0 = inactive 1 = active	–
530	–	3781	N/A	F50P#1 phase A timeout target status bit	0 = inactive 1 = active	–
531	–	3781	N/A	F50P#1 phase B timeout target status bit	0 = inactive 1 = active	–
532	–	3781	N/A	F50P#1 phase C timeout target status bit	0 = inactive 1 = active	–
533	–	3782	N/A	F50P#2 phase A timeout target status bit	0 = inactive 1 = active	–
534	–	3782	N/A	F50P#2 phase B timeout target status bit	0 = inactive 1 = active	–
535	–	3782	N/A	F50P#2 phase C timeout target status bit	0 = inactive 1 = active	–
536	–	3782	N/A	F50P#3 phase A timeout target status bit	0 = inactive 1 = active	–
537	–	3782	N/A	F50P#3 phase B timeout target status bit	0 = inactive 1 = active	–
538	–	3782	N/A	F50P#3 phase C timeout target status bit	0 = inactive 1 = active	–
539	–	3782	N/A	F50P#4 phase A timeout target status bit	0 = inactive 1 = active	–
540	–	3782	N/A	F50P#4 phase B timeout target status bit	0 = inactive 1 = active	–
541	–	3782	N/A	F50P#4 phase C timeout target status bit	0 = inactive 1 = active	–
542	–	3782	N/A	F50P#5 phase A timeout target status bit	0 = inactive 1 = active	–
543	–	3782	N/A	F50P#5 phase B timeout target status bit	0 = inactive 1 = active	–
544	–	3782	N/A	F50P#5 phase C timeout target status bit	0 = inactive 1 = active	–
545	–	3782	N/A	F50N#1 timeout target status bit	0 = inactive 1 = active	–
546	–	3782	N/A	F50N#2 timeout target status bit	0 = inactive 1 = active	–
547	–	3782	N/A	F50N#3 timeout target status bit	0 = inactive 1 = active	–
548	–	3782	N/A	F50N#4 timeout target status bit	0 = inactive 1 = active	–
549	–	3783	N/A	F50N#5 timeout target status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
550	–	3783	N/A	F50G#1 timeout target status bit	0 = inactive 1 = active	–
551	–	3783	N/A	F50G#2 timeout target status bit	0 = inactive 1 = active	–
552	–	3783	N/A	F50G#3 timeout target status bit	0 = inactive 1 = active	–
553	–	3783	N/A	F50G#4 timeout target status bit	0 = inactive 1 = active	–
554	–	3783	N/A	F50G#5 timeout target status bit	0 = inactive 1 = active	–
555	–	3783	N/A	F46DT#1 timeout target status bit	0 = inactive 1 = active	–
556	–	3783	N/A	F46DT#2 timeout target status bit	0 = inactive 1 = active	–
557	–	3783	N/A	F46DT#3 timeout target status bit	0 = inactive 1 = active	–
558	–	3783	N/A	F46DT#4 timeout target status bit	0 = inactive 1 = active	–
559	–	3783	N/A	F46DT#5 timeout target status bit	0 = inactive 1 = active	–
560	–	3783	N/A	F50BF timeout target status bit	0 = inactive 1 = active	–
561	–	3783	N/A	F51P#1 phase A timeout target status bit	0 = inactive 1 = active	–
562	–	3783	N/A	F51P#1 phase B timeout target status bit	0 = inactive 1 = active	–
563	–	3783	N/A	F51P#1 phase C timeout target status bit	0 = inactive 1 = active	–
564	–	3783	N/A	F51P#2 phase A timeout target status bit	0 = inactive 1 = active	–
565	–	3784	N/A	F51P#2 phase B timeout target status bit	0 = inactive 1 = active	–
566	–	3784	N/A	F51P#2 phase C timeout target status bit	0 = inactive 1 = active	–
567	–	3784	N/A	F51P#3 phase A timeout target status bit	0 = inactive 1 = active	–
568	–	3784	N/A	F51P#3 phase B timeout target status bit	0 = inactive 1 = active	–
569	–	3784	N/A	F51P#3 phase C timeout target status bit	0 = inactive 1 = active	–
570	–	3784	N/A	F51P#4 phase A timeout target status bit	0 = inactive 1 = active	–
571	–	3784	N/A	F51P#4 phase B timeout target status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
572	–	3784	N/A	F51P#4 phase C timeout target status bit	0 = inactive 1 = active	–
573	–	3784	N/A	F51P#5 phase A timeout target status bit	0 = inactive 1 = active	–
574	–	3784	N/A	F51P#5 phase B timeout target status bit	0 = inactive 1 = active	–
575	–	3784	N/A	F51P#5 phase C timeout target status bit	0 = inactive 1 = active	–
576	–	3784	N/A	F51N#1 timeout target status bit	0 = inactive 1 = active	–
577	–	3784	N/A	F51N#2 timeout target status bit	0 = inactive 1 = active	–
578	–	3784	N/A	F51N#3 timeout target status bit	0 = inactive 1 = active	–
579	–	3784	N/A	F51N#4 timeout target status bit	0 = inactive 1 = active	–
580	–	3784	N/A	F51N#5 timeout target status bit	0 = inactive 1 = active	–
581	–	3785	N/A	F51G#1 timeout target status bit	0 = inactive 1 = active	–
582	–	3785	N/A	F51G#2 timeout target status bit	0 = inactive 1 = active	–
583	–	3785	N/A	F51G#3 timeout target status bit	0 = inactive 1 = active	–
584	–	3785	N/A	F51G#4 timeout target status bit	0 = inactive 1 = active	–
585	–	3785	N/A	F51G#5 timeout target status bit	0 = inactive 1 = active	–
586	–	3785	N/A	F46IT#1 timeout target status bit	0 = inactive 1 = active	–
587	–	3785	N/A	F46IT#2 timeout target status bit	0 = inactive 1 = active	–
588	–	3785	N/A	F46IT#3 timeout target status bit	0 = inactive 1 = active	–
589	–	3785	N/A	F46IT#4 timeout target status bit	0 = inactive 1 = active	–
590	–	3785	N/A	F46IT#5 timeout target status bit	0 = inactive 1 = active	–
591	–	3785	N/A	F59#1 phase A timeout target status bit	0 = inactive 1 = active	–
592	–	3785	N/A	F59#1 phase B timeout target status bit	0 = inactive 1 = active	–
593	–	3785	N/A	F59#1 phase C timeout target status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
594	–	3785	N/A	F59#2 phase A timeout target status bit	0 = inactive 1 = active	–
595	–	3785	N/A	F59#2 phase B timeout target status bit	0 = inactive 1 = active	–
596	–	3785	N/A	F59#2 phase C timeout target status bit	0 = inactive 1 = active	–
597	–	3786	N/A	F59#3 phase A timeout target status bit	0 = inactive 1 = active	–
598	–	3786	N/A	F59#3 phase B timeout target status bit	0 = inactive 1 = active	–
599	–	3786	N/A	F59#3 phase C timeout target status bit	0 = inactive 1 = active	–
600	–	3786	N/A	F59#4 phase A timeout target status bit	0 = inactive 1 = active	–
601	–	3786	N/A	F59#4 phase B timeout target status bit	0 = inactive 1 = active	–
602	–	3786	N/A	F59#4 phase C timeout target status bit	0 = inactive 1 = active	–
603	–	3786	N/A	F59PP phase A timeout target status bit	0 = inactive 1 = active	–
604	–	3786	N/A	F59PP phase B timeout target status bit	0 = inactive 1 = active	–
605	–	3786	N/A	F59PP phase C timeout target status bit	0 = inactive 1 = active	–
606	–	3786	N/A	F59N #1 timeout target status bit	0 = inactive 1 = active	–
607	–	2089	N/A	F59N #2 timeout target status bit	0 = inactive 1 = active	–
608	–	3786	N/A	F59Vz1 timeout target status bit	0 = inactive 1 = active	–
609	–	3786	N/A	F47 timeout target status bit	0 = inactive 1 = active	–
610	–	3786	N/A	F67#1 phase A timeout target status bit	0 = inactive 1 = active	–
611	–	3786	N/A	F67#1 phase B timeout target status bit	0 = inactive 1 = active	–
612	–	3786	N/A	F67#1 phase C timeout target status bit	0 = inactive 1 = active	–
613	–	3786	N/A	F67#2 phase A timeout target status bit	0 = inactive 1 = active	–
614	–	3787	N/A	F67#2 phase B timeout target status bit	0 = inactive 1 = active	–
615	–	3787	N/A	F67#2 phase C timeout target status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
616	–	3787	N/A	F67#3 phase A timeout target status bit	0 = inactive 1 = active	–
617	–	3787	N/A	F67#3 phase B timeout target status bit	0 = inactive 1 = active	–
618	–	3787	N/A	F67#3 phase C timeout target status bit	0 = inactive 1 = active	–
619	–	3787	N/A	F67#4 phase A timeout target status bit	0 = inactive 1 = active	–
620	–	3787	N/A	F67#4 phase B timeout target status bit	0 = inactive 1 = active	–
621	–	3787	N/A	F67#4 phase C timeout target status bit	0 = inactive 1 = active	–
622	–	3787	N/A	F67#5 phase A timeout target status bit	0 = inactive 1 = active	–
623	–	3787	N/A	F67#5 phase B timeout target status bit	0 = inactive 1 = active	–
624	–	3787	N/A	F67#5 phase C timeout target status bit	0 = inactive 1 = active	–
625	–	3787	N/A	F67N#1 timeout target status bit	0 = inactive 1 = active	–
626	–	3787	N/A	F67N#2 timeout target status bit	0 = inactive 1 = active	–
627	–	3787	N/A	F67N#3 timeout target status bit	0 = inactive 1 = active	–
628	–	3787	N/A	F67N#4 timeout target status bit	0 = inactive 1 = active	–
629	–	3787	N/A	F67N#5 timeout target status bit	0 = inactive 1 = active	–
630	–	3788	N/A	F67G#1 timeout target status bit	0 = inactive 1 = active	–
631	–	3788	N/A	F67G#2 timeout target status bit	0 = inactive 1 = active	–
632	–	3788	N/A	F67G#3 timeout target status bit	0 = inactive 1 = active	–
633	–	3788	N/A	F67G#4 timeout target status bit	0 = inactive 1 = active	–
634	–	3788	N/A	F67G#5 timeout target status bit	0 = inactive 1 = active	–
635	–	3788	N/A	F67Q#1 timeout target status bit	0 = inactive 1 = active	–
636	–	3788	N/A	F67Q#2 timeout target status bit	0 = inactive 1 = active	–
637	–	3788	N/A	F67Q#3 timeout target status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
638	–	3788	N/A	F67Q#4 timeout target status bit	0 = inactive 1 = active	–
639	–	3788	N/A	F67Q#5 timeout target status bit	0 = inactive 1 = active	–
640	–	3788	N/A	TCM timeout target status bit	0 = inactive 1 = active	–
641	–	3788	N/A	CCM timeout target status bit	0 = inactive 1 = active	–
642	–	3788	N/A	F81#1 timeout target status bit	0 = inactive 1 = active	–
643	–	3788	N/A	F81#2 timeout target status bit	0 = inactive 1 = active	–
644	–	3788	N/A	F81#3 timeout target status bit	0 = inactive 1 = active	–
645	–	3789	N/A	F81#4 timeout target status bit	0 = inactive 1 = active	–
646	–	3789	N/A	F81R#1 timeout target status bit	0 = inactive 1 = active	–
647	–	3789	N/A	F81R#2 timeout target status bit	0 = inactive 1 = active	–
648	–	3789	N/A	IPSLOGIC#1 timeout target status bit	0 = inactive 1 = active	–
649	–	3789	N/A	IPSLOGIC#2 timeout target status bit	0 = inactive 1 = active	–
650	–	3789	N/A	IPSLOGIC#3 timeout target status bit	0 = inactive 1 = active	–
651	–	3789	N/A	IPSLOGIC#4 timeout target status bit	0 = inactive 1 = active	–
652	–	3789	N/A	IPSLOGIC#5 timeout target status bit	0 = inactive 1 = active	–
653	–	3789	N/A	IPSLOGIC#6 timeout target status bit	0 = inactive 1 = active	–
654	–	3789	N/A	IPSLOGIC#7 timeout target status bit	0 = inactive 1 = active	–
655	–	3789	N/A	IPSLOGIC#8 timeout target status bit	0 = inactive 1 = active	–
656	–	3789	N/A	PSM timeout target status bit	0 = inactive 1 = active	–
657	–	3789	N/A	F60FL timeout target status bit	0 = inactive 1 = active	–
658	–	3789	N/A	FTHD/TDD #1 phase A timeout target status bit	0 = inactive 1 = active	–
659	–	1939	N/A	FTHD/TDD #1 phase B timeout target status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
660	–	1939	N/A	FTHD/TDD #1 phase C timeout target status bit	0 = inactive 1 = active	–
661	–	1939	N/A	FTHD/TDD #2 phase A timeout target status bit	0 = inactive 1 = active	–
662	–	1939	N/A	FTHD/TDD #2 phase B timeout target status bit	0 = inactive 1 = active	–
663	–	1939	N/A	FTHD/TDD #2 phase C timeout target status bit	0 = inactive 1 = active	–
664	–	1939	N/A	F59I phase A timeout target status bit	0 = inactive 1 = active	–
665	–	1939	N/A	F59I phase B timeout target status bit	0 = inactive 1 = active	–
666	–	1939	N/A	F59I phase C timeout target status bit	0 = inactive 1 = active	–
667	–	1939	N/A	F40 #1 timeout target status bit	0 = inactive 1 = active	–
668	–	1939	N/A	F40 #2 timeout target status bit	0 = inactive 1 = active	–
669	–	1939	N/A	F40VC #1 timeout target status bit	0 = inactive 1 = active	–
670	–	1939	N/A	F40VC #2 timeout target status bit	0 = inactive 1 = active	–
671	–	1939	N/A	FBM #1 timeout target status bit	0 = inactive 1 = active	–
672	–	1992	N/A	HLT F50P phase A timeout target status bit	0 = inactive 1 = active	–
673	–	1992	N/A	HLT F50P phase B timeout target status bit	0 = inactive 1 = active	–
674	–	1992	N/A	HLT F50P phase C timeout target status bit	0 = inactive 1 = active	–
675	–	1992	N/A	HLT F50 Ground/Ground Sensitive timeout target status bit	0 = inactive 1 = active	–
676	–	1992	N/A	HLT F51P phase A timeout target status bit	0 = inactive 1 = active	–
677	–	1992	N/A	HLT F51P phase B timeout target status bit	0 = inactive 1 = active	–
678	–	1992	N/A	HLT F51P phase C timeout target status bit	0 = inactive 1 = active	–
679	–	1992	N/A	HLT F51 Ground/Ground Sensitive timeout target status bit	0 = inactive 1 = active	–
680	–	1992	N/A	Sectionalizer timeout target status bit	0 = inactive 1 = active	–
681	–	1997	N/A	Close Block Ph A timeout target status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
682	–	2085	N/A	Close Block Ph B timeout target status bit	0 = inactive 1 = active	–
683	–	2086	N/A	Close Block Ph C timeout target status bit	0 = inactive 1 = active	–
684	–	2089	N/A	Loop Scheme timeout target status bit	0 = inactive 1 = active	–
685	–	2089	N/A	F27#5 3 phases timeout target status bit	0 = inactive 1 = active	–
686	–	2089	N/A	F27#6 3 phases timeout target status bit	0 = inactive 1 = active	–
687	–	4706	N/A	F27#7 3 phases timeout target status bit	0 = inactive 1 = active	–
688	–	4706	N/A	F27#8 3 phases timeout target status bit	0 = inactive 1 = active	–
689	–	4706	N/A	F59#5 3 phases timeout target status bit	0 = inactive 1 = active	–
690	–	4706	N/A	F59#6 3 phases timeout target status bit	0 = inactive 1 = active	–
691	–	4706	N/A	F59#7 3 phases timeout target status bit	0 = inactive 1 = active	–
692	–	4706	N/A	F59#8 3 phases timeout target status bit	0 = inactive 1 = active	–
693	–	4706	N/A	HLT F50 Neutral timeout target status bit	0 = inactive 1 = active	–
694	–	4706	N/A	HLT F51 Neutral timeout target status bit	0 = inactive 1 = active	–
695	–	4812	N/A	F81#5 timeout target status bit	0 = inactive 1 = active	–
696	–	4813	N/A	F81#6 timeout target status bit	0 = inactive 1 = active	–
697	–	4706	N/A	HCL A timeout target status bit	0 = inactive 1 = active	–
698	–	4706	N/A	HCL B timeout target status bit	0 = inactive 1 = active	–
699	–	4706	N/A	HCL C timeout target status bit	0 = inactive 1 = active	–
700	–	4813	N/A	HCL G timeout target status bit	0 = inactive 1 = active	–
701	–	4813	N/A	F81#7 timeout target status bit	0 = inactive 1 = active	–
702	–	4813	N/A	F81#8 timeout target status bit	0 = inactive 1 = active	–
703	–	4813	N/A	F81R#3 timeout target status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
704	–	4855	N/A	F81R#4 timeout target status bit	0 = inactive 1 = active	–
705	–	4855	N/A	F81R#5 timeout target status bit	0 = inactive 1 = active	–
706	–	4855	N/A	F81R#6 timeout target status bit	0 = inactive 1 = active	–
707	–	4855	N/A	F81R#7 timeout target status bit	0 = inactive 1 = active	–
708	–	4855	N/A	F81R#8 timeout target status bit	0 = inactive 1 = active	–
709	–	4855	N/A	F46BC timeout target status bit	0 = inactive 1 = active	–
710	–	4855	N/A	FBM #2 timeout target status bit	0 = inactive 1 = active	–
711	–	1906	N/A	Trouble State	0 = inactive 1 = active	–
712	–	1906	N/A	Control Status OK	0 = inactive 1 = active	–
713	–	3794	N/A	Recloser Malfunction	0 = inactive 1 = active	–
714	–	1906	N/A	Ground Trip blocked	0 = inactive 1 = active	–
715	–	1906	N/A	Non-reclosing Active	0 = inactive 1 = active	–
716	–	1906	N/A	Local/Remote State	0 = inactive 1 = active	–
717	–	1906	N/A	Hot Line Tag Active State	0 = inactive 1 = active	–
718	–	1906	N/A	Recloser Closed	0 = inactive 1 = active	–
719	–	1906	N/A	Control Lockout State	0 = inactive 1 = active	–
720	–	1906	N/A	Normal Profile Active	0 = inactive 1 = active	–
721	–	3798	N/A	Battery Discharge test Fail	0 = inactive 1 = active	–
722	–	3798	N/A	Main Power is ON	0 = inactive 1 = active	–
723	–	3798	N/A	Battery Charging Active	0 = inactive 1 = active	–
724	–	3798	N/A	Battery Charging Board Condition(BCB)	0 = inactive 1 = active	–
725	–	4991	N/A	Charging Mode (boost or float, trickle)	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
726	–	3798	N/A	Battery Present (No batt)	0 = inactive 1 = active	–
727	–	2139	N/A	LED 1 RED Front Panel Status	0 = inactive 1 = active	–
728	–	2140	N/A	LED 2 RED Front Panel Status	0 = inactive 1 = active	–
729	–	2141	N/A	LED 3 RED Front Panel Status	0 = inactive 1 = active	–
730	–	2142	N/A	LED 4 RED Front Panel Status	0 = inactive 1 = active	–
731	–	2143	N/A	LED 5 RED Front Panel Status	0 = inactive 1 = active	–
732	–	2144	N/A	LED 6 RED Front Panel Status	0 = inactive 1 = active	–
733	–	2145	N/A	LED 7 RED Front Panel Status	0 = inactive 1 = active	–
734	–	2146	N/A	LED 8 RED Front Panel Status	0 = inactive 1 = active	–
735	–	2147	N/A	LED 9 RED Front Panel Status	0 = inactive 1 = active	–
736	–	2148	N/A	LED 10 RED Front Panel Status	0 = inactive 1 = active	–
737	–	2149	N/A	LED 11 RED Front Panel Status	0 = inactive 1 = active	–
738	–	2150	N/A	LED 12 RED Front Panel Status	0 = inactive 1 = active	–
739	–	2151	N/A	LED 13 RED Front Panel Status	0 = inactive 1 = active	–
740	–	2152	N/A	LED 14 RED Front Panel Status	0 = inactive 1 = active	–
741	–	2153	N/A	LED 15 RED Front Panel Status	0 = inactive 1 = active	–
742	–	2154	N/A	LED 16 RED Front Panel Status	0 = inactive 1 = active	–
743	–	2155	N/A	LED 17 RED Front Panel Status	0 = inactive 1 = active	–
744	–	2156	N/A	LED 18 RED Front Panel Status	0 = inactive 1 = active	–
745	–	2157	N/A	LED 19 RED Front Panel Status	0 = inactive 1 = active	–
746	–	2158	N/A	LED 20 RED Front Panel Status	0 = inactive 1 = active	–
747	–	2159	N/A	LED 21 RED Front Panel Status	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
748	–	2160	N/A	LED 22 RED Front Panel Status	0 = inactive 1 = active	–
749	–	2161	N/A	LED 23 RED Front Panel Status	0 = inactive 1 = active	–
750	–	2139	N/A	LED 1 GREEN Front Panel Status	0 = inactive 1 = active	–
751	–	2140	N/A	LED 2 GREEN Front Panel Status	0 = inactive 1 = active	–
752	–	2141	N/A	LED 3 GREEN Front Panel Status	0 = inactive 1 = active	–
753	–	2142	N/A	LED 4 GREEN Front Panel Status	0 = inactive 1 = active	–
754	–	2143	N/A	LED 5 GREEN Front Panel Status	0 = inactive 1 = active	–
755	–	2144	N/A	LED 6 GREEN Front Panel Status	0 = inactive 1 = active	–
756	–	2145	N/A	LED 7 GREEN Front Panel Status	0 = inactive 1 = active	–
757	–	2146	N/A	LED 8 GREEN Front Panel Status	0 = inactive 1 = active	–
758	–	2147	N/A	LED 9 GREEN Front Panel Status	0 = inactive 1 = active	–
759	–	2148	N/A	LED 10 GREEN Front Panel Status	0 = inactive 1 = active	–
760	–	2149	N/A	LED 11 GREEN Front Panel Status	0 = inactive 1 = active	–
761	–	2150	N/A	LED 12 GREEN Front Panel Status	0 = inactive 1 = active	–
762	–	2151	N/A	LED 13 GREEN Front Panel Status	0 = inactive 1 = active	–
763	–	2152	N/A	LED 14 GREEN Front Panel Status	0 = inactive 1 = active	–
764	–	2153	N/A	LED 15 GREEN Front Panel Status	0 = inactive 1 = active	–
765	–	2154	N/A	LED 16 GREEN Front Panel Status	0 = inactive 1 = active	–
766	–	2155	N/A	LED 17 GREEN Front Panel Status	0 = inactive 1 = active	–
767	–	2156	N/A	LED 18 GREEN Front Panel Status	0 = inactive 1 = active	–
768	–	2157	N/A	LED 19 GREEN Front Panel Status	0 = inactive 1 = active	–
769	–	2158	N/A	LED 20 GREEN Front Panel Status	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
770	–	2159	N/A	LED 21 GREEN Front Panel Status	0 = inactive 1 = active	–
771	–	2160	N/A	LED 22 GREEN Front Panel Status	0 = inactive 1 = active	–
772	–	2161	N/A	LED 23 GREEN Front Panel Status	0 = inactive 1 = active	–
773	–	2139	N/A	LED 1 AMBER Front Panel Status	0 = inactive 1 = active	–
774	–	2140	N/A	LED 2 AMBER Front Panel Status	0 = inactive 1 = active	–
775	–	2141	N/A	LED 3 AMBER Front Panel Status	0 = inactive 1 = active	–
776	–	2142	N/A	LED 4 AMBER Front Panel Status	0 = inactive 1 = active	–
777	–	2143	N/A	LED 5 AMBER Front Panel Status	0 = inactive 1 = active	–
778	–	2144	N/A	LED 6 AMBER Front Panel Status	0 = inactive 1 = active	–
779	–	2145	N/A	LED 7 AMBER Front Panel Status	0 = inactive 1 = active	–
780	–	2146	N/A	LED 8 AMBER Front Panel Status	0 = inactive 1 = active	–
781	–	2147	N/A	LED 9 AMBER Front Panel Status	0 = inactive 1 = active	–
782	–	2148	N/A	LED 10 AMBER Front Panel Status	0 = inactive 1 = active	–
783	–	2149	N/A	LED 11 AMBER Front Panel Status	0 = inactive 1 = active	–
784	–	2150	N/A	LED 12 AMBER Front Panel Status	0 = inactive 1 = active	–
785	–	2151	N/A	LED 13 AMBER Front Panel Status	0 = inactive 1 = active	–
786	–	2152	N/A	LED 14 AMBER Front Panel Status	0 = inactive 1 = active	–
787	–	2153	N/A	LED 15 AMBER Front Panel Status	0 = inactive 1 = active	–
788	–	2154	N/A	LED 16 AMBER Front Panel Status	0 = inactive 1 = active	–
789	–	2155	N/A	LED 17 AMBER Front Panel Status	0 = inactive 1 = active	–
790	–	2156	N/A	LED 18 AMBER Front Panel Status	0 = inactive 1 = active	–
791	–	2157	N/A	LED 19 AMBER Front Panel Status	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
792	–	2158	N/A	LED 20 AMBER Front Panel Status	0 = inactive 1 = active	–
793	–	2159	N/A	LED 21 AMBER Front Panel Status	0 = inactive 1 = active	–
794	–	2160	N/A	LED 22 AMBER Front Panel Status	0 = inactive 1 = active	–
795	–	2161	N/A	LED 23 AMBER Front Panel Status	0 = inactive 1 = active	–
796	–	3965	N/A	Sectionalizer Initiate Restraint Status Ph-A	0 = inactive 1 = active	–
797	–	3965	N/A	Sectionalizer Initiate Restraint Status Ph-B	0 = inactive 1 = active	–
798	–	3965	N/A	Sectionalizer Initiate Restraint Status Ph-C	0 = inactive 1 = active	–
799	–	3965	N/A	Sectionalizer Current Restraint Status Ph-A	0 = inactive 1 = active	–
800	–	3965	N/A	Sectionalizer Current Restraint Status Ph-B	0 = inactive 1 = active	–
801	–	3965	N/A	Sectionalizer Current Restraint Status Ph-C	0 = inactive 1 = active	–
802	–	3965	N/A	Sectionalizer Ground Current Restraint Block	0 = inactive 1 = active	–
803	–	3932	N/A	HLT active via IPSlogic	0 = inactive 1 = active	–
804	–	3932	N/A	HLT active via Communications	0 = inactive 1 = active	–
805	–	3932	N/A	HLT active via Local	0 = inactive 1 = active	–
806	–	2050	N/A	Fail to Trip 3-Phases	0 = inactive 1 = active	–
807	–	2050	N/A	Fail to Trip Phase A	0 = inactive 1 = active	–
808	–	2050	N/A	Fail to Trip Phase B	0 = inactive 1 = active	–
809	–	2050	N/A	Fail to Trip Phase C	0 = inactive 1 = active	–
810	–	2050	N/A	Fail to Close 3-Phases	0 = inactive 1 = active	–
811	–	2050	N/A	Fail to Close Phase A	0 = inactive 1 = active	–
812	–	2050	N/A	Fail to Close Phase B	0 = inactive 1 = active	–
813	–	2050	N/A	Fail to Close Phase C	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
814	–	2052	N/A	Ethernet Link Lost Port 2	0 = inactive 1 = active	–
815	–	2052	N/A	Ethernet Link Lost Port 3	0 = inactive 1 = active	–
816	–	3798	N/A	BECO Drive Not Ready	0 = inactive 1 = active	–
817	–	3798	N/A	BECO Drive Status	0 = inactive 1 = active	–
818	–	2052	N/A	Control Status	0 = inactive 1 = active	–
819	–	2050	N/A	Recloser Block 3-Phases	0 = inactive 1 = active	–
820	–	2050	N/A	Recloser Block Phase-A	0 = inactive 1 = active	–
821	–	2050	N/A	Recloser Block Phase-B	0 = inactive 1 = active	–
822	–	2050	N/A	Recloser Block Phase-C	0 = inactive 1 = active	–
823	–	2511	N/A	Instantaneous Trip Lockout 3-Phase	0 = inactive 1 = active	–
824	–	4555	N/A	Instantaneous Trip Lockout Phase A	0 = inactive 1 = active	–
825	–	4570	N/A	Instantaneous Trip Lockout Phase B	0 = inactive 1 = active	–
826	–	4585	N/A	Instantaneous Trip Lockout Phase C	0 = inactive 1 = active	–
827	–	2253	N/A	Pole Failure Ph A	0 = inactive 1 = active	–
828	–	2253	N/A	Pole Failure Ph B	0 = inactive 1 = active	–
829	–	2253	N/A	Pole Failure Ph C	0 = inactive 1 = active	–
830	–	2052	N/A	Control Circuit Interrupted	0 = inactive 1 = active	–
831	–	2253	N/A	Control Circuit Interrupted Phase A	0 = inactive 1 = active	–
832	–	2253	N/A	Control Circuit Interrupted Phase B	0 = inactive 1 = active	–
833	–	2253	N/A	Control Circuit Interrupted Phase C	0 = inactive 1 = active	–
834	–	2511	N/A	Cold Load Pickup Status 3-Phase	0 = inactive 1 = active	–
835	–	4555	N/A	Cold Load Pickup Status Phase A	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
836	–	4570	N/A	Cold Load Pickup Status Phase B	0 = inactive 1 = active	–
837	–	4585	N/A	Cold Load Pickup Status Phase C	0 = inactive 1 = active	–
838	–	2052	N/A	Control General Alarm	0 = inactive 1 = active	–
839	–	2054	N/A	Above OC Pickup	0 = inactive 1 = active	–
840	–	2075	N/A	Reclose in process 3-Phase	0 = inactive 1 = active	–
841	–	2076	N/A	Reclose in process Phase A	0 = inactive 1 = active	–
842	–	2077	N/A	Reclose in process Phase B	0 = inactive 1 = active	–
843	–	2078	N/A	Reclose in process Phase C	0 = inactive 1 = active	–
844	–	2006	N/A	Sectionalizer Count in process 3-Phase	0 = inactive 1 = active	–
845	–	2006	N/A	Sectionalizer Count in process Phase A	0 = inactive 1 = active	–
846	–	2007	N/A	Sectionalizer Count in process Phase B	0 = inactive 1 = active	–
847	–	2008	N/A	Sectionalizer Count in process Phase C	0 = inactive 1 = active	–
848	–	2050	N/A	Close Block Override Active Phase A	0 = inactive 1 = active	–
849	–	2050	N/A	Close Block Override Active Phase B	0 = inactive 1 = active	–
850	–	2050	N/A	Close Block Override Active Phase C	0 = inactive 1 = active	–
851	–	2050	N/A	Phase Slow Curve Active	0 = inactive 1 = active	–
852	–	2050	N/A	Phase Fast Curve Active	0 = inactive 1 = active	–
853	–	2050	N/A	Ground Slow Curve Active	0 = inactive 1 = active	–
854	–	2050	N/A	Ground Fast Curve Active	0 = inactive 1 = active	–
855	–	2050	N/A	Recloser Deactivate G/N Functions	0 = inactive 1 = active	–
856	–	3926	N/A	Button 10 Status	0 = inactive 1 = active	–
857	–	3926	N/A	Button 11 Status	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
858	–	3926	N/A	Button 12 Status	0 = inactive 1 = active	–
859	–	3926	N/A	Button 13 Status	0 = inactive 1 = active	–
860	–	3926	N/A	Button 14 Status	0 = inactive 1 = active	–
861	–	3926	N/A	Button 15 Status	0 = inactive 1 = active	–
862	–	3926	N/A	Button 16 Status	0 = inactive 1 = active	–
863	–	3926	N/A	Button 17 Status	0 = inactive 1 = active	–
864	–	2096	N/A	Button 10 Toggle State	0 = inactive 1 = active	–
865	–	2096	N/A	Button 11 Toggle State	0 = inactive 1 = active	–
866	–	2096	N/A	Button 12 Toggle State	0 = inactive 1 = active	–
867	–	2096	N/A	Button 13 Toggle State	0 = inactive 1 = active	–
868	–	2096	N/A	Button 14 Toggle State	0 = inactive 1 = active	–
869	–	2096	N/A	Button 15 Toggle State	0 = inactive 1 = active	–
870	–	2096	N/A	Button 16 Toggle State	0 = inactive 1 = active	–
871	–	2096	N/A	Button 17 Toggle State	0 = inactive 1 = active	–
872	–	1538	N/A	Button 10 Function Status	0 = inactive 1 = active	–
873	–	1538	N/A	Button 11 Function Status	0 = inactive 1 = active	–
874	–	1538	N/A	Button 12 Function Status	0 = inactive 1 = active	–
875	–	1538	N/A	Button 13 Function Status	0 = inactive 1 = active	–
876	–	1538	N/A	Button 14 Function Status	0 = inactive 1 = active	–
877	–	1538	N/A	Button 15 Function Status	0 = inactive 1 = active	–
878	–	1538	N/A	Button 16 Function Status	0 = inactive 1 = active	–
879	–	1538	N/A	Button 17 Function Status	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
880	–	4700	N/A	Loop Scheme DeadLineSens Vy Pickup Status	0 = inactive 1 = active	–
881	–	4700	N/A	Loop Scheme DeadLineSens Vy Timeout Status	0 = inactive 1 = active	–
882	–	4700	N/A	Loop Scheme DeadLineSens Vz Pickup Status	0 = inactive 1 = active	–
883	–	4700	N/A	Loop Scheme DeadLineSens Vz Timeout Status	0 = inactive 1 = active	–
884	–	4700	N/A	Loop Scheme Voltage Restore Vy Pickup Status	0 = inactive 1 = active	–
885	–	4700	N/A	Loop Scheme LiveLineSens Vy Timeout Status	0 = inactive 1 = active	–
886	–	4700	N/A	Loop Scheme LiveLineSens Vz Timeout Status	0 = inactive 1 = active	–
887	–	4700	N/A	Loop Scheme Live Line Vz Timeout Status	0 = inactive 1 = active	–
888	–	4700	N/A	Loop Scheme DeadLine Y Pickup Status	0 = inactive 1 = active	–
889	–	4700	N/A	Loop Scheme DeadLine Y Timeout Status	0 = inactive 1 = active	–
890	–	4700	N/A	Loop Scheme DeadLine Z Pickup Status	0 = inactive 1 = active	–
891	–	4700	N/A	Loop Scheme DeadLine Z Timeout Status	0 = inactive 1 = active	–
892	–	4700	N/A	Loop Scheme LiveLine Y Pickup Status	0 = inactive 1 = active	–
893	–	4700	N/A	Loop Scheme LiveLineWindow Y Timeout Status	0 = inactive 1 = active	–
894	–	4700	N/A	Loop Scheme LiveLine Z Pickup Status	0 = inactive 1 = active	–
895	–	4700	N/A	Loop Scheme LiveLineWindow Z Timeout Status	0 = inactive 1 = active	–
896	–	4701	N/A	Loop Scheme DeadLine Y Action Executed	0 = inactive 1 = active	–
897	–	4701	N/A	Loop Scheme DeadLine Z Action Executed	0 = inactive 1 = active	–
898	–	4701	N/A	Loop Scheme LiveLine Y Action Executed	0 = inactive 1 = active	–
899	–	4701	N/A	Loop Scheme LiveLine Z Action Executed	0 = inactive 1 = active	–
900	–	4701	N/A	Loop Scheme Action Breaker Trip	0 = inactive 1 = active	–
901	–	4701	N/A	Loop Scheme Action Breaker Close	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
902	–	4701	N/A	Loop Scheme After Action Gnd Trip Block	0 = inactive 1 = active	–
903	–	4701	N/A	Loop Scheme After Action Reclosing Block	0 = inactive 1 = active	–
904	–	4701	N/A	Loop Scheme After Action Profile Switch	0 = inactive 1 = active	–
905	–	4701	N/A	Loop Scheme Reset	0 = inactive 1 = active	–
906	–	4701	N/A	Loop Scheme LiveLineConfirm Vy Timeout Status	0 = inactive 1 = active	–
907	–	4701	N/A	Loop Scheme LiveLineConfirm Vz Timeout Status	0 = inactive 1 = active	–
908	–	4691	N/A	Loop Scheme Sectionalizer Mode	0 = inactive 1 = active	–
909	–	4691	N/A	Loop Scheme Tie Mode	0 = inactive 1 = active	–
910	–	4691	N/A	Loop Scheme Midpoint Mode	0 = inactive 1 = active	–
911	–	4691	N/A	Loop Scheme Half LS Vy Direction Selected	0 = inactive 1 = active	–
912	–	4691	N/A	Loop Scheme Half LS Vz Direction Selected	0 = inactive 1 = active	–
913	–	4691	N/A	Loop Scheme Full LS Selected	0 = inactive 1 = active	–
914	–	4701	N/A	Loop Scheme Ready	0 = inactive 1 = active	–
915	–	4691	N/A	Loop Scheme Wait for Reset	0 = inactive 1 = active	–
916	–	4691	N/A	Loop Scheme Status Lockout	0 = inactive 1 = active	–
917	–	4691	N/A	Loop Scheme Dual Voltage Loss	0 = inactive 1 = active	–
918	–	4691	N/A	Loop Scheme Supervision Auto-Trip Close Block	0 = inactive 1 = active	–
919	–	4691	N/A	Loop Scheme LiveLine Action Block	0 = inactive 1 = active	–
920	–	4691	N/A	Loop Scheme Auto Reset Operated	0 = inactive 1 = active	–
921	–	4691	N/A	Loop Scheme Supervision Rev Power Detection	0 = inactive 1 = active	–
922	–	4691	N/A	Loop Scheme Midpoint Fwd Power Detection	0 = inactive 1 = active	–
923	–	4691	N/A	Loop Scheme Tie Directional Power Detection	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
924	–	2085	N/A	SGI Pickup Status bit	0 = inactive 1 = active	–
925	–	2086	N/A	SGI Timeout Status bit	0 = inactive 1 = active	–
926	–	2085	N/A	GlobalLOGIC#1 pickup status bit	0 = inactive 1 = active	–
927	–	2086	N/A	GlobalLOGIC#1 timeout status bit	0 = inactive 1 = active	–
928	–	2085	N/A	GlobalLOGIC#2 pickup status bit	0 = inactive 1 = active	–
929	–	2086	N/A	GlobalLOGIC#2 timeout status bit	0 = inactive 1 = active	–
930	–	2085	N/A	Global LOGIC#3 pickup status bit	0 = inactive 1 = active	–
931	–	2086	N/A	GlobalLOGIC#3 timeout status bit	0 = inactive 1 = active	–
932	–	2085	N/A	Global LOGIC#4 pickup status bit	0 = inactive 1 = active	–
933	–	2086	N/A	GlobalLOGIC#4 timeout status bit	0 = inactive 1 = active	–
934	–	2085	N/A	Global LOGIC#5 pickup status bit	0 = inactive 1 = active	–
935	–	2086	N/A	GlobalLOGIC#5 timeout status bit	0 = inactive 1 = active	–
936	–	2085	N/A	Global LOGIC#6 pickup status bit	0 = inactive 1 = active	–
937	–	2086	N/A	GlobalLOGIC#6 timeout status bit	0 = inactive 1 = active	–
938	–	2085	N/A	Global LOGIC#7 pickup status bit	0 = inactive 1 = active	–
939	–	2086	N/A	GlobalLOGIC#7 timeout status bit	0 = inactive 1 = active	–
940	–	2085	N/A	Global LOGIC#8 pickup status bit	0 = inactive 1 = active	–
941	–	2086	N/A	GlobalLOGIC#8 timeout status bit	0 = inactive 1 = active	–
942	–	2089	N/A	Global LOGIC#1 timeout target status bit	0 = inactive 1 = active	–
943	–	2089	N/A	GlobalLOGIC#2 timeout target status bit	0 = inactive 1 = active	–
944	–	2089	N/A	GlobalLOGIC#3 timeout target status bit	0 = inactive 1 = active	–
945	–	2089	N/A	GlobalLOGIC#4 timeout target status bit	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
946	–	2089	N/A	GlobalLOGIC#5 timeout target status bit	0 = inactive 1 = active	–
947	–	2089	N/A	GlobalLOGIC#6 timeout target status bit	0 = inactive 1 = active	–
948	–	2089	N/A	GlobalLOGIC#7 timeout target status bit	0 = inactive 1 = active	–
949	1	2089	N/A	GlobalLOGIC#8 timeout target status bit	0 = inactive 1 = active	–
950	1	46138	N/A	Sectionalizer SCM Enabled	0 = inactive 1 = active	–
951	1	46154	N/A	Sectionalizer Ph A SCM Enabled	0 = inactive 1 = active	–
952	1	46170	N/A	Sectionalizer Ph B SCM Enabled	0 = inactive 1 = active	–
953	1	46186	N/A	Sectionalizer Ph C SCM Enabled	0 = inactive 1 = active	–
954	–	145	N/A	Trip Mode Status	0 = inactive 1 = active	–
955	–	146	N/A	Lockout Mode Status	0 = inactive 1 = active	–
956	–	30922	N/A	G/N Trip 3T3LO Only Status	0 = inactive 1 = active	–
957	–	30922	N/A	F79 Trip Mode Active	0 = inactive 1 = active	–
958	–	33047	N/A	F79 Lock Mode Active	0 = inactive 1 = active	–
959	–	150	N/A	79 Global Operation Mode Status	0 = inactive 1 = active	–
960	–	4771	N/A	Phase A OC Trip Bit0	0 = inactive 1 = active	–
961	–	4771	N/A	Phase B OC Trip Bit1	0 = inactive 1 = active	–
962	–	4771	N/A	Phase C OC Trip Bit2	0 = inactive 1 = active	–
963	–	4771	N/A	G/GS OC Trip Bit3	0 = inactive 1 = active	–
964	–	4771	N/A	N OC Trip Bit4	0 = inactive 1 = active	–
965	–	4802	N/A	Fault Latch	0 = inactive 1 = active	–
966	–	3917	N/A	Reclose Disable	0 = inactive 1 = active	–
967	–	46808	N/A	F46BC Enabled/Disabled	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
968	–	4864	N/A	Virtual Input 1 Status	0 = inactive 1 = active	–
969	–	4864	N/A	Virtual Input 2 Status	0 = inactive 1 = active	–
970	–	4864	N/A	Virtual Input 3 Status	0 = inactive 1 = active	–
971	–	4865	N/A	Virtual Input 4 Status	0 = inactive 1 = active	–
972	–	4865	N/A	Virtual Input 5 Status	0 = inactive 1 = active	–
973	–	4865	N/A	Virtual Input 6 Status	0 = inactive 1 = active	–
974	–	4865	N/A	Virtual Input 7 Status	0 = inactive 1 = active	–
975	–	4865	N/A	Virtual Input 8 Status	0 = inactive 1 = active	–
976	–	4865	N/A	Virtual Input 9 Status	0 = inactive 1 = active	–
977	–	4865	N/A	Virtual Input 10 Status	0 = inactive 1 = active	–
978	–	4865	N/A	Virtual Input 11 Status	0 = inactive 1 = active	–
979	–	4865	N/A	Virtual Input 12 Status	0 = inactive 1 = active	–
980	–	4865	N/A	Virtual Input 13 Status	0 = inactive 1 = active	–
981	–	4865	N/A	Virtual Input 14 Status	0 = inactive 1 = active	–
982	–	4865	N/A	Virtual Input 15 Status	0 = inactive 1 = active	–
983	–	4865	N/A	Virtual Input 16 Status	0 = inactive 1 = active	–
984	–	4865	N/A	Virtual Input 17 Status	0 = inactive 1 = active	–
985	–	4865	N/A	Virtual Input 18 Status	0 = inactive 1 = active	–
986	–	4865	N/A	Virtual Input 19 Status	0 = inactive 1 = active	–
987	–	4868	N/A	FCLP Block	0 = inactive 1 = active	–
988	–	4868	N/A	FCLP Reset	0 = inactive 1 = active	–
989	–	4868	N/A	FCLP Initiated	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
990	–	4868	N/A	FCLP 79 Lockout	0 = inactive 1 = active	–
991	–	4869	N/A	FCLP Arm Pickup	0 = inactive 1 = active	–
992	–	4869	N/A	FCLP Arm Timeout	0 = inactive 1 = active	–
993	–	4869	N/A	FCLP Initiate Action Pickup	0 = inactive 1 = active	–
994	–	4869	N/A	FCLP Initiate Action Timeout	0 = inactive 1 = active	–
995	–	4869	N/A	FCLP Low current Phase Pickup	0 = inactive 1 = active	–
996	–	4869	N/A	FCLP Low current Phase Timeout	0 = inactive 1 = active	–
997	–	4869	N/A	FCLP Low current Ground Pickup	0 = inactive 1 = active	–
998	–	4869	N/A	FCLP Low current Ground Timeout	0 = inactive 1 = active	–
999	–	4869	N/A	FCLP Override Active Pickup	0 = inactive 1 = active	–
1000	–	4869	N/A	FCLP Override Active Timeout	0 = inactive 1 = active	–
1001	–	4869	N/A	FCLP BMT Pickup	0 = inactive 1 = active	–
1002	–	4869	N/A	FCLP BMT Timeout	0 = inactive 1 = active	–
1003	–	4869	N/A	FCLP Low Current Residual Pickup	0 = inactive 1 = active	–
1004	–	4869	N/A	FCLP Low Current Residual Timeout	0 = inactive 1 = active	–
1005	–	47375	N/A	FCLP PhOverride Set	0 = inactive 1 = active	–
1006	–	47393	N/A	FCLP GndOverride Set	0 = inactive 1 = active	–
1007	–	47399	N/A	FCLP NeuOverride Set	0 = inactive 1 = active	–
1008	–	4891	N/A	FBM #1 percent pickup	0 = inactive 1 = active	–
1009	–	4891	N/A	FBM #1 percent timeout	0 = inactive 1 = active	–
1010	–	4892	N/A	FBM #2 percent pickup	0 = inactive 1 = active	–
1011	–	4892	N/A	FBM #2 percent timeout	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1012	–	7062	N/A	F27 3-Ph #1 Enabled/Disabled	0 = inactive 1 = active	–
1013	–	7068	N/A	F27 3-Ph #2 Enabled/Disabled	0 = inactive 1 = active	–
1014	–	7074	N/A	F27 3-Ph #3 Enabled/Disabled	0 = inactive 1 = active	–
1015	–	7080	N/A	F27 3-Ph #4 Enabled/Disabled	0 = inactive 1 = active	–
1016	–	46352	N/A	F27 3-Ph #5 Enabled/Disabled	0 = inactive 1 = active	–
1017	–	46357	N/A	F27 3-Ph #6 Enabled/Disabled	0 = inactive 1 = active	–
1018	–	46362	N/A	F27 3-Ph #7 Enabled/Disabled	0 = inactive 1 = active	–
1019	–	46367	N/A	F27 3-Ph #8 Enabled/Disabled	0 = inactive 1 = active	–
1020	–	7086	N/A	F27 Ph-A #1 Enabled/Disabled	0 = inactive 1 = active	–
1021	–	7091	N/A	F27 Ph-B #1 Enabled/Disabled	0 = inactive 1 = active	–
1022	–	7096	N/A	F27 Ph-C #1 Enabled/Disabled	0 = inactive 1 = active	–
1023	–	7101	N/A	F27 Ph-A #2 Enabled/Disabled	0 = inactive 1 = active	–
1024	–	7106	N/A	F27 Ph-B #2 Enabled/Disabled	0 = inactive 1 = active	–
1025	–	7111	N/A	F27 Ph-C #2 Enabled/Disabled	0 = inactive 1 = active	–
1026	–	7116	N/A	F27 Ph-A #3 Enabled/Disabled	0 = inactive 1 = active	–
1027	–	7121	N/A	F27 Ph-B #3 Enabled/Disabled	0 = inactive 1 = active	–
1028	–	7126	N/A	F27 Ph-C #3 Enabled/Disabled	0 = inactive 1 = active	–
1029	–	7131	N/A	F27 Ph-A #4 Enabled/Disabled	0 = inactive 1 = active	–
1030	–	7136	N/A	F27 Ph-B #4 Enabled/Disabled	0 = inactive 1 = active	–
1031	–	7141	N/A	F27 Ph-C #4 Enabled/Disabled	0 = inactive 1 = active	–
1032	–	8696	N/A	F27PP 3-Ph Enabled/Disabled	0 = inactive 1 = active	–
1033	–	8702	N/A	F27PP Ph-AB Enabled/Disabled	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1034	–	8707	N/A	F27PP Ph-BC Enabled/Disabled	0 = inactive 1 = active	–
1035	–	8712	N/A	F27PP Ph-CA Enabled/Disabled	0 = inactive 1 = active	–
1036	–	–	N/A	F27BSVS 3- Phase Enabled/Disabled	0 = inactive 1 = active	–
1037	–	–	N/A	F27BSVS Independent Phase Enabled/Disabled	0 = inactive 1 = active	–
1038	–	8002	N/A	F59 3-Ph #1 Enabled/Disabled	0 = inactive 1 = active	–
1039	–	8008	N/A	F59 3-Ph #2 Enabled/Disabled	0 = inactive 1 = active	–
1040	–	8014	N/A	F59 3-Ph #3 Enabled/Disabled	0 = inactive 1 = active	–
1041	–	8020	N/A	F59 3-Ph #4 Enabled/Disabled	0 = inactive 1 = active	–
1042	–	46372	N/A	F59 3-Ph #5 Enabled/Disabled	0 = inactive 1 = active	–
1043	–	46377	N/A	F59 3-Ph #6 Enabled/Disabled	0 = inactive 1 = active	–
1044	–	46382	N/A	F59 3-Ph #7 Enabled/Disabled	0 = inactive 1 = active	–
1045	–	46387	N/A	F59 3-Ph #8 Enabled/Disabled	0 = inactive 1 = active	–
1046	–	8026	N/A	F59 Ph-A #1 Enabled/Disabled	0 = inactive 1 = active	–
1047	–	8031	N/A	F59 Ph-B #1 Enabled/Disabled	0 = inactive 1 = active	–
1048	–	8036	N/A	F59 Ph-C #1 Enabled/Disabled	0 = inactive 1 = active	–
1049	–	8041	N/A	F59 Ph-A #2 Enabled/Disabled	0 = inactive 1 = active	–
1050	–	8046	N/A	F59 Ph-B #2 Enabled/Disabled	0 = inactive 1 = active	–
1051	–	8051	N/A	F59 Ph-C #2 Enabled/Disabled	0 = inactive 1 = active	–
1052	–	8056	N/A	F59 Ph-A #3 Enabled/Disabled	0 = inactive 1 = active	–
1053	–	8061	N/A	F59 Ph-B #3 Enabled/Disabled	0 = inactive 1 = active	–
1054	–	8066	N/A	F59 Ph-C #3 Enabled/Disabled	0 = inactive 1 = active	–
1055	–	8071	N/A	F59 Ph-A #4 Enabled/Disabled	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1056	–	8076	N/A	F59 Ph-B #4 Enabled/Disabled	0 = inactive 1 = active	–
1057	–	8081	N/A	F59 Ph-C #4 Enabled/Disabled	0 = inactive 1 = active	–
1058	–	8744	N/A	F59PP 3-Ph Enabled/Disabled	0 = inactive 1 = active	–
1059	–	8750	N/A	F59PP Ph-A Enabled/Disabled	0 = inactive 1 = active	–
1060	–	8755	N/A	F59PP Ph-B Enabled/Disabled	0 = inactive 1 = active	–
1061	–	8760	N/A	F59PP Ph-C Enabled/Disabled	0 = inactive 1 = active	–
1062	–	8128	N/A	F59N #1 Enabled/Disabled	0 = inactive 1 = active	–
1063	–	8134	N/A	F59N #2 Enabled/Disabled	0 = inactive 1 = active	–
1064	–	8112	N/A	F59Q Enabled/Disabled	0 = inactive 1 = active	–
1065	–	9144	N/A	F59I 3-Ph Enabled/Disabled	0 = inactive 1 = active	–
1066	–	9150	N/A	F59I Ph-A Enabled/Disabled	0 = inactive 1 = active	–
1067	–	9155	N/A	F59I Ph-B Enabled/Disabled	0 = inactive 1 = active	–
1068	–	9160	N/A	F59I Ph-C Enabled/Disabled	0 = inactive 1 = active	–
1069	–	7274	N/A	F50P 3-Ph #1 Enabled/Disabled	0 = inactive 1 = active	–
1070	–	7280	N/A	F50P 3-Ph #2 Enabled/Disabled	0 = inactive 1 = active	–
1071	–	7286	N/A	F50P 3-Ph #3 Enabled/Disabled	0 = inactive 1 = active	–
1072	–	7292	N/A	F50P 3-Ph #4 Enabled/Disabled	0 = inactive 1 = active	–
1073	–	7298	N/A	F50P 3-Ph #5 Enabled/Disabled	0 = inactive 1 = active	–
1074	–	7304	N/A	F50P Ph-A #1 Enabled/Disabled	0 = inactive 1 = active	–
1075	–	7309	N/A	F50P Ph-B #1 Enabled/Disabled	0 = inactive 1 = active	–
1076	–	7314	N/A	F50P Ph-C #1 Enabled/Disabled	0 = inactive 1 = active	–
1077	–	7319	N/A	F50P Ph-A #2 Enabled/Disabled	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1078	–	7323	N/A	F50P Ph-B #2 Enabled/Disabled	0 = inactive 1 = active	–
1079	–	7329	N/A	F50P Ph-C #2 Enabled/Disabled	0 = inactive 1 = active	–
1080	–	7334	N/A	F50P Ph-A #3 Enabled/Disabled	0 = inactive 1 = active	–
1081	–	7399	N/A	F50P Ph-B #3 Enabled/Disabled	0 = inactive 1 = active	–
1082	–	7344	N/A	F50P Ph-C #3 Enabled/Disabled	0 = inactive 1 = active	–
1083	–	7349	N/A	F50P Ph-A #4 Enabled/Disabled	0 = inactive 1 = active	–
1084	–	7354	N/A	F50P Ph-B #4 Enabled/Disabled	0 = inactive 1 = active	–
1085	–	7359	N/A	F50P Ph-C #4 Enabled/Disabled	0 = inactive 1 = active	–
1086	–	7364	N/A	F50P Ph-A #5 Enabled/Disabled	0 = inactive 1 = active	–
1087	–	7369	N/A	F50P Ph-B #5 Enabled/Disabled	0 = inactive 1 = active	–
1088	–	7374	N/A	F50P Ph-C #5 Enabled/Disabled	0 = inactive 1 = active	–
1089	–	7462	N/A	F46DT #1 Enabled/Disabled	0 = inactive 1 = active	–
1090	–	7467	N/A	F46DT #2 Enabled/Disabled	0 = inactive 1 = active	–
1091	–	7472	N/A	F46DT #3 Enabled/Disabled	0 = inactive 1 = active	–
1092	–	7477	N/A	F46DT #4 Enabled/Disabled	0 = inactive 1 = active	–
1093	–	7482	N/A	F46DT #5 Enabled/Disabled	0 = inactive 1 = active	–
1094	–	7426	N/A	F50 Gnd #1 Enabled/Disabled	0 = inactive 1 = active	–
1095	–	7431	N/A	F50 Gnd #2 Enabled/Disabled	0 = inactive 1 = active	–
1096	–	7436	N/A	F50 Gnd #3 Enabled/Disabled	0 = inactive 1 = active	–
1097	–	7441	N/A	F50 Gnd #4 Enabled/Disabled	0 = inactive 1 = active	–
1098	–	7446	N/A	F50 Gnd #5 Enabled/Disabled	0 = inactive 1 = active	–
1099	–	7390	N/A	F50N #1 Enabled/Disabled	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1100	–	7395	N/A	F50N #2 Enabled/Disabled	0 = inactive 1 = active	–
1101	–	7400	N/A	F50N #3 Enabled/Disabled	0 = inactive 1 = active	–
1102	–	7405	N/A	F50N #4 Enabled/Disabled	0 = inactive 1 = active	–
1103	–	7410	N/A	F50N #5 Enabled/Disabled	0 = inactive 1 = active	–
1104	–	8856	N/A	F50 Gnd Sens #1 Enabled/Disabled	0 = inactive 1 = active	–
1105	–	8861	N/A	F50 Gnd Sens #2 Enabled/Disabled	0 = inactive 1 = active	–
1106	–	8866	N/A	F50 Gnd Sens #3 Enabled/Disabled	0 = inactive 1 = active	–
1107	–	8871	N/A	F50 Gnd Sens #4 Enabled/Disabled	0 = inactive 1 = active	–
1108	–	8876	N/A	F50 Gnd Sens #5 Enabled/Disabled	0 = inactive 1 = active	–
1109	–	7498	N/A	F51P 3-Ph #1 Enabled/Disabled	0 = inactive 1 = active	–
1110	–	7512	N/A	F51P 3-Ph #2 Enabled/Disabled	0 = inactive 1 = active	–
1111	–	7526	N/A	F51P 3-Ph #3 Enabled/Disabled	0 = inactive 1 = active	–
1112	–	7540	N/A	F51P 3-Ph #4 Enabled/Disabled	0 = inactive 1 = active	–
1113	–	7554	N/A	F51P 3-Ph #5 Enabled/Disabled	0 = inactive 1 = active	–
1114	–	7568	N/A	F51P Ph-A #1 Enabled/Disabled	0 = inactive 1 = active	–
1115	–	7581	N/A	F51P Ph-B #1 Enabled/Disabled	0 = inactive 1 = active	–
1116	–	7594	N/A	F51P Ph-C #1 Enabled/Disabled	0 = inactive 1 = active	–
1117	–	7607	N/A	F51P Ph-A #2 Enabled/Disabled	0 = inactive 1 = active	–
1118	–	7620	N/A	F51P Ph-B #2 Enabled/Disabled	0 = inactive 1 = active	–
1119	–	7633	N/A	F51P Ph-C #2 Enabled/Disabled	0 = inactive 1 = active	–
1120	–	7646	N/A	F51P Ph-A #3 Enabled/Disabled	0 = inactive 1 = active	–
1121	–	7659	N/A	F51P Ph-B #3 Enabled/Disabled	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1122	–	7672	N/A	F51P Ph-C #3 Enabled/Disabled	0 = inactive 1 = active	–
1123	–	7685	N/A	F51P Ph-A #4 Enabled/Disabled	0 = inactive 1 = active	–
1124	–	7698	N/A	F51P Ph-B #4 Enabled/Disabled	0 = inactive 1 = active	–
1125	–	7711	N/A	F51P Ph-C #4 Enabled/Disabled	0 = inactive 1 = active	–
1126	–	7724	N/A	F51P Ph-A #5 Enabled/Disabled	0 = inactive 1 = active	–
1127	–	7737	N/A	F51P Ph-B #5 Enabled/Disabled	0 = inactive 1 = active	–
1128	–	7750	N/A	F51P Ph-C #5 Enabled/Disabled	0 = inactive 1 = active	–
1129	–	7926	N/A	F46IT #1 Enabled/Disabled	0 = inactive 1 = active	–
1130	–	7939	N/A	F46IT #2 Enabled/Disabled	0 = inactive 1 = active	–
1131	–	7952	N/A	F46IT #3 Enabled/Disabled	0 = inactive 1 = active	–
1132	–	7965	N/A	F46IT #4 Enabled/Disabled	0 = inactive 1 = active	–
1133	–	7978	N/A	F46IT #5 Enabled/Disabled	0 = inactive 1 = active	–
1134	–	7850	N/A	F51 Gnd #1 Enabled/Disabled	0 = inactive 1 = active	–
1135	–	7863	N/A	F51 Gnd #2 Enabled/Disabled	0 = inactive 1 = active	–
1136	–	7876	N/A	F51 Gnd #3 Enabled/Disabled	0 = inactive 1 = active	–
1137	–	7889	N/A	F51 Gnd #4 Enabled/Disabled	0 = inactive 1 = active	–
1138	–	7902	N/A	F51 Gnd #5 Enabled/Disabled	0 = inactive 1 = active	–
1139	–	7774	N/A	F51N #1 Enabled/Disabled	0 = inactive 1 = active	–
1140	–	7787	N/A	F51N #2 Enabled/Disabled	0 = inactive 1 = active	–
1141	–	7800	N/A	F51N #3 Enabled/Disabled	0 = inactive 1 = active	–
1142	–	7813	N/A	F51N #4 Enabled/Disabled	0 = inactive 1 = active	–
1143	–	7826	N/A	F51N #5 Enabled/Disabled	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1144	–	8892	N/A	F51 Gnd Sens #1 Enabled/Disabled	0 = inactive 1 = active	–
1145	–	8905	N/A	F51 Gnd Sens #2 Enabled/Disabled	0 = inactive 1 = active	–
1146	–	8918	N/A	F51 Gnd Sens #3 Enabled/Disabled	0 = inactive 1 = active	–
1147	–	8931	N/A	F51 Gnd Sens #4 Enabled/Disabled	0 = inactive 1 = active	–
1148	–	8944	N/A	F51 Gnd Sens #5 Enabled/Disabled	0 = inactive 1 = active	–
1149	–	8144	N/A	F67 3-Ph #1 Enabled/Disabled	0 = inactive 1 = active	–
1150	–	8153	N/A	F67 3-Ph #2 Enabled/Disabled	0 = inactive 1 = active	–
1151	–	8162	N/A	F67 3-Ph #3 Enabled/Disabled	0 = inactive 1 = active	–
1152	–	8171	N/A	F67 3-Ph #4 Enabled/Disabled	0 = inactive 1 = active	–
1153	–	8180	N/A	F67 3-Ph #5 Enabled/Disabled	0 = inactive 1 = active	–
1154	–	8189	N/A	F67 Ph-A #1 Enabled/Disabled	0 = inactive 1 = active	–
1155	–	8229	N/A	F67 Ph-B #1 Enabled/Disabled	0 = inactive 1 = active	–
1156	–	8269	N/A	F67 Ph-C #1 Enabled/Disabled	0 = inactive 1 = active	–
1157	–	8197	N/A	F67 Ph-A #2 Enabled/Disabled	0 = inactive 1 = active	–
1158	–	8237	N/A	F67 Ph-B #2 Enabled/Disabled	0 = inactive 1 = active	–
1159	–	8277	N/A	F67 Ph-C #2 Enabled/Disabled	0 = inactive 1 = active	–
1160	–	8205	N/A	F67 Ph-A #3 Enabled/Disabled	0 = inactive 1 = active	–
1161	–	8245	N/A	F67 Ph-B #3 Enabled/Disabled	0 = inactive 1 = active	–
1162	–	8285	N/A	F67 Ph-C #3 Enabled/Disabled	0 = inactive 1 = active	–
1163	–	8213	N/A	F67 Ph-A #4 Enabled/Disabled	0 = inactive 1 = active	–
1164	–	8253	N/A	F67 Ph-B #4 Enabled/Disabled	0 = inactive 1 = active	–
1165	–	8293	N/A	F67 Ph-C #4 Enabled/Disabled	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1166	–	8221	N/A	F67 Ph-A #5 Enabled/Disabled	0 = inactive 1 = active	–
1167	–	8261	N/A	F67 Ph-B #5 Enabled/Disabled	0 = inactive 1 = active	–
1168	–	8301	N/A	F67 Ph-C #5 Enabled/Disabled	0 = inactive 1 = active	–
1169	–	8420	N/A	F67Q #1 Enabled/Disabled	0 = inactive 1 = active	–
1170	–	8428	N/A	F67Q #2 Enabled/Disabled	0 = inactive 1 = active	–
1171	–	8436	N/A	F67Q #3 Enabled/Disabled	0 = inactive 1 = active	–
1172	–	8444	N/A	F67Q #4 Enabled/Disabled	0 = inactive 1 = active	–
1173	–	8452	N/A	F67Q #5 Enabled/Disabled	0 = inactive 1 = active	–
1174	–	8370	N/A	F67 Gnd #1 Enabled/Disabled	0 = inactive 1 = active	–
1175	–	8378	N/A	F67 Gnd #2 Enabled/Disabled	0 = inactive 1 = active	–
1176	–	8386	N/A	F67 Gnd #3 Enabled/Disabled	0 = inactive 1 = active	–
1177	–	8394	N/A	F67 Gnd #4 Enabled/Disabled	0 = inactive 1 = active	–
1178	–	8402	N/A	F67 Gnd #5 Enabled/Disabled	0 = inactive 1 = active	–
1179	–	8320	N/A	F67N #1 Enabled/Disabled	0 = inactive 1 = active	–
1180	–	8382	N/A	F67N #2 Enabled/Disabled	0 = inactive 1 = active	–
1181	–	8336	N/A	F67N #3 Enabled/Disabled	0 = inactive 1 = active	–
1182	–	8344	N/A	F67N #4 Enabled/Disabled	0 = inactive 1 = active	–
1183	–	8352	N/A	F67N #5 Enabled/Disabled	0 = inactive 1 = active	–
1184	–	8968	N/A	F67 Gnd Sens #1 Enabled/Disabled	0 = inactive 1 = active	–
1185	–	8976	N/A	F67 Gnd Sens #2 Enabled/Disabled	0 = inactive 1 = active	–
1186	–	8984	N/A	F67 Gnd Sens #3 Enabled/Disabled	0 = inactive 1 = active	–
1187	–	8992	N/A	F67 Gnd Sens #4 Enabled/Disabled	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1188	–	9000	N/A	F67 Gnd Sens #5 Enabled/Disabled	0 = inactive 1 = active	–
1189	–	7022	N/A	F25 Enabled/Disabled	0 = inactive 1 = active	–
1190	–	7152	N/A	F32 3-Ph #1 Enabled/Disabled	0 = inactive 1 = active	–
1191	–	7160	N/A	F32 3-Ph #2 Enabled/Disabled	0 = inactive 1 = active	–
1192	–	7168	N/A	F32 3-Ph #3 Enabled/Disabled	0 = inactive 1 = active	–
1193	–	7176	N/A	F32 3-Ph #4 Enabled/Disabled	0 = inactive 1 = active	–
1194	–	7184	N/A	F32 Ph-A #1 Enabled/Disabled	0 = inactive 1 = active	–
1195	–	7191	N/A	F32 Ph-B #1 Enabled/Disabled	0 = inactive 1 = active	–
1196	–	7198	N/A	F32 Ph-C #1 Enabled/Disabled	0 = inactive 1 = active	–
1197	–	7205	N/A	F32 Ph-A #2 Enabled/Disabled	0 = inactive 1 = active	–
1198	–	7212	N/A	F32 Ph-B #2 Enabled/Disabled	0 = inactive 1 = active	–
1199	–	7219	N/A	F32 Ph-C #2 Enabled/Disabled	0 = inactive 1 = active	–
1200	–	7226	N/A	F32 Ph-A #3 Enabled/Disabled	0 = inactive 1 = active	–
1201	–	7233	N/A	F32 Ph-B #3 Enabled/Disabled	0 = inactive 1 = active	–
1202	–	7240	N/A	F32 Ph-C #3 Enabled/Disabled	0 = inactive 1 = active	–
1203	–	7247	N/A	F32 Ph-A #4 Enabled/Disabled	0 = inactive 1 = active	–
1204	–	7254	N/A	F32 Ph-B #4 Enabled/Disabled	0 = inactive 1 = active	–
1205	–	7261	N/A	F32 Ph-C #4 Enabled/Disabled	0 = inactive 1 = active	–
1206	–	8814	N/A	F50BF Enabled/Disabled	0 = inactive 1 = active	–
1207	–	8564	N/A	F81#1 Enabled/Disabled	0 = inactive 1 = active	–
1208	–	8573	N/A	F81#2 Enabled/Disabled	0 = inactive 1 = active	–
1209	–	8582	N/A	F81#3 Enabled/Disabled	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1210	–	8591	N/A	F81#4 Enabled/Disabled	0 = inactive 1 = active	–
1211	–	46712	N/A	F81#5 Enabled/Disabled	0 = inactive 1 = active	–
1212	–	46721	N/A	F81#6 Enabled/Disabled	0 = inactive 1 = active	–
1213	–	46730	N/A	F81#7 Enabled/Disabled	0 = inactive 1 = active	–
1214	–	46739	N/A	F81#8 Enabled/Disabled	0 = inactive 1 = active	–
1215	–	8674	N/A	F81R#1 Enabled/Disabled	0 = inactive 1 = active	–
1216	–	8683	N/A	F81R#2 Enabled/Disabled	0 = inactive 1 = active	–
1217	–	46748	N/A	F81R#3 Enabled/Disabled	0 = inactive 1 = active	–
1218	–	46758	N/A	F81R#4 Enabled/Disabled	0 = inactive 1 = active	–
1219	–	46768	N/A	F81R#5 Enabled/Disabled	0 = inactive 1 = active	–
1220	–	46778	N/A	F81R#6 Enabled/Disabled	0 = inactive 1 = active	–
1221	–	46788	N/A	F81R#7 Enabled/Disabled	0 = inactive 1 = active	–
1222	–	46798	N/A	F81R#8 Enabled/Disabled	0 = inactive 1 = active	–
1223	–	8608	N/A	FCLP (Ind and 3-Ph) Enabled/Disabled	0 = inactive 1 = active	–
1224	–	8774	N/A	F50P in HLT Enabled/Disabled	0 = inactive 1 = active	–
1225	–	8777	N/A	F50G in HLT Enabled/Disabled	0 = inactive 1 = active	–
1226	–	8780	N/A	F51P in HLT Enabled/Disabled	0 = inactive 1 = active	–
1227	–	8791	N/A	F51G in HLT Enabled/Disabled	0 = inactive 1 = active	–
1228	–	8804	N/A	F50N in HLT Enabled/Disabled	0 = inactive 1 = active	–
1229	–	8807	N/A	F51N in HLT Enabled/Disabled	0 = inactive 1 = active	–
1230	–	8832	N/A	Autorestitution Freq loadshed restore limits Enabled/Disabled	0 = inactive 1 = active	–
1231	–	8832	N/A	Autorestitution Volt loadshed restore limits Enabled/Disabled	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1232	–	8840	N/A	Autorestation Volt and Freq restore limits Enabled/Disabled	0 = inactive 1 = active	–
1233	–	8840	N/A	Autorestation 3-Ph/1-Ph mode in Vol restore Enabled/Disabled	0 = inactive 1 = active	–
1234	–	9102	N/A	IPSLLogic #1 Enabled/Disabled	0 = inactive 1 = active	–
1235	–	9106	N/A	IPSLLogic #2 Enabled/Disabled	0 = inactive 1 = active	–
1236	–	9110	N/A	IPSLLogic #3 Enabled/Disabled	0 = inactive 1 = active	–
1237	–	9114	N/A	IPSLLogic #4 Enabled/Disabled	0 = inactive 1 = active	–
1238	–	9118	N/A	IPSLLogic #5 Enabled/Disabled	0 = inactive 1 = active	–
1239	–	9122	N/A	IPSLLogic #6 Enabled/Disabled	0 = inactive 1 = active	–
1240	–	9126	N/A	IPSLLogic #7 Enabled/Disabled	0 = inactive 1 = active	–
1241	–	9130	N/A	IPSLLogic #8 Enabled/Disabled	0 = inactive 1 = active	–
1242	–	9174	N/A	LEL Enabled/Disabled	0 = inactive 1 = active	–
1243	–	46045	N/A	Breaker Monitor #1 Enabled/Disabled	0 = inactive 1 = active	–
1244	–	47472	N/A	Breaker Monitor #2 Enabled/Disabled	0 = inactive 1 = active	–
1245	–	8469	N/A	79 Recloser Relay 3-Ph Enabled/Disabled	0 = inactive 1 = active	–
1246	–	30924	N/A	79 Recloser Relay Independent Ph Enabled/Disabled	0 = inactive 1 = active	–
1247	–	46136	N/A	Sectionalizer 3-Ph Enabled/Disabled	0 = inactive 1 = active	–
1248	–	46152	N/A	Sectionalizer Independent Ph Enabled/Disabled	0 = inactive 1 = active	–
1249	–	46319	N/A	Loop Scheme Enabled/Disabled	0 = inactive 1 = active	–
1250	–	25457	N/A	PSM Enabled/Disabled	0 = inactive 1 = active	–
1251	–	6507	N/A	Battery Charger Enabled/Disabled	0 = inactive 1 = active	–
1252	–	25432	N/A	TCM Enabled/Disabled	0 = inactive 1 = active	–
1253	–	25437	N/A	CCM Enabled/Disabled	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1254	–	25487	N/A	60FL Enabled/Disabled	0 = inactive 1 = active	–
1255	–	28060	N/A	Close Block 3-Ph Enabled/Disabled	0 = inactive 1 = active	–
1256	–	28069	N/A	Close Block Ph-A Enabled/Disabled	0 = inactive 1 = active	–
1257	–	28078	N/A	Close Block Ph-B Enabled/Disabled	0 = inactive 1 = active	–
1258	–	28087	N/A	Close Block Ph-C Enabled/Disabled	0 = inactive 1 = active	–
1259	–	65002	N/A	GlobalLogic#1 Enabled/Disabled	0 = inactive 1 = active	–
1260	–	65011	N/A	GlobalLogic#2 Enabled/Disabled	0 = inactive 1 = active	–
1261	–	65020	N/A	GlobalLogic#3 Enabled/Disabled	0 = inactive 1 = active	–
1262	–	65029	N/A	GlobalLogic#4 Enabled/Disabled	0 = inactive 1 = active	–
1263	–	65038	N/A	GlobalLogic#5 Enabled/Disabled	0 = inactive 1 = active	–
1264	–	65047	N/A	GlobalLogic#6 Enabled/Disabled	0 = inactive 1 = active	–
1265	–	65056	N/A	GlobalLogic#7 Enabled/Disabled	0 = inactive 1 = active	–
1266	–	65065	N/A	GlobalLogic#8 Enabled/Disabled	0 = inactive 1 = active	–
1267	–	29052	N/A	SGI Enabled/Disabled	0 = inactive 1 = active	–
1268	–	28002	N/A	THD #1 3-Ph Enabled/Disabled	0 = inactive 1 = active	–
1269	–	28009	N/A	THD #2 3-Ph Enabled/Disabled	0 = inactive 1 = active	–
1270	–	28016	N/A	THD #1 Ph-A Enabled/Disabled	0 = inactive 1 = active	–
1271	–	28022	N/A	THD #1 Ph-B Enabled/Disabled	0 = inactive 1 = active	–
1272	–	28028	N/A	THD #1 Ph-C Enabled/Disabled	0 = inactive 1 = active	–
1273	–	28034	N/A	THD #2 Ph-A Enabled/Disabled	0 = inactive 1 = active	–
1274	–	28040	N/A	THD #2 Ph-B Enabled/Disabled	0 = inactive 1 = active	–
1275	–	28046	N/A	THD #2 Ph-C Enabled/Disabled	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1276	–	4893	N/A	SD Card port Active	0 = inactive 1 = active	–
1277	–	4893	N/A	USB Front Port active	–	–
1278	–	4893	N/A	Port 1 Serial Active	–	–
1279	–	4893	N/A	Port 2 Ethernet Active	–	–
1280	–	4893	N/A	Port 3 Ethernet Active	–	–
1281	–	4893	N/A	Port 4 Serial Active	–	–
1282	–	62	N/A	Source Orientation Vy	0 = inactive 1 = active	–
1283	–	62	N/A	Source Orientation Vz	0 = inactive 1 = active	–
1284	–	1410	N/A	Remote Input 1	0 = inactive 1 = active	–
1285	–	1410	N/A	Remote Input 2	0 = inactive 1 = active	–
1286	–	1410	N/A	Remote Input 3	0 = inactive 1 = active	–
1287	–	1410	N/A	Remote Input 4	0 = inactive 1 = active	–
1288	–	1410	N/A	Remote Input 5	0 = inactive 1 = active	–
1289	–	1410	N/A	Remote Input 6	0 = inactive 1 = active	–
1290	–	1410	N/A	Remote Input 7	0 = inactive 1 = active	–
1291	–	1410	N/A	Remote Input 8	0 = inactive 1 = active	–
1292	–	1410	N/A	Remote Input 9	0 = inactive 1 = active	–
1293	–	1410	N/A	Remote Input 10	0 = inactive 1 = active	–
1294	–	1410	N/A	Remote Input 11	0 = inactive 1 = active	–
1295	–	1410	N/A	Remote Input 12	0 = inactive 1 = active	–
1296	–	1410	N/A	Remote Input 13	0 = inactive 1 = active	–
1297	–	1410	N/A	Remote Input 14	0 = inactive 1 = active	–
1298	–	1410	N/A	Remote Input 15	0 = inactive 1 = active	–
1299	–	1410	N/A	Remote Input 16	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1300	–	1422	N/A	Remote Input 17	0 = inactive 1 = active	–
1301	–	1422	N/A	Remote Input 18	0 = inactive 1 = active	–
1302	–	1422	N/A	Remote Input 19	0 = inactive 1 = active	–
1303	–	1422	N/A	Remote Input 20	0 = inactive 1 = active	–
1304	–	1422	N/A	Remote Input 21	0 = inactive 1 = active	–
1305	–	1422	N/A	Remote Input 22	0 = inactive 1 = active	–
1306	–	1422	N/A	Remote Input 23	0 = inactive 1 = active	–
1307	–	1422	N/A	Remote Input 24	0 = inactive 1 = active	–
1308	–	1422	N/A	Remote Input 25	0 = inactive 1 = active	–
1309	–	1422	N/A	Remote Input 26	0 = inactive 1 = active	–
1310	–	1422	N/A	Remote Input 27	0 = inactive 1 = active	–
1311	–	1422	N/A	Remote Input 28	0 = inactive 1 = active	–
1312	–	1422	N/A	Remote Input 29	0 = inactive 1 = active	–
1313	–	1422	N/A	Remote Input 30	0 = inactive 1 = active	–
1314	–	1422	N/A	Remote Input 31	0 = inactive 1 = active	–
1315	–	1422	N/A	Remote Input 32	0 = inactive 1 = active	–
1316	–	1423	N/A	Remote Input 33	0 = inactive 1 = active	–
1317	–	1423	N/A	Remote Input 34	0 = inactive 1 = active	–
1318	–	1423	N/A	Remote Input 35	0 = inactive 1 = active	–
1319	–	1423	N/A	Remote Input 36	0 = inactive 1 = active	–
1320	–	1423	N/A	Remote Input 37	0 = inactive 1 = active	–
1321	–	1423	N/A	Remote Input 38	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1322	–	1423	N/A	Remote Input 39	0 = inactive 1 = active	–
1323	–	1423	N/A	Remote Input 40	0 = inactive 1 = active	–
1324	–	1423	N/A	Remote Input 41	0 = inactive 1 = active	–
1325	–	1423	N/A	Remote Input 42	0 = inactive 1 = active	–
1326	–	1423	N/A	Remote Input 43	0 = inactive 1 = active	–
1327	–	1423	N/A	Remote Input 44	0 = inactive 1 = active	–
1328	–	1423	N/A	Remote Input 45	0 = inactive 1 = active	–
1329	–	1423	N/A	Remote Input 46	0 = inactive 1 = active	–
1330	–	1423	N/A	Remote Input 47	0 = inactive 1 = active	–
1331	–	1423	N/A	Remote Input 48	0 = inactive 1 = active	–
1332	–	1424	N/A	Remote Input 49	0 = inactive 1 = active	–
1333	–	1424	N/A	Remote Input 50	0 = inactive 1 = active	–
1334	–	1424	N/A	Remote Input 51	0 = inactive 1 = active	–
1335	–	1424	N/A	Remote Input 52	0 = inactive 1 = active	–
1336	–	1424	N/A	Remote Input 53	0 = inactive 1 = active	–
1337	–	1424	N/A	Remote Input 54	0 = inactive 1 = active	–
1338	–	1424	N/A	Remote Input 55	0 = inactive 1 = active	–
1339	–	1424	N/A	Remote Input 56	0 = inactive 1 = active	–
1340	–	1424	N/A	Remote Input 57	0 = inactive 1 = active	–
1341	–	1424	N/A	Remote Input 58	0 = inactive 1 = active	–
1342	–	1424	N/A	Remote Input 59	0 = inactive 1 = active	–
1343	–	1424	N/A	Remote Input 60	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1344	–	1424	N/A	Remote Input 61	0 = inactive 1 = active	–
1345	–	1424	N/A	Remote Input 62	0 = inactive 1 = active	–
1346	–	1424	N/A	Remote Input 63	0 = inactive 1 = active	–
1347	–	1424	N/A	Remote Input 64	0 = inactive 1 = active	–
1348	–	4893	N/A	Port 1 Serial Comms Lost	–	–
1349	–	4893	N/A	Port 2 Ethernet Comms Lost	–	–
1350	–	4893	N/A	Port 3 Ethernet Comms Lost	–	–
1351	–	4893	N/A	Port 4 Serial Comms Lost	–	–
1352	–	1411	N/A	Phase and Ground Fast Curves Active	0 = inactive 1 = active	–
1353	–	1411	N/A	Phase and Ground Slow Curves Active	0 = inactive 1 = active	–
1354	–	1411	N/A	Phase Fast Curves Only Active	0 = inactive 1 = active	–
1355	–	1411	N/A	Phase Slow Curves Only Active	0 = inactive 1 = active	–
1356	–	1411	N/A	Ground Fast Curves Only Active	0 = inactive 1 = active	–
1357	–	1411	N/A	Ground Slow Curves Only Active	0 = inactive 1 = active	–
1358	–	1411	N/A	Slow Elements Only (TSlow)	0 = inactive 1 = active	–
1359	–	1411	N/A	Fast/Slow Curve Selection Disable Active	0 = inactive 1 = active	–
1360	–	4894	N/A	Contact wear Ph-A (BM function) 95% used	0 = inactive 1 = active	–
1361	–	4894	N/A	Contact wear Ph-B (BM function) 95% used	0 = inactive 1 = active	–
1362	–	4894	N/A	Contact wear Ph-C (BM function) 95% used	0 = inactive 1 = active	–
1363	–	3798	N/A	Battery Charger Test in Progress	–	–
1364	–	3798	N/A	Battery Charger Mode Absorb	–	–
1365	–	3798	N/A	Battery Charger Cap Charged	–	–
1366	–	4991	N/A	Battery Charger Mode Bulk	–	–
1367	–	4991	N/A	Battery Charger Mode Float	–	–
1368	–	4470	N/A	32H Battery Charger On/Off	–	–
1369	–	4901	N/A	32H Internal Door Status	–	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1370	–	4901	N/A	32H External Door Status	–	–
1371	–	4901	N/A	32H Low Capacitor Voltage	–	–
1372	–	4901	N/A	32H High Temperature	–	–
1373	–	4901	N/A	32H Low Battery	–	–
1374	–	4958	N/A	Any F27 Pickup	0 = inactive 1 = active	–
1375	–	4958	N/A	Any F46 Pickup	0 = inactive 1 = active	–
1376	–	4958	N/A	Any F50P Ph-A Pickup	0 = inactive 1 = active	–
1377	–	4958	N/A	Any F50P Ph-B Pickup	0 = inactive 1 = active	–
1378	–	4958	N/A	Any F50P Ph-C Pickup	0 = inactive 1 = active	–
1379	–	4958	N/A	Any F50 Gnd Residual Pickup	0 = inactive 1 = active	–
1380	–	4958	N/A	Any F51P Ph-A Pickup	0 = inactive 1 = active	–
1381	–	4958	N/A	Any F51P Ph-B Pickup	0 = inactive 1 = active	–
1382	–	4958	N/A	Any F51P Ph-C Pickup	0 = inactive 1 = active	–
1383	–	4958	N/A	Any F51 Gnd Residual Pickup	0 = inactive 1 = active	–
1384	–	4958	N/A	Any F59 Pickup	0 = inactive 1 = active	–
1385	–	4958	N/A	Any F81 Pickup	0 = inactive 1 = active	–
1386	–	4958	N/A	Any HLT50P Pickup	0 = inactive 1 = active	–
1387	–	4958	N/A	Any HLT51P Pickup	0 = inactive 1 = active	–
1388	–	4958	N/A	Any HLT Phase Pickup	0 = inactive 1 = active	–
1389	–	4958	N/A	Any HLT Ground Pickup	0 = inactive 1 = active	–
1390	–	4958	N/A	Any HLT Residual Pickup	0 = inactive 1 = active	–
1391	–	4958	N/A	Extra F59 Pickup	0 = inactive 1 = active	–
1392	–	4958	N/A	Any F81R Pickup	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1393	–	4960	N/A	Any F27 Timeout	0 = inactive 1 = active	–
1394	–	4960	N/A	Any F46 Timeout	0 = inactive 1 = active	–
1395	–	4960	N/A	Any F50P Ph-A Timeout	0 = inactive 1 = active	–
1396	–	4960	N/A	Any F50P Ph-B Timeout	0 = inactive 1 = active	–
1397	–	4960	N/A	Any F50P Ph-C Timeout	0 = inactive 1 = active	–
1398	–	4960	N/A	Any F50 Gnd Residual Timeout	0 = inactive 1 = active	–
1499	–	4960	N/A	Any F51P Ph-A Timeout	0 = inactive 1 = active	–
1400	–	4960	N/A	Any F51P Ph-B Timeout	0 = inactive 1 = active	–
1401	–	4960	N/A	Any F51P Ph-C Timeout	0 = inactive 1 = active	–
1402	–	4960	N/A	Any F51 Gnd Residual Timeout	0 = inactive 1 = active	–
1403	–	4960	N/A	Any F59 Timeout	0 = inactive 1 = active	–
1404	–	4960	N/A	Any F81 Timeout	0 = inactive 1 = active	–
1405	–	4960	N/A	Any HLT50P Timeout	0 = inactive 1 = active	–
1406	–	4960	N/A	Any HLT51P Timeout	0 = inactive 1 = active	–
1407	–	4960	N/A	Any HLT Phase Timeout	0 = inactive 1 = active	–
1408	–	4960	N/A	Any HLT Ground Timeout	0 = inactive 1 = active	–
1409	–	4960	N/A	Any HLT Residual Timeout	0 = inactive 1 = active	–
1410	–	4960	N/A	Extra F59 Timeout	0 = inactive 1 = active	–
1411	–	4960	N/A	Any F81R Timeout	0 = inactive 1 = active	–
1412	–	4990	N/A	Power Target #1	0 = inactive 1 = active	–
1413	–	4990	N/A	Power Target #2	0 = inactive 1 = active	–
1414	–	4990	N/A	Power Target #3	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1415	–	4990	N/A	Power Target #4	0 = inactive 1 = active	–
1416	–	4990	N/A	Power Target #5	0 = inactive 1 = active	–
1417	–	4990	N/A	Power Target #6	0 = inactive 1 = active	–
1418	–	4990	N/A	Power Target #7	0 = inactive 1 = active	–
1419	–	4990	N/A	Power Target #8	0 = inactive 1 = active	–
1420	–	4990	N/A	Power Target #9	0 = inactive 1 = active	–
1421	–	4990	N/A	Power Target #10	0 = inactive 1 = active	–
1422	–	4990	N/A	Power Target #11	0 = inactive 1 = active	–
1423	–	4990	N/A	Power Target #12	0 = inactive 1 = active	–
1424	–	1501	N/A	VLogic Output 1 Active Profile	0 = inactive 1 = active	–
1425	–	1501	N/A	VLogic Output 2 Active Profile	0 = inactive 1 = active	–
1426	–	1501	N/A	VLogic Output 3 Active Profile	0 = inactive 1 = active	–
1427	–	1501	N/A	VLogic Output 4 Active Profile	0 = inactive 1 = active	–
1428	–	1501	N/A	VLogic Output 5 Active Profile	0 = inactive 1 = active	–
1429	–	1501	N/A	VLogic Output 6 Active Profile	0 = inactive 1 = active	–
1430	–	1501	N/A	VLogic Output 7 Active Profile	0 = inactive 1 = active	–
1431	–	1501	N/A	VLogic Output 8 Active Profile	0 = inactive 1 = active	–
1432	–	1501	N/A	VLogic Output 9 Active Profile	0 = inactive 1 = active	–
1433	–	1501	N/A	VLogic Output 10 Active Profile	0 = inactive 1 = active	–
1434	–	1501	N/A	VLogic Output 11 Active Profile	0 = inactive 1 = active	–
1435	–	1501	N/A	VLogic Output 12 Active Profile	0 = inactive 1 = active	–
1436	–	1501	N/A	VLogic Output 13 Active Profile	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1437	–	1501	N/A	VLogic Output 14 Active Profile	0 = inactive 1 = active	–
1438	–	1501	N/A	VLogic Output 15 Active Profile	0 = inactive 1 = active	–
1439	–	1501	N/A	VLogic Output 16 Active Profile	0 = inactive 1 = active	–
1440	–	1502	N/A	VLogic Output 17 Active Profile	0 = inactive 1 = active	–
1441	–	1502	N/A	VLogic Output 18 Active Profile	0 = inactive 1 = active	–
1442	–	1502	N/A	VLogic Output 19 Active Profile	0 = inactive 1 = active	–
1443	–	1502	N/A	VLogic Output 20 Active Profile	0 = inactive 1 = active	–
1444	–	1502	N/A	VLogic Output 21 Active Profile	0 = inactive 1 = active	–
1445	–	1502	N/A	VLogic Output 22 Active Profile	0 = inactive 1 = active	–
1446	–	1502	N/A	VLogic Output 23 Active Profile	0 = inactive 1 = active	–
1447	–	1502	N/A	VLogic Output 24 Active Profile	0 = inactive 1 = active	–
1448	–	1502	N/A	VLogic Output 25 Active Profile	0 = inactive 1 = active	–
1449	–	1502	N/A	VLogic Output 26 Active Profile	0 = inactive 1 = active	–
1450	–	1502	N/A	VLogic Output 27 Active Profile	0 = inactive 1 = active	–
1451	–	1502	N/A	VLogic Output 28 Active Profile	0 = inactive 1 = active	–
1452	–	1502	N/A	VLogic Output 29 Active Profile	0 = inactive 1 = active	–
1453	–	1502	N/A	VLogic Output 30 Active Profile	0 = inactive 1 = active	–
1454	–	1502	N/A	VLogic Output 31 Active Profile	0 = inactive 1 = active	–
1455	–	1502	N/A	VLogic Output 32 Active Profile	0 = inactive 1 = active	–
1456	–	1503	N/A	VLogic Output 1 Common Profile	0 = inactive 1 = active	–
1457	–	1503	N/A	VLogic Output 2 Common Profile	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1458	–	1503	N/A	VLogic Output 3 Common Profile	0 = inactive 1 = active	–
1459	–	1503	N/A	VLogic Output 4 Common Profile	0 = inactive 1 = active	–
1460	–	1503	N/A	VLogic Output 5 Common Profile	0 = inactive 1 = active	–
1461	–	1503	N/A	VLogic Output 6 Common Profile	0 = inactive 1 = active	–
1462	–	1503	N/A	VLogic Output 7 Common Profile	0 = inactive 1 = active	–
1463	–	1503	N/A	VLogic Output 8 Common Profile	0 = inactive 1 = active	–
1464	–	1503	N/A	VLogic Output 9 Common Profile	0 = inactive 1 = active	–
1465	–	1503	N/A	VLogic Output 10 Common Profile	0 = inactive 1 = active	–
1466	–	1503	N/A	VLogic Output 11 Common Profile	0 = inactive 1 = active	–
1467	–	1503	N/A	VLogic Output 12 Common Profile	0 = inactive 1 = active	–
1468	–	1503	N/A	VLogic Output 13 Common Profile	0 = inactive 1 = active	–
1469	–	1503	N/A	VLogic Output 14 Common Profile	0 = inactive 1 = active	–
1470	–	1503	N/A	VLogic Output 15 Common Profile	0 = inactive 1 = active	–
1471	–	1503	N/A	VLogic Output 16 Common Profile	0 = inactive 1 = active	–
1472	–	1504	N/A	VLogic Output 17 Common Profile	0 = inactive 1 = active	–
1473	–	1504	N/A	VLogic Output 18 Common Profile	0 = inactive 1 = active	–
1474	–	1504	N/A	VLogic Output 19 Common Profile	0 = inactive 1 = active	–
1475	–	1504	N/A	VLogic Output 20 Common Profile	0 = inactive 1 = active	–
1476	–	1504	N/A	VLogic Output 21 Common Profile	0 = inactive 1 = active	–
1477	–	1504	N/A	VLogic Output 22 Common Profile	0 = inactive 1 = active	–
1478	–	1504	N/A	VLogic Output 23 Common Profile	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1479	–	1504	N/A	VLogic Output 24 Common Profile	0 = inactive 1 = active	–
1480	–	1504	N/A	VLogic Output 25 Common Profile	0 = inactive 1 = active	–
1481	–	1504	N/A	VLogic Output 26 Common Profile	0 = inactive 1 = active	–
1482	–	1504	N/A	VLogic Output 27 Common Profile	0 = inactive 1 = active	–
1483	–	1504	N/A	VLogic Output 28 Common Profile	0 = inactive 1 = active	–
1484	–	1504	N/A	VLogic Output 29 Common Profile	0 = inactive 1 = active	–
1485	–	1504	N/A	VLogic Output 30 Common Profile	0 = inactive 1 = active	–
1486	–	1504	N/A	VLogic Output 31 Common Profile	0 = inactive 1 = active	–
1487	–	1504	N/A	VLogic Output 32 Common Profile	0 = inactive 1 = active	–
1488	–	46150	N/A	One Shot Sectionalizing 3-Ph Enabled/Disabled	0 = inactive 1 = active	–
1489	–	46166	N/A	One Shot Sectionalizing Ph-A Enabled/Disabled	0 = inactive 1 = active	–
1490	–	46182	N/A	One Shot Sectionalizing Ph-B Enabled/Disabled	0 = inactive 1 = active	–
1491	–	46198	N/A	One Shot Sectionalizing Ph-C Enabled/Disabled	0 = inactive 1 = active	–
1492	–	3794	N/A	Control Alarm Max Breaker Operations	0 = inactive 1 = active	–
1493	–	6700	N/A	Gas Pressure Alarm	0 = inactive 1 = active	–
1494	–	6700	N/A	Trip/Close Error Alarm	0 = inactive 1 = active	–
1495	–	3917	N/A	79 Reclosing Blocked	0 = inactive 1 = active	–
1496	–	4473	N/A	Any Phase Protection Function Block	0 = inactive 1 = active	–
1497	–	4473	N/A	Any OverCurrent Block	0 = inactive 1 = active	–
1498	–	4473	N/A	Any Phase OverCurrent Block	0 = inactive 1 = active	–
1499	–	4473	N/A	Any Ground Block	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1500	–	4473	N/A	Any Residual Block	0 = inactive 1 = active	–
1501	–	4473	N/A	Any Protection Function Block	0 = inactive 1 = active	–
1502	–	4473	N/A	Any Overvoltage Function Block	0 = inactive 1 = active	–
1503	–	4473	N/A	Any Undervoltage Function Block	0 = inactive 1 = active	–
1504	–	4441	N/A	Block F46BC	0 = inactive 1 = active	–
1505	–	3942	N/A	Block CLP	0 = inactive 1 = active	–
1506	–	4958	N/A	Any F50P Pickup	0 = inactive 1 = active	–
1507	–	4960	N/A	Any F50P Timeout	0 = inactive 1 = active	–
1508	–	2256	N/A	F50P#1 Phase Pickup	0 = inactive 1 = active	–
1509	–	2266	N/A	F50P#1 Phase Timeout	0 = inactive 1 = active	–
1510	–	2257	N/A	F50P#2 Phase Pickup	0 = inactive 1 = active	–
1511	–	2267	N/A	F50P#2 Phase Timeout	0 = inactive 1 = active	–
1512	–	2257	N/A	F50P#3 Phase Pickup	0 = inactive 1 = active	–
1513	–	2267	N/A	F50P#3 Phase Timeout	0 = inactive 1 = active	–
1514	–	2257	N/A	F50P#4 Phase Pickup	0 = inactive 1 = active	–
1515	–	2267	N/A	F50P#4 Phase Timeout	0 = inactive 1 = active	–
1516	–	2257	N/A	F50P#5 Phase Pickup	0 = inactive 1 = active	–
1517	–	2267	N/A	F50P#5 Phase Timeout	0 = inactive 1 = active	–
1518	–	4958	N/A	Any F50N Pickup	0 = inactive 1 = active	–
1519	–	4960	N/A	Any F50N Timeout	0 = inactive 1 = active	–
1520	–	2258	N/A	Any F50G/GS Pickup	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1521	–	2268	N/A	Any F50G/GS Timeout	0 = inactive 1 = active	–
1522	–	4958	N/A	Any F51P Pickup	0 = inactive 1 = active	–
1523	–	4960	N/A	Any F51P Timeout	0 = inactive 1 = active	–
1524	–	2258	N/A	F51P#1 Phase Pickup	0 = inactive 1 = active	–
1525	–	2268	N/A	F51P#1 Phase Timeout	0 = inactive 1 = active	–
1526	–	4958	N/A	F51P#2 Phase Pickup	0 = inactive 1 = active	–
1527	–	4960	N/A	F51P#2 Phase Timeout	0 = inactive 1 = active	–
1528	–	2259	N/A	F51P#3 Phase Pickup	0 = inactive 1 = active	–
1529	–	2269	N/A	F51P#3 Phase Timeout	0 = inactive 1 = active	–
1530	–	2259	N/A	F51P#4 Phase Pickup	0 = inactive 1 = active	–
1531	–	2269	N/A	F51P#4 Phase Timeout	0 = inactive 1 = active	–
1532	–	2259	N/A	F51P#5 Phase Pickup	0 = inactive 1 = active	–
1533	–	2269	N/A	F51P#5 Phase Timeout	0 = inactive 1 = active	–
1534	–	2259	N/A	Any F51N Pickup	0 = inactive 1 = active	–
1535	–	2269	N/A	Any F51N Timeout	0 = inactive 1 = active	–
1536	–	2260	N/A	Any F51G/GS Pickup	0 = inactive 1 = active	–
1537	–	2270	N/A	Any F51G/GS Timeout	0 = inactive 1 = active	–
1538	–	4958	N/A	Any F67P Pickup	0 = inactive 1 = active	–
1539	–	4960	N/A	Any F67P Timeout	0 = inactive 1 = active	–
1540	–	2261	N/A	F67P#1 Phase Pickup	0 = inactive 1 = active	–
1541	–	2271	N/A	F67P#1 Phase Timeout	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1542	–	4958	N/A	F67P#2 Phase Pickup	0 = inactive 1 = active	–
1543	–	4960	N/A	F67P#2 Phase Timeout	0 = inactive 1 = active	–
1544	–	2262	N/A	F67P#3 Phase Pickup	0 = inactive 1 = active	–
1545	–	2272	N/A	F67P#3 Phase Timeout	0 = inactive 1 = active	–
1546	–	2262	N/A	F67P#4 Phase Pickup	0 = inactive 1 = active	–
1547	–	2272	N/A	F67P#4 Phase Timeout	0 = inactive 1 = active	–
1548	–	2262	N/A	F67P#5 Phase Pickup	0 = inactive 1 = active	–
1549	–	2272	N/A	F67P#5 Phase Timeout	0 = inactive 1 = active	–
1550	–	2262	N/A	Any F67N Pickup	0 = inactive 1 = active	–
1551	–	2272	N/A	Any F67N Timeout	0 = inactive 1 = active	–
1552	–	2263	N/A	Any F67G/GS Pickup	0 = inactive 1 = active	–
1553	–	2273	N/A	Any F67G/GS Timeout	0 = inactive 1 = active	–
1554	–	2263	N/A	Any F67Q Pickup	0 = inactive 1 = active	–
1555	–	2273	N/A	Any F67Q Timeout	0 = inactive 1 = active	–
1556	–	2258	N/A	Any F46DT Pickup	0 = inactive 1 = active	–
1557	–	2268	N/A	Any F46DT Timeout	0 = inactive 1 = active	–
1558	–	2260	N/A	Any F46IT Pickup	0 = inactive 1 = active	–
1559	–	2260	N/A	Any F46IT Timeout	0 = inactive 1 = active	–
1560	–	4960	N/A	Any F46 Trip	0 = inactive 1 = active	–
1561	–	4992	N/A	Any F46 Trip Target	0 = inactive 1 = active	–
1562	–	4992	N/A	Target Any F50P/F51P Ph-A	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1563	–	4992	N/A	Target Any F50P/F51P Ph-B	0 = inactive 1 = active	–
1564	–	4992	N/A	Target Any F50P/F51P Ph-C	0 = inactive 1 = active	–
1565	–	4992	N/A	Target Any F50N/F51N	0 = inactive 1 = active	–
1566	–	4992	N/A	Target Any 50/51 G/GS	0 = inactive 1 = active	–
1567	–	2050	N/A	Mechanical Trip Failure	0 = inactive 1 = active	–
1568	–	2050	N/A	Mechanical Close Failure	0 = inactive 1 = active	–
1569	–	2050	N/A	Mechanical Trip/Close Failure	0 = inactive 1 = active	–
1570	–	4996	N/A	Control Lockout from 79 Function	0 = inactive 1 = active	–
1571	–	4474	N/A	All Frequency Functions Blocked	0 = inactive 1 = active	–
1572	–	3942	N/A	Over Frequency Functions Blocked	0 = inactive 1 = active	–
1573	–	3942	N/A	Under Frequency Functions Blocked	0 = inactive 1 = active	–
1574	–	8479	N/A	HCL Enabled/Disabled	0 = inactive 1 = active	–
1575	–	2513	N/A	F79 Sequence Co-ordination in progress	0 = inactive 1 = active	–
1576	–	4997	N/A	Any HCL Phase Element Pickup	0 = inactive 1 = active	–
1577	–	4997	N/A	Any HCL Phase Element Timeout	0 = inactive 1 = active	–
1578	–	4997	N/A	HCL#1 Any Phase Pickup	0 = inactive 1 = active	–
1579	–	4997	N/A	HCL#1 Any Phase Timeout	0 = inactive 1 = active	–
1580	–	4997	N/A	HCL#2 Any Phase Pickup	0 = inactive 1 = active	–
1581	–	4997	N/A	HCL#2 Any Phase Timeout	0 = inactive 1 = active	–
1582	–	4997	N/A	HCL#3 Any Phase Pickup	0 = inactive 1 = active	–
1583	–	4997	N/A	HCL#3 Any Phase Timeout	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1584	–	4997	N/A	HCL#4 Any Phase Pickup	0 = inactive 1 = active	–
1585	–	4997	N/A	HCL#4 Any Phase Timeout	0 = inactive 1 = active	–
1586	–	4997	N/A	HCL#5 Any Phase Pickup	0 = inactive 1 = active	–
1587	–	4997	N/A	HCL#5 Any Phase Timeout	0 = inactive 1 = active	–
1588	–	4997	N/A	Any HCL N/G/GS Element Pickup	0 = inactive 1 = active	–
1589	–	4997	N/A	Any HCL N/G/GS Element Timeout	0 = inactive 1 = active	–
1590	–	8479	N/A	HCL Ph#1 Enabled/Disabled	0 = inactive 1 = active	–
1591	–	8484	N/A	HCL Ph#2 Enabled/Disabled	0 = inactive 1 = active	–
1592	–	8489	N/A	HCL Ph#3 Enabled/Disabled	0 = inactive 1 = active	–
1593	–	8494	N/A	HCL Ph#4 Enabled/Disabled	0 = inactive 1 = active	–
1594	–	8499	N/A	HCL Ph#5 Enabled/Disabled	0 = inactive 1 = active	–
1595	–	8479	N/A	HCL N/G/GS#1 Enabled/Disabled	0 = inactive 1 = active	–
1596	–	8484	N/A	HCL N/G/GS#2 Enabled/Disabled	0 = inactive 1 = active	–
1597	–	8489	N/A	HCL N/G/GS#3 Enabled/Disabled	0 = inactive 1 = active	–
1598	–	8494	N/A	HCL N/G/GS#4 Enabled/Disabled	0 = inactive 1 = active	–
1599	–	8499	N/A	HCL N/G/GS#5 Enabled/Disabled	0 = inactive 1 = active	–
1600	–	4996	N/A	Close Block on Frequency	0 = inactive 1 = active	–
1601	–	4996	N/A	Close Block on Voltage	0 = inactive 1 = active	–
1602	–	4996	N/A	Close Block on Voltage Upper Limit	0 = inactive 1 = active	–
1603	–	4996	N/A	Close Block on Voltage Lower Limit	0 = inactive 1 = active	–
1604	–	2052	N/A	SGI Target	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1605	–	5058	N/A	SGI Pickup IG Fund Threshold	0 = inactive 1 = active	–
1606	–	5058	N/A	SGI Timeout IG Fund Threshold	0 = inactive 1 = active	–
1607	–	5058	N/A	SGI IPSLogic Reset	0 = inactive 1 = active	–
1608	–	5058	N/A	SGI IPSLogic Initiate	0 = inactive 1 = active	–
1609	–	5058	N/A	SGI IG Frozen Trigger	0 = inactive 1 = active	–
1610	–	5058	N/A	SGI Initiated	0 = inactive 1 = active	–
1611	–	5058	N/A	SGI Reset Timer Start	0 = inactive 1 = active	–
1612	–	5058	N/A	SGI Reset Timer Expired	0 = inactive 1 = active	–
1613	–	5058	N/A	SGI Initiate Pickup	0 = inactive 1 = active	–
1614	–	5058	N/A	SGI Initiate Timeout	0 = inactive 1 = active	–
1615	–	5058	N/A	SGI Reset	0 = inactive 1 = active	–
1616	–	5058	N/A	SGI Enabled	0 = inactive 1 = active	–
1617	–	5058	N/A	SGI Blocked	0 = inactive 1 = active	–
1618	–	5058	N/A	SGI Pickup Delta IG Fund	0 = inactive 1 = active	–
1619	–	5059	N/A	SGI Timeout Delta IG Fund	0 = inactive 1 = active	–
1620	–	5059	N/A	SGI Pickup IG Harmonic	0 = inactive 1 = active	–
1621	–	5059	N/A	SGI Timeout IG Harmonic	0 = inactive 1 = active	–
1622	–	5059	N/A	SGI Pickup Delta IG % 3rd Harmonic	0 = inactive 1 = active	–
1623	–	5059	N/A	SGI Reset Frozen Timer Start	0 = inactive 1 = active	–
1624	–	5059	N/A	SGI Reset Frozen Timer Expired	0 = inactive 1 = active	–
1625	–	5059	N/A	SGI AND Gate Enabled	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1626	–	5059	N/A	SGI Latch Reset Timer Start	0 = inactive 1 = active	–
1627	–	5059	N/A	SGI Latch Reset Timer Expired	0 = inactive 1 = active	–
1628	–	5060	N/A	SGI Pickup IA Harmonic 1	0 = inactive 1 = active	–
1629	–	5060	N/A	SGI Pickup IB Harmonic 1	0 = inactive 1 = active	–
1630	–	5060	N/A	SGI Pickup IC Harmonic 1	0 = inactive 1 = active	–
1631	–	5060	N/A	SGI Pickup IA Harmonic 2	0 = inactive 1 = active	–
1632	–	5060	N/A	SGI Pickup IB Harmonic 2	0 = inactive 1 = active	–
1633	–	5060	N/A	SGI Pickup IC Harmonic 2	0 = inactive 1 = active	–
1634	–	5060	N/A	SGI Pickup IA Harmonic 3	0 = inactive 1 = active	–
1635	–	5060	N/A	SGI Pickup IB Harmonic 3	0 = inactive 1 = active	–
1636	–	5060	N/A	SGI Pickup IC Harmonic 3	0 = inactive 1 = active	–
1637	–	5060	N/A	SGI Pickup VA Harmonic 1	0 = inactive 1 = active	–
1638	–	5060	N/A	SGI Pickup VB Harmonic 1	0 = inactive 1 = active	–
1639	–	5060	N/A	SGI Pickup VC Harmonic 1	0 = inactive 1 = active	–
1640	–	5060	N/A	SGI Pickup VA Harmonic 2	0 = inactive 1 = active	–
1641	–	5060	N/A	SGI Pickup VB Harmonic 2	0 = inactive 1 = active	–
1642	–	5060	N/A	SGI Pickup VC Harmonic 2	0 = inactive 1 = active	–
1643	–	5060	N/A	SGI Pickup VA Harmonic 3	0 = inactive 1 = active	–
1644	–	5061	N/A	SGI Pickup VB Harmonic 3	0 = inactive 1 = active	–
1645	–	5061	N/A	SGI Pickup VC Harmonic 3	0 = inactive 1 = active	–
1646	–	5061	N/A	SGI Pickup Vy N Harmonic	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1647	–	5061	N/A	SGI Pickup Delta Vy N Harmonic	0 = inactive 1 = active	–
1648	–	5061	N/A	SGI Pickup IG Directional	0 = inactive 1 = active	–
1649	–	5061	N/A	SGI Pickup Delta IG Harmonic Directional	0 = inactive 1 = active	–
1650	–	5062	N/A	SGI Timeout IA Harmonic 1	0 = inactive 1 = active	–
1651	–	5062	N/A	SGI Timeout IB Harmonic 1	0 = inactive 1 = active	–
1652	–	5062	N/A	SGI Timeout IC Harmonic 1	0 = inactive 1 = active	–
1653	–	5062	N/A	SGI Timeout IA Harmonic 2	0 = inactive 1 = active	–
1654	–	5062	N/A	SGI Timeout IB Harmonic 2	0 = inactive 1 = active	–
1655	–	5062	N/A	SGI Timeout IC Harmonic 2	0 = inactive 1 = active	–
1656	–	5062	N/A	SGI Timeout IA Harmonic 3	0 = inactive 1 = active	–
1657	–	5062	N/A	SGI Timeout IB Harmonic 3	0 = inactive 1 = active	–
1658	–	5062	N/A	SGI Timeout IC Harmonic 3	0 = inactive 1 = active	–
1659	–	5062	N/A	SGI Timeout VyA Harmonic 1	0 = inactive 1 = active	–
1660	–	5062	N/A	SGI Timeout VyB Harmonic 1	0 = inactive 1 = active	–
1661	–	5062	N/A	SGI Timeout VyC Harmonic 1	0 = inactive 1 = active	–
1662	–	5062	N/A	SGI Timeout VyA Harmonic 2	0 = inactive 1 = active	–
1663	–	5062	N/A	SGI Timeout VyB Harmonic 2	0 = inactive 1 = active	–
1664	–	5062	N/A	SGI Timeout VyC Harmonic 2	0 = inactive 1 = active	–
1665	–	5062	N/A	SGI Timeout VyA Harmonic 3	0 = inactive 1 = active	–
1666	–	5063	N/A	SGI Timeout VyB Harmonic 3	0 = inactive 1 = active	–
1667	–	5063	N/A	SGI Timeout VyC Harmonic 3	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Single-Bit Binary Inputs (Status) Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1668	–	5063	N/A	SIG Timeout VN Harmonic	0 = inactive 1 = active	–
1669	–	5063	N/A	SIG Timeout VN Delta Harmonic	0 = inactive 1 = active	–
1670	–	5063	N/A	SIG Timeout IG Directional	0 = inactive 1 = active	–
1671	–	5063	N/A	SIG Timeout Delta IG Harmonic Directional	0 = inactive 1 = active	–
1672	–	29064	N/A	SIG Init Logic Enable	0 = inactive 1 = active	–
1673	–	29064	N/A	SIG Reset Logic Enable	0 = inactive 1 = active	–
1674	–	29064	N/A	SIG Apply Fund Delta Ig Supervision	0 = inactive 1 = active	–
1675	–	2087	N/A	SIG Change Pickup	0 = inactive 1 = active	–
1676	–	2088	N/A	SIG Change Timeout	0 = inactive 1 = active	–

Table 1 Single-Bit Binary Inputs (Status)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
0	–	1400	N/A	OPEN breaker command 0 = Disable 1 = Enable	0-1	–
1	–	1404	N/A	OPEN Ph A breaker command 0 = Disable 1 = Enable	0-1	–
2	–	1405	N/A	OPEN Ph B breaker comma 0 = Disable 1 = Enable nd	0-1	–
3	–	1406	N/A	TRIP Ph C breaker command 0 = Disable 1 = Enable	0-1	–
4	–	1401	N/A	CLOSE breaker 0 = Disable 1 = Enable	0-1	–
5	–	1407	N/A	CLOSE Ph A breaker command 0 = Disable 1 = Enable	0-1	–
6	–	1408	N/A	CLOSE Ph B breaker command 0 = Disable 1 = Enable	0-1	–
7	–	1409	N/A	CLOSE Ph C breaker command 0 = Disable 1 = Enable	0-1	–
8	–	1402	N/A	Hot Line Tag Command 0 = Disable 1 = Enable	0-1	–
9	–	1403	N/A	Reset command 0 = Disable 1 = Enable	0-1	–
10	–	0	N/A	Set to enable profile edit 0 = Disable 1 = Enable	0-1	–
11	–	3804	N/A	Energize/De-energize the output 1 0 = Disable 1 = Enable	0-1	–
12	–	3804	N/A	Energize/De-energize the output 2 0 = Disable 1 = Enable	0-1	–
13	–	3804	N/A	Energize/De-energize the output 3 0 = Disable 1 = Enable	0-1	–
14	–	3804	N/A	Energize/De-energize the output 4 0 = Disable 1 = Enable	0-1	–
15	–	3804	N/A	Energize/De-energize the output 5 0 = Disable 1 = Enable	0-1	–
16	–	3804	N/A	Energize/De-energize the output 6 0 = Disable 1 = Enable	0-1	–
17	–	3804	N/A	Energize/De-energize the output 7 0 = Disable 1 = Enable	0-1	–
18	–	3804	N/A	Energize/De-energize the output 8 0 = Disable 1 = Enable	0-1	–
19	–	3804	N/A	Energize/De-energize the output 9 0 = Disable 1 = Enable	0-1	–
20	–	3804	N/A	Energize/De-energize the output 10 0 = Disable 1 = Enable	0-1	–
21	–	3804	N/A	Energize/De-energize the output 11 0 = Disable 1 = Enable	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
22	–	3804	N/A	Energize/De-energize the output 12 0 = Disable 1 = Enable	0-1	–
23	–	1410	N/A	Remote IPSLOGIC Input 1 0 = Disable 1 = Enable	0-1	–
24	–	1410	N/A	Remote IPSLOGIC Input 2 0 = Disable 1 = Enable	0-1	–
25	–	1410	N/A	Remote IPSLOGIC Input 3 0 = Disable 1 = Enable	0-1	–
26	–	1410	N/A	Remote IPSLOGIC Input 4 0 = Disable 1 = Enable	0-1	–
27	–	1410	N/A	Remote IPSLOGIC Input 5 0 = Disable 1 = Enable	0-1	–
28	–	1410	N/A	Remote IPSLOGIC Input 6 0 = Disable 1 = Enable	0-1	–
29	–	1410	N/A	Remote IPSLOGIC Input 7 0 = Disable 1 = Enable	0-1	–
30	–	1410	N/A	Remote IPSLOGIC Input 8 0 = Disable 1 = Enable	0-1	–
31	–	1410	N/A	Remote IPSLOGIC Input 9 0 = Disable 1 = Enable	0-1	–
32	–	1410	N/A	Remote IPSLOGIC Input 10 0 = Disable 1 = Enable	0-1	–
33	1	1410	N/A	Remote IPSLOGIC Input 11 0 = Disable 1 = Enable	0-1	–
34	1	1410	N/A	Remote IPSLOGIC Input 12 0 = Disable 1 = Enable	0-1	–
35	1	1410	N/A	Remote IPSLOGIC Input 13 0 = Disable 1 = Enable	0-1	–
36	1	1410	N/A	Remote IPSLOGIC Input 14 0 = Disable 1 = Enable	0-1	–
37	1	1410	N/A	Remote IPSLOGIC Input 15 0 = Disable 1 = Enable	0-1	–
38	1	1410	N/A	Remote IPSLOGIC Input 16 0 = Disable 1 = Enable	0-1	–
39	1	1422	N/A	Remote IPSLOGIC Input 17 0 = Disable 1 = Enable	0-1	–
40	1	1422	N/A	Remote IPSLOGIC Input 18 0 = Disable 1 = Enable	0-1	–
41	1	1422	N/A	Remote IPSLOGIC Input 19 0 = Disable 1 = Enable	0-1	–
42	1	1422	N/A	Remote IPSLOGIC Input 20 0 = Disable 1 = Enable	0-1	–
43	1	1422	N/A	Remote IPSLOGIC Input 21 0 = Disable 1 = Enable	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
44	1	1422	N/A	Remote IPSLOGIC Input 22 0 = Disable 1 = Enable	0-1	—
45	1	1422	N/A	Remote IPSLOGIC Input 23 0 = Disable 1 = Enable	0-1	—
46	1	1422	N/A	Remote IPSLOGIC Input 24 0 = Disable 1 = Enable	0-1	—
47	1	1422	N/A	Remote IPSLOGIC Input 25 0 = Disable 1 = Enable	0-1	—
48	1	1422	N/A	Remote IPSLOGIC Input 26 0 = Disable 1 = Enable	0-1	—
49	1	1422	N/A	Remote IPSLOGIC Input 27 0 = Disable 1 = Enable	0-1	—
50	1	1422	N/A	Remote IPSLOGIC Input 28 0 = Disable 1 = Enable	0-1	—
51	1	1422	N/A	Remote IPSLOGIC Input 29 0 = Disable 1 = Enable	0-1	—
52	1	1422	N/A	Remote IPSLOGIC Input 30 0 = Disable 1 = Enable	0-1	—
53	1	1422	N/A	Remote IPSLOGIC Input 31 0 = Disable 1 = Enable	0-1	—
54	1	1422	N/A	Remote IPSLOGIC Input 32 0 = Disable 1 = Enable	0-1	—
55	1	1423	N/A	Remote IPSLOGIC Input 33 0 = Disable 1 = Enable	0-1	—
56	1	1423	N/A	Remote IPSLOGIC Input 34 0 = Disable 1 = Enable	0-1	—
57	1	1423	N/A	Remote IPSLOGIC Input 35 0 = Disable 1 = Enable	0-1	—
58	1	1423	N/A	Remote IPSLOGIC Input 36 0 = Disable 1 = Enable	0-1	—
59	1	1423	N/A	Remote IPSLOGIC Input 37 0 = Disable 1 = Enable	0-1	—
60	1	1423	N/A	Remote IPSLOGIC Input 38 0 = Disable 1 = Enable	0-1	—
61	1	1423	N/A	Remote IPSLOGIC Input 39 0 = Disable 1 = Enable	0-1	—
62	1	1423	N/A	Remote IPSLOGIC Input 40 0 = Disable 1 = Enable	0-1	—
63	1	1423	N/A	Remote IPSLOGIC Input 41 0 = Disable 1 = Enable	0-1	—
64	1	1423	N/A	Remote IPSLOGIC Input 42 0 = Disable 1 = Enable	0-1	—
65	1	1423	N/A	Remote IPSLOGIC Input 43 0 = Disable 1 = Enable	0-1	—

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
66	1	1423	N/A	Remote IPSLOGIC Input 44 0 = Disable 1 = Enable	0-1	—
67	1	1423	N/A	Remote IPSLOGIC Input 45 0 = Disable 1 = Enable	0-1	—
68	1	1423	N/A	Remote IPSLOGIC Input 46 0 = Disable 1 = Enable	0-1	—
69	1	1423	N/A	Remote IPSLOGIC Input 47 0 = Disable 1 = Enable	0-1	—
70	1	1423	N/A	Remote IPSLOGIC Input 48 0 = Disable 1 = Enable	0-1	—
71	1	1424	N/A	Remote IPSLOGIC Input 49 0 = Disable 1 = Enable	0-1	—
72	1	1424	N/A	Remote IPSLOGIC Input 50 0 = Disable 1 = Enable	0-1	—
73	1	1424	N/A	Remote IPSLOGIC Input 51 0 = Disable 1 = Enable	0-1	—
74	1	1424	N/A	Remote IPSLOGIC Input 52 0 = Disable 1 = Enable	0-1	—
75	1	1424	N/A	Remote IPSLOGIC Input 53 0 = Disable 1 = Enable	0-1	—
76	1	1424	N/A	Remote IPSLOGIC Input 54 0 = Disable 1 = Enable	0-1	—
77	1	1424	N/A	Remote IPSLOGIC Input 55 0 = Disable 1 = Enable	0-1	—
78	1	1424	N/A	Remote IPSLOGIC Input 56 0 = Disable 1 = Enable	0-1	—
79	1	1424	N/A	Remote IPSLOGIC Input 57 0 = Disable 1 = Enable	0-1	—
80	1	1424	N/A	Remote IPSLOGIC Input 58 0 = Disable 1 = Enable	0-1	—
81	1	1424	N/A	Remote IPSLOGIC Input 59 0 = Disable 1 = Enable	0-1	—
82	1	1424	N/A	Remote IPSLOGIC Input 60 0 = Disable 1 = Enable	0-1	—
83	1	1424	N/A	Remote IPSLOGIC Input 61 0 = Disable 1 = Enable	0-1	—
84	1	1424	N/A	Remote IPSLOGIC Input 62 0 = Disable 1 = Enable	0-1	—
85	1	1424	N/A	Remote IPSLOGIC Input 63 0 = Disable 1 = Enable	0-1	—
86	1	1424	N/A	Remote IPSLOGIC Input 64 0 = Disable 1 = Enable	0-1	—
87	1	20	N/A	Change to Profile 1 0 = Disable 1 = Enable	0-1	—

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
88	1	20	N/A	Change to Profile 2 0 = Disable 1 = Enable	0-1	–
89	1	20	N/A	Change to Profile 3 0 = Disable 1 = Enable	0-1	–
90	1	20	N/A	Change to Profile 4 0 = Disable 1 = Enable	0-1	–
91	1	20	N/A	Change to Profile 5 0 = Disable 1 = Enable	0-1	–
92	1	20	N/A	Change to Profile 6 0 = Disable 1 = Enable	0-1	–
93	1	20	N/A	Change to Profile 7 0 = Disable 1 = Enable	0-1	–
94	1	20	N/A	Change to Profile 8 0 = Disable 1 = Enable	0-1	–
95	1	–	N/A	RESET DEMAND 0 = Disable Residual Disable 1 = Enable	0-1	–
96	1	1403	N/A	RESET ALARMS 0 = Disable 1 = Enable	0-1	–
97	1	1150	N/A	Reset Pickup OC Counter Phase A 0 = Disable 1 = Enable	0-1	–
98	1	1154	N/A	Reset Pickup OC Counter Phase B 0 = Disable 1 = Enable	0-1	–
99	1	1158	N/A	Reset Pickup OC Counter Phase C 0 = Disable 1 = Enable	0-1	–
100	1	1188-1189	N/A	Reset Pickup OC Counter Ground 0 = Disable 1 = Enable	0-1	–
101	1	1186-1187	N/A	Reset Pickup OC Counter Residual 0 = Disable 1 = Enable	0-1	–
102	1	1152-1153	N/A	Reset Total Trip OC Counter Phase A 0 = Disable 1 = Enable	0-1	–
103	1	1156-1157	N/A	Reset Total Trip OC Counter Phase B 0 = Disable 1 = Enable	0-1	–
104	1	1160-1161	N/A	Reset Total Trip OC Counter Phase C 0 = Disable 1 = Enable	0-1	–
105	1	1184-1185	N/A	Reset Total Trip OC Counter Ground 0 = Disable 1 = Enable	0-1	–
106	1	1182-1183	N/A	Reset Total Trip OC Counter Residual 0 = Disable 1 = Enable	0-1	–
107	1	1174	N/A	Reset IPSlogic Counter #1 0 = Disable 1 = Enable	0-1	–
108	1	1175	N/A	Reset IPSlogic Counter #2 0 = Disable 1 = Enable	0-1	–
109	–	1176	N/A	Reset IPSlogic Counter #3 0 = Disable 1 = Enable	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
110	–	1177	N/A	Reset IPSlogic Counter #4 0 = Disable 1 = Enable	0-1	–
111	–	1178	N/A	Reset IPSlogic Counter #5 0 = Disable 1 = Enable	0-1	–
112	–	1179	N/A	Reset IPSlogic Counter #6 0 = Disable 1 = Enable	0-1	–
113	–	1180	N/A	Reset IPSlogic Counter #7 0 = Disable 1 = Enable	0-1	–
114	–	1181	N/A	Reset IPSlogic Counter #8 0 = Disable 1 = Enable	0-1	–
115	–	9798	N/A	Fast Trip (51P#1) Enable/Disable 0 = Disable 1 = Enable	0-1	–
116	–	6515	N/A	Battery Test Schedule Enable/Disable 0 = Disable 1 = Enable	0-1	–
117	–	7022	N/A	Sync Check Rly (25) Enable/Disable 0 = Disable 1 = Enable	0-1	–
118	–	11410	N/A	TT Enable/Disable 0 = Disable 1 = Enable	0-1	–
119	–	6508	N/A	Start Battery Test Enable/Disable 0 = Disable 1 = Enable	0-1	–
120	–	1411	N/A	Disable Fast/Slow Curves 0 = Disable 1 = Enable	0-1	–
121	–	1411	N/A	Enable Ph and Gnd Fast Curves (TPGF) 0 = Disable 1 = Enable	0-1	–
122	–	1411	N/A	Enable Ph and Gnd Slow Curves (TPGS) 0 = Disable 1 = Enable	0-1	–
123	–	1411	N/A	Enable Ph Fast Curve Only (TPF) 0 = Disable 1 = Enable	0-1	–
124	–	1411	N/A	Enable Ph Slow Curve Only (TPS) 0 = Disable 1 = Enable	0-1	–
125	–	1411	N/A	Enable Gnd Fast Curve Only (TGF) 0 = Disable 1 = Enable	0-1	–
126	–	1411	N/A	Enable Gnd Slow Curve Only (TGS) 0 = Disable 1 = Enable	0-1	–
127	–	1411	N/A	Enable Slow Elements Only (TSlow) 0 = Disable 1 = Enable	0-1	–
128	–	1412	N/A	Enable Close Block Override 0 = Disable 1 = Enable	0-1	–
129	–	1413	N/A	Activate Button S10 0 = Disable 1 = Enable	0-1	–
130	–	1413	N/A	Activate Button S11 0 = Disable 1 = Enable	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
131	–	1413	N/A	Activate Button S12 0 = Disable 1 = Enable	0-1	–
132	–	1413	N/A	Activate Button S13 0 = Disable 1 = Enable	0-1	–
133	–	1413	N/A	Activate Button S14 0 = Disable 1 = Enable	0-1	–
134	–	1413	N/A	Activate Button S15 0 = Disable 1 = Enable	0-1	–
135	–	1413	N/A	Activate Button S16 0 = Disable 1 = Enable	0-1	–
136	–	1413	N/A	Activate Button S17 0 = Disable 1 = Enable	0-1	–
137	–	1415	N/A	Remote IPSLOGIC Pulse Input 1 0 = Disable 1 = Enable	0-1	–
138	–	1415	N/A	Remote IPSLOGIC Pulse Input 2 0 = Disable 1 = Enable	0-1	–
139	–	1415	N/A	Remote IPSLOGIC Pulse Input 3 0 = Disable 1 = Enable	0-1	–
140	–	1415	N/A	Remote IPSLOGIC Pulse Input 4 0 = Disable 1 = Enable	0-1	–
141	–	1415	N/A	Remote IPSLOGIC Pulse Input 5 0 = Disable 1 = Enable	0-1	–
142	–	1415	N/A	Remote IPSLOGIC Pulse Input 6 0 = Disable 1 = Enable	0-1	–
143	–	1415	N/A	Remote IPSLOGIC Pulse Input 7 0 = Disable 1 = Enable	0-1	–
144	–	1415	N/A	Remote IPSLOGIC Pulse Input 8 0 = Disable 1 = Enable	0-1	–
145	–	1415	N/A	Remote IPSLOGIC Pulse Input 9 0 = Disable 1 = Enable	0-1	–
146	–	1415	N/A	Remote IPSLOGIC Pulse Input 10 0 = Disable 1 = Enable	0-1	–
147	–	1415	N/A	Remote IPSLOGIC Pulse Input 11 0 = Disable 1 = Enable	0-1	–
148	–	1415	N/A	Remote IPSLOGIC Pulse Input 12 0 = Disable 1 = Enable	0-1	–
149	–	3917	N/A	Ground/Ground Sensitive Disable 0 = Disable 1 = Enable	0-1	–
150	–	3942	N/A	Residual Enable/Disable 0 = Disable 1 = Enable	0-1	–
151	–	3942,3943	–	Loop Scheme Inhibit	0-1	–
152	–	29064	N/A	SGL Init Logic Enable (Bit position- 0)	0-31	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
153	–	29064	N/A	SGI Reset Logic Enable (Bit position- 1)	0-31	–
154	–	29064	N/A	SGI Apply Fund Delta Ig Supervision (Bit position- 2)	0-31	–
155	–	29000	N/A	Enable Harmonic current 1 (Bit position- 0)	0-1	–
156	–	29000	N/A	Enable Harmonic current 2 (Bit position- 1)	0-1	–
157	–	29000	N/A	Enable Harmonic current 3 (Bit position- 2)	0-1	–
158	–	29000	N/A	Enable Harmonic current G1 (Bit position- 3)	0-1	–
159	–	29000	N/A	Enable Harmonic current G2 (Bit position- 4)	0-1	–
160	–	29000	N/A	Enable Voltage Harmonic 1 (Bit position- 5)	0-1	–
161	–	29000	N/A	Enable Voltage Harmonic 2 (Bit position- 6)	0-1	–
162	–	29000	N/A	Enable Voltage Harmonic 3 (Bit position- 7)	0-1	–
163	–	29000	N/A	Enable Voltage Harmonic G1 (Bit position- 8)	0-1	–
164	–	29000	N/A	Enable Voltage Harmonic G2 (Bit position- 9)	0-1	–
165	–	1417	N/A	Clear Alarms	0-1	–
166	–	1418	N/A	Buttons Init to Default	0-1	–
167	–	46138	N/A	Sectionalizer Enable/Disable SCM	0-31	–
168	–	1400	N/A	Breaker Operation 3 Ph	0-1	–
169	–	1404	N/A	Breaker Operation Ph A	0-1	–
170	–	1405	N/A	Breaker Operation Ph B	0-1	–
171	–	1406	N/A	Breaker Operation Ph C	0-1	–
172	–	1100	N/A	Reset COMIPSlogic Counter #1	0-1	–
173	–	1101	N/A	Reset COMIPSlogic Counter #2	0-1	–
174	–	1102	N/A	Reset COMIPSlogic Counter #3	0-1	–
175	–	1103	N/A	Reset COMIPSlogic Counter #4	0-1	–
176	–	1104	N/A	Reset COMIPSlogic Counter #5	0-1	–
177	–	1105	N/A	Reset COMIPSlogic Counter #6	0-1	–
178	–	1106	N/A	Reset COMIPSlogic Counter #7	0-1	–
179	–	1107	N/A	Reset COMIPSlogic Counter #8	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
180	–	46327	N/A	Loop Scheme Enable Live Line	0-1	–
181	–	46327	N/A	Loop Scheme Enable Keep Status on Prof Switch	0-1	–
182	–	46327	N/A	Loop Scheme Dead Line 3Ph OR/AND	0-1	–
183	–	145	N/A	Trip Mode (TM)	0-1	–
184	–	146	N/A	Lockout Mode (LOM)	0-1	–
185	–	30922	N/A	Enable G/N Trip 3T3LO Only	0-1	–
186	–	30922	N/A	F79 Trip Mode	0-1	–
187	–	30922	N/A	F79 Lock Mode	0-1	–
188	–	150	N/A	Enable Use 79 Global Operation Mode	0-1	–
189	–	148	N/A	Enable Fault Current	0-1	–
190	–	4464	N/A	FCLP Initiate	0-1	–
191	–	8608	N/A	FCLP Enable/Disable	0-1	–
192	–	3942	N/A	FCLP Pickup Block	0-1	–
193	–	8774	N/A	F50P in HLT Enable/Disable	0-1	–
194	–	8777	N/A	F50G in HLT Enable/Disable	0-1	–
195	–	8780	N/A	F51P in HLT Enable/Disable	0-1	–
196	–	8791	N/A	F51G in HLT Enable/Disable	0-1	–
197	–	8804	N/A	F50N in HLT Enable/Disable	0-1	–
198	–	8807	N/A	F51N in HLT Enable/Disable	0-1	–
199	–	8832	N/A	Auto restoration Enable Frequency load shed restore limits	0-1	–
200	–	8832	N/A	Auto restoration Enable Voltage load shed restore limits	0-1	–
201	–	8840	N/A	Auto restoration Enable use both Vol and Freq restore limits	0-1	–
202	–	8840	N/A	Auto restoration Enable 3-Ph/1-Ph mode in Vol restore	0-1	–
203	–	9102	N/A	IPSLogic #1 Enable/Disable	0-1	–
204	–	9106	N/A	IPSLogic #2 Enable/Disable	0-1	–
205	–	9110	N/A	IPSLogic #3 Enable/Disable	0-1	–
206	–	9114	N/A	IPSLogic #4 Enable/Disable	0-1	–
207	–	9119	N/A	IPSLogic #5 Enable/Disable	0-1	–
208	–	9122	N/A	IPSLogic #6 Enable/Disable	0-1	–
209	–	9126	N/A	IPSLogic #7 Enable/Disable	0-1	–
210	–	9130	N/A	IPSLogic #8 Enable/Disable	0-1	–
211	–	9174	N/A	LEL Enable/Disable	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
212	–	46045	N/A	Breaker Monitor #1 Enable/Disable	0-1	–
213	–	47472	N/A	Breaker Monitor #2 Enable/Disable	0-1	–
214	–	10769	N/A	79 Recloser Relay 3-Ph Enable/Disable	0-1	–
215	–	33049	N/A	79 Recloser Relay Independent Ph Enable/Disable	0-1	–
216	–	48436	N/A	Sectionalizer 3-Ph Enable/Disable	0-1	–
217	–	48452	N/A	Sectionalizer Independent Ph Enable/Disable	0-1	–
218	–	46319	N/A	Loop Scheme Enable/Disable	0-1	–
219	–	25457	N/A	PSM Enable/Disable	0-1	–
220	1	6507	N/A	Battery Charger Enable/Disable	0-1	–
221	–	25432	N/A	TCM Enable/Disable	0-1	–
222	–	25437	N/A	CCM Enable/Disable	0-1	–
223	–	25487	N/A	60FL Enable/Disable	0-1	–
224	–	28060	N/A	Close Block 3-Ph Enable/Disable	0-1	–
225	–	28069	N/A	Close Block Ph-A Enable/Disable	0-1	–
226	–	28078	N/A	Close Block Ph-B Enable/Disable	0-1	–
227	–	28087	N/A	Close Block Ph-C Enable/Disable	0-1	–
228	–	65002	N/A	Global IPSLogic #1 Enable/Disable	0-1	–
229	–	65011	N/A	Global IPSLogic #2 Enable/Disable	0-1	–
230	–	65020	N/A	Global IPSLogic #3 Enable/Disable	0-1	–
231	–	65029	N/A	Global IPSLogic #4 Enable/Disable	0-1	–
232	–	65038	N/A	Global IPSLogic #5 Enable/Disable	0-1	–
233	–	65047	N/A	Global IPSLogic #6 Enable/Disable	0-1	–
234	–	65056	N/A	Global IPSLogic #7 Enable/Disable	0-1	–
235	–	65065	N/A	Global IPSLogic #8 Enable/Disable	0-1	–
236	–	29052	N/A	SGI Enable/Disable	0-1	–
237	–	28002	N/A	THD #1 3-Ph Enable/Disable	0-1	–
238	–	28009	N/A	THD #2 3-Ph Enable/Disable	0-1	–
239	–	28016	N/A	THD #1 Ph-A Enable/Disable	0-1	–
240	–	28034	N/A	THD #2 Ph-A Enable/Disable	0-1	–
241	–	28022	N/A	THD #1 Ph-B Enable/Disable	0-1	–
242	–	28040	N/A	THD #2 Ph-B Enable/Disable	0-1	–
243	–	28028	N/A	THD #1 Ph-C Enable/Disable	0-1	–
244	–	28046	N/A	THD #2 Ph-C Enable/Disable	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
245	–	1400	N/A	OPEN ABC with Remote Disable	0-1	–
246	–	1404	N/A	OPEN A with Remote Disable	0-1	–
247	–	1405	N/A	OPEN B with Remote Disable	0-1	–
248	–	1406	N/A	OPEN C with Remote Disable	0-1	–
249	–	1166	N/A	Reset Switching Device Operations Ph-A	0-1	–
250	–	1168	N/A	Reset Switching Device Operations Ph-B	0-1	–
251	–	1170	N/A	Reset Switching Device Operations Ph-C	0-1	–
252	–	20	N/A	Profile toggle Alternate Prof 1 / Previous Active Prof X	0-1	–
253	–	20	N/A	Profile toggle Alternate Prof 2 / Previous Active Prof X	0-1	–
254	–	20	N/A	Profile toggle Alternate Prof 3 / Previous Active Prof X	0-1	–
255	–	20	N/A	Profile toggle Alternate Prof 4 / Previous Active Prof X	0-1	–
256	–	20	N/A	Profile toggle Alternate Prof 5 / Previous Active Prof X	0-1	–
257	–	20	N/A	Profile toggle Alternate Prof 6 / Previous Active Prof X	0-1	–
258	–	20	N/A	Profile toggle Alternate Prof 7 / Previous Active Prof X	0-1	–
259	–	20	N/A	Profile toggle Alternate Prof 8 / Previous Active Prof X	0-1	–
260	–	20	N/A	Profile toggle Alternate Prof 1 / Default Active Prof	0-1	–
261	–	20	N/A	Profile toggle Alternate Prof 2 / Default Active Prof	0-1	–
262	–	20	N/A	Profile toggle Alternate Prof 3 / Default Active Prof	0-1	–
263	–	20	N/A	Profile toggle Alternate Prof 4 / Default Active Prof	0-1	–
264	–	20	N/A	Profile toggle Alternate Prof 5 / Default Active Prof	0-1	–
265	–	20	N/A	Profile toggle Alternate Prof 6 / Default Active Prof	0-1	–
266	–	20	N/A	Profile toggle Alternate Prof 7 / Default Active Prof	0-1	–
267	–	20	N/A	Profile toggle Alternate Prof 8 / Default Active Prof	0-1	–
268	–	3917	N/A	Reclose Disable	0-1	–
269	–	3917	N/A	Reclose Enable	0-1	–
270	–	3940	N/A	Sectionalizer Disable	0-1	–
271	–	46303	N/A	IHR 50P #1 Enable/Disable	0-1	–
272	–	46303	N/A	IHR 50P #2 Enable/Disable	0-1	–
273	–	46303	N/A	IHR 50P #3 Enable/Disable	0-1	–
274	–	46303	N/A	IHR 50P #4 Enable/Disable	0-1	–
275	–	46303	N/A	IHR 50P #5 Enable/Disable	0-1	–
276	–	46304	N/A	IHR 50N #1 Enable/Disable	0-1	–
277	–	46304	N/A	IHR 50N #2 Enable/Disable	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
278	–	46304	N/A	IHR 50N #3 Enable/Disable	0-1	–
279	–	46304	N/A	IHR 50N #4 Enable/Disable	0-1	–
280	–	46304	N/A	IHR 50N #5 Enable/Disable	0-1	–
281	–	46304	N/A	IHR 50G/GS #1 Enable/Disable	0-1	–
282	–	46304	N/A	IHR 50G/GS #2 Enable/Disable	0-1	–
283	–	46304	N/A	IHR 50G/GS #3 Enable/Disable	0-1	–
284	–	46304	N/A	IHR 50G/GS #4 Enable/Disable	0-1	–
285	–	46304	N/A	IHR 50G/GS #5 Enable/Disable	0-1	–
286	–	46304	N/A	IHR 46DT #1 Enable/Disable	0-1	–
287	–	46304	N/A	IHR 46DT #2 Enable/Disable	0-1	–
288	–	46304	N/A	IHR 46DT #3 Enable/Disable	0-1	–
289	–	46304	N/A	IHR 46DT #4 Enable/Disable	0-1	–
290	–	46304	N/A	IHR 46DT #5 Enable/Disable	0-1	–
291	–	46305	N/A	IHR 51P #1 Enable/Disable	0-1	–
292	–	46305	N/A	IHR 51P #2 Enable/Disable	0-1	–
293	–	46305	N/A	IHR 51P #3 Enable/Disable	0-1	–
294	–	46305	N/A	IHR 51P #4 Enable/Disable	0-1	–
295	–	46305	N/A	IHR 51P #5 Enable/Disable	0-1	–
296	–	46306	N/A	IHR 51N #1 Enable/Disable	0-1	–
297	–	46306	N/A	IHR 51N #2 Enable/Disable	0-1	–
298	–	46306	N/A	IHR 51N #3 Enable/Disable	0-1	–
299	–	46306	N/A	IHR 51N #4 Enable/Disable	0-1	–
300	–	46306	N/A	IHR 51N #5 Enable/Disable	0-1	–
301	–	46306	N/A	IHR 51G/GS #1 Enable/Disable	0-1	–
302	–	46306	N/A	IHR 51G/GS #2 Enable/Disable	0-1	–
303	–	46306	N/A	IHR 51G/GS #3 Enable/Disable	0-1	–
304	–	46306	N/A	IHR 51G/GS #4 Enable/Disable	0-1	–
305	–	46306	N/A	IHR 51G/GS #5 Enable/Disable	0-1	–
306	–	46306	N/A	IHR 46IT #1 Enable/Disable	0-1	–
307	–	46306	N/A	IHR 46IT #2 Enable/Disable	0-1	–
308	–	46306	N/A	IHR 46IT #3 Enable/Disable	0-1	–
309	–	46306	N/A	IHR 46IT #4 Enable/Disable	0-1	–
310	–	46306	N/A	IHR 46IT #5 Enable/Disable	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
311	–	46307	N/A	IHR 67P #1 Enable/Disable	0-1	–
312	–	46307	N/A	IHR 67P #2 Enable/Disable	0-1	–
313	–	46307	N/A	IHR 67P #3 Enable/Disable	0-1	–
314	–	46307	N/A	IHR 67P #4 Enable/Disable	0-1	–
315	–	46307	N/A	IHR 67P #5 Enable/Disable	0-1	–
316	–	46308	N/A	IHR 67N #1 Enable/Disable	0-1	–
317	–	46308	N/A	IHR 67N #2 Enable/Disable	0-1	–
318	–	46308	N/A	IHR 67N #3 Enable/Disable	0-1	–
319	–	46308	N/A	IHR 67N #4 Enable/Disable	0-1	–
320	–	46308	N/A	IHR 67N #5 Enable/Disable	0-1	–
321	–	46308	N/A	IHR 67G/GS #1 Enable/Disable	0-1	–
322	–	46308	N/A	IHR 67G/GS #2 Enable/Disable	0-1	–
323	–	46308	N/A	IHR 67G/GS #3 Enable/Disable	0-1	–
324	–	46308	N/A	IHR 67G/GS #4 Enable/Disable	0-1	–
325	–	46308	N/A	IHR 67G/GS #5 Enable/Disable	0-1	–
326	–	46308	N/A	IHR 67Q #1 Enable/Disable	0-1	–
327	–	46308	N/A	IHR 67Q #2 Enable/Disable	0-1	–
328	–	46308	N/A	IHR 67Q #3 Enable/Disable	0-1	–
329	–	46308	N/A	IHR 67Q #4 Enable/Disable	0-1	–
330	–	46308	N/A	IHR 67Q #5 Enable/Disable	0-1	–
331	–	3917	N/A	Ground and Residual Disable	0-1	–
332	–	3924	N/A	LOCK Enable/Disable	0-1	–
333	–	3942	N/A	SGF CutOut Enable/Disable	0-1	–
334	–	3942	N/A	Gnd CutOut Enable/Disable	0-1	–
335	–	46150	N/A	One Shot Sectionalizing 3-Ph Enable/Disable	0-1	–
336	–	46166	N/A	One Shot Sectionalizing Ph-A Enable/Disable	0-1	–
337	–	46182	N/A	One Shot Sectionalizing Ph-B Enable/Disable	0-1	–
338	–	46198	N/A	One Shot Sectionalizing Ph-C Enable/Disable	0-1	–
339	–	26	N/A	Change Active Prof to Default	0-1	–
340	–	153	N/A	Change Active Prof to Alternate	0-1	–
341	–	46319	N/A	Loop Auto Disable	0-1	–
342	–	46319	N/A	Loop Auto Enable	0-1	–
343	–	48373	N/A	Hot Line Tag Off Control	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
344	–	48373	N/A	Hot Line Tag On Control	0-1	–
345	–	3917	N/A	Ground and Residual Enable	0-1	–
346	–	1411	N/A	Turn Fast Operate Off	0-1	–
347	–	1411	N/A	Turn Fast Operate On	0-1	–
348	–	3917	N/A	Reclosing (79) Block/Unblock	0-1	–
349	–	4473	N/A	Any Phase Protection Function Block/Unblock	0-1	–
350	–	4473	N/A	Any Overcurrent Block/Unblock	0-1	–
351	–	4473	N/A	Any Phase Overcurrent Block/Unblock	0-1	–
352	–	4473	N/A	Any Ground Block/Unblock	0-1	–
353	–	4473	N/A	Any Residual Block/Unblock	0-1	–
354	–	4473	N/A	Any Protection Function Block/Unblock	0-1	–
355	–	4473	N/A	Any Overvoltage Function Block/Unblock	0-1	–
356	–	4473	N/A	Any Undervoltage Function Block/Unblock	0-1	–
357	–	4468	N/A	F46BC Block/Unblock	0-1	–
358	–	–	N/A	Select Phase A/B/C/ABC	0-1	–
359	–	4413	N/A	F27 #1 Ph-A Block/Unblock	0-1	–
360	–	4413	N/A	F27 #1 Ph-B Block/Unblock	0-1	–
361	–	4413	N/A	F27 #1 Ph-C Block/Unblock	0-1	–
362	–	4413	N/A	F27 #1 All Phases Block/Unblock	0-1	–
363	–	4413	N/A	F27 #2 Ph-A Block/Unblock	0-1	–
364	–	4413	N/A	F27 #2 Ph-B Block/Unblock	0-1	–
365	–	4413	N/A	F27 #2 Ph-C Block/Unblock	0-1	–
366	–	4413	N/A	F27 #2 All Phases Block/Unblock	0-1	–
367	–	4413	N/A	F27 #3 Ph-A Block/Unblock	0-1	–
368	–	4413	N/A	F27 #3 Ph-B Block/Unblock	0-1	–
369	–	4413	N/A	F27 #3 Ph-C Block/Unblock	0-1	–
370	–	4413	N/A	F27 #3 All Phases Block/Unblock	0-1	–
371	–	4413	N/A	F27 #4 Ph-A Block/Unblock	0-1	–
372	–	4413	N/A	F27 #4 Ph-B Block/Unblock	0-1	–
373	–	4413	N/A	F27 #4 Ph-C Block/Unblock	0-1	–
374	–	4413	N/A	F27 #4 All Phases Block/Unblock	0-1	–
375	–	4428	N/A	F27 #5 3-Ph Block/Unblock	0-1	–
376	–	4428	N/A	F27 #6 3-Ph Block/Unblock	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
377	–	4432	N/A	F27 #7 3-Ph Block/Unblock	0-1	–
378	–	4432	N/A	F27 #8 3-Ph Block/Unblock	0-1	–
379	–	4413	N/A	F27PP Ph-A Block/Unblock	0-1	–
380	–	4413	N/A	F27PP Ph-B Block/Unblock	0-1	–
381	–	4413	N/A	F27PP Ph-C Block/Unblock	0-1	–
382	–	4413	N/A	F27PP All Phases Block/Unblock	0-1	–
383	–	4419	N/A	F47 Block/Unblock	0-1	–
384	–	4418	N/A	F59 #1 Ph-A Block/Unblock	0-1	–
385	–	4418	N/A	F59 #1 Ph-B Block/Unblock	0-1	–
386	–	4418	N/A	F59 #1 Ph-C Block/Unblock	0-1	–
387	–	4418	N/A	F59 #1 All Phases Block/Unblock	0-1	–
388	–	4418	N/A	F59 #2 Ph-A Block/Unblock	0-1	–
389	–	4418	N/A	F59 #2 Ph-B Block/Unblock	0-1	–
390	–	4418	N/A	F59 #2 Ph-C Block/Unblock	0-1	–
391	–	4418	N/A	F59 #2 All Phases Block/Unblock	0-1	–
392	–	4419	N/A	F59 #3 Ph-A Block/Unblock	0-1	–
393	–	4419	N/A	F59 #3 Ph-B Block/Unblock	0-1	–
394	–	4419	N/A	F59 #3 Ph-C Block/Unblock	0-1	–
395	–	4419	N/A	F59 #3 All Phases Block/Unblock	0-1	–
396	–	4419	N/A	F59 #4 Ph-A Block/Unblock	0-1	–
397	–	4419	N/A	F59 #4 Ph-B Block/Unblock	0-1	–
398	–	4419	N/A	F59 #4 Ph-C Block/Unblock	0-1	–
399	–	4419	N/A	F59 #4 All Phases Block/Unblock	0-1	–
400	–	4432	N/A	F59 #5 3-Ph Block/Unblock	0-1	–
401	–	4432	N/A	F59 #6 3-Ph Block/Unblock	0-1	–
402	–	4432	N/A	F59 #7 3-Ph Block/Unblock	0-1	–
403	–	4432	N/A	F59 #8 3-Ph Block/Unblock	0-1	–
404	–	4419	N/A	F59N #1 Block/Unblock	0-1	–
405	–	4428	N/A	F59N #2 Block/Unblock	0-1	–
406	–	4419	N/A	F59PP Ph-A Block/Unblock	0-1	–
407	–	4419	N/A	F59PP Ph-B Block/Unblock	0-1	–
408	–	4419	N/A	F59PP Ph-C Block/Unblock	0-1	–
409	–	4419	N/A	F59PP All Phases Block/Unblock	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
410	—	4423	N/A	F59I Ph-A Block/Unblock	0-1	—
411	—	4423	N/A	F59I Ph-B Block/Unblock	0-1	—
412	—	4423	N/A	F59I Ph-C Block/Unblock	0-1	—
413	—	4423	N/A	F59I All Phases Block/Unblock	0-1	—
414	—	4416	N/A	F46DT #1 Block/Unblock	0-1	—
415	—	4416	N/A	F46DT #2 Block/Unblock	0-1	—
416	—	4416	N/A	F46DT #3 Block/Unblock	0-1	—
417	—	4416	N/A	F46DT #4 Block/Unblock	0-1	—
418	—	4416	N/A	F46DT #5 Block/Unblock	0-1	—
419	—	4418	N/A	F46IT #1 Block/Unblock	0-1	—
420	—	4418	N/A	F46IT #2 Block/Unblock	0-1	—
421	—	4418	N/A	F46IT #3 Block/Unblock	0-1	—
422	—	4418	N/A	F46IT #4 Block/Unblock	0-1	—
423	—	4418	N/A	F46IT #5 Block/Unblock	0-1	—
424	—	4414	N/A	F50P #1 Ph-A Block/Unblock	0-1	—
425	—	4414	N/A	F50P #1 Ph-B Block/Unblock	0-1	—
426	—	4414	N/A	F50P #1 Ph-C Block/Unblock	0-1	—
427	—	4414	N/A	F50P #1 All Phases Block/Unblock	0-1	—
428	—	4415	N/A	F50P #2 Ph-A Block/Unblock	0-1	—
429	—	4415	N/A	F50P #2 Ph-B Block/Unblock	0-1	—
430	—	4415	N/A	F50P #2 Ph-C Block/Unblock	0-1	—
431	—	4415	N/A	F50P #2 All Phases Block/Unblock	0-1	—
432	—	4415	N/A	F50P #3 Ph-A Block/Unblock	0-1	—
433	—	4415	N/A	F50P #3 Ph-B Block/Unblock	0-1	—
434	—	4415	N/A	F50P #3 Ph-C Block/Unblock	0-1	—
435	—	4415	N/A	F50P #3 All Phases Block/Unblock	0-1	—
436	—	4415	N/A	F50P #4 Ph-A Block/Unblock	0-1	—
437	—	4415	N/A	F50P #4 Ph-B Block/Unblock	0-1	—
438	—	4415	N/A	F50P #4 Ph-C Block/Unblock	0-1	—
439	—	4415	N/A	F50P #4 All Phases Block/Unblock	0-1	—
440	—	4415	N/A	F50P #5 Ph-A Block/Unblock	0-1	—
441	—	4415	N/A	F50P #5 Ph-B Block/Unblock	0-1	—
442	—	4415	N/A	F50P #5 Ph-C Block/Unblock	0-1	—

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
443	–	4415	N/A	F50P #5 All Phases Block/Unblock	0-1	–
444	–	4415	N/A	F50N #1 Block/Unblock	0-1	–
445	–	4415	N/A	F50N #2 Block/Unblock	0-1	–
446	–	4415	N/A	F50N #3 Block/Unblock	0-1	–
447	–	4415	N/A	F50N #4 Block/Unblock	0-1	–
448	–	4416	N/A	F50N #5 Block/Unblock	0-1	–
449	–	4416	N/A	F50G #1 Block/Unblock	0-1	–
450	–	4416	N/A	F50G #2 Block/Unblock	0-1	–
451	–	4416	N/A	F50G #3 Block/Unblock	0-1	–
452	–	4416	N/A	F50G #4 Block/Unblock	0-1	–
453	–	4416	N/A	F50G #5 Block/Unblock	0-1	–
454	–	4416	N/A	F50BF Block/Unblock	0-1	–
455	–	4416	N/A	F51P #1 Ph-A Block/Unblock	0-1	–
456	–	4416	N/A	F51P #1 Ph-B Block/Unblock	0-1	–
457	–	4416	N/A	F51P #1 Ph-C Block/Unblock	0-1	–
458	–	4416	N/A	F51P #1 All Phases Block/Unblock	0-1	–
459	–	4416	N/A	F51P #2 Ph-A Block/Unblock	0-1	–
460	–	4417	N/A	F51P #2 Ph-B Block/Unblock	0-1	–
461	–	4417	N/A	F51P #2 Ph-C Block/Unblock	0-1	–
462	–	4474	N/A	F51P #2 All Phases Block/Unblock	0-1	–
463	–	4417	N/A	F51P #3 Ph-A Block/Unblock	0-1	–
464	–	4417	N/A	F51P #3 Ph-B Block/Unblock	0-1	–
465	–	4417	N/A	F51P #3 Ph-C Block/Unblock	0-1	–
466	–	4417	N/A	F51P #3 All Phases Block/Unblock	0-1	–
467	–	4417	N/A	F51P #4 Ph-A Block/Unblock	0-1	–
468	–	4417	N/A	F51P #4 Ph-B Block/Unblock	0-1	–
469	–	4417	N/A	F51P #4 Ph-C Block/Unblock	0-1	–
470	–	4417	N/A	F51P #4 All Phases Block/Unblock	0-1	–
471	–	4417	N/A	F51P #5 Ph-A Block/Unblock	0-1	–
472	–	4417	N/A	F51P #5 Ph-B Block/Unblock	0-1	–
473	–	4417	N/A	F51P #5 Ph-C Block/Unblock	0-1	–
474	–	4417	N/A	F51P #5 All Phases Block/Unblock	0-1	–
475	–	4417	N/A	F51N #1 Block/Unblock	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
476	–	4417	N/A	F51N #2 Block/Unblock	0-1	–
477	–	4417	N/A	F51N #3 Block/Unblock	0-1	–
478	–	4417	N/A	F51N #4 Block/Unblock	0-1	–
479	–	4417	N/A	F51N #5 Block/Unblock	0-1	–
480	–	4418	N/A	F51G #1 Block/Unblock	0-1	–
481	–	4418	N/A	F51G #2 Block/Unblock	0-1	–
482	–	4418	N/A	F51G #3 Block/Unblock	0-1	–
483	–	4418	N/A	F51G #4 Block/Unblock	0-1	–
484	–	4418	N/A	F51G #5 Block/Unblock	0-1	–
485	–	4421	N/A	F81 #1 Block/Unblock	0-1	–
486	–	4421	N/A	F81 #2 Block/Unblock	0-1	–
487	–	4421	N/A	F81 #3 Block/Unblock	0-1	–
488	–	4422	N/A	F81 #4 Block/Unblock	0-1	–
489	–	4433	N/A	F81 #5 Block/Unblock	0-1	–
490	–	4434	N/A	F81 #6 Block/Unblock	0-1	–
491	–	4434	N/A	F81 #7 Block/Unblock	0-1	–
492	–	4434	N/A	F81 #8 Block/Unblock	0-1	–
493	–	4474	N/A	All F81 Block/Unblock	0-1	–
494	–	3942	N/A	Over Freq Block/Unblock	0-1	–
495	–	3942	N/A	Under Freq Block/Unblock	0-1	–
496	–	4422	N/A	F81R #1 Block/Unblock	0-1	–
497	–	4422	N/A	F81R #2 Block/Unblock	0-1	–
498	–	4434	N/A	F81R #3 Block/Unblock	0-1	–
499	–	4441	N/A	F81R #4 Block/Unblock	0-1	–
500	–	4441	N/A	F81R #5 Block/Unblock	0-1	–
501	–	4441	N/A	F81R #6 Block/Unblock	0-1	–
502	–	4441	N/A	F81R #7 Block/Unblock	0-1	–
503	–	4441	N/A	F81R #8 Block/Unblock	0-1	–
504	–	4474	N/A	All F81R Block/Unblock	0-1	–
505	–	4474	N/A	All Freq Funcs Block/Unblock	0-1	–
506	–	4414	N/A	F32 #1 Ph-A Block/Unblock	0-1	–
507	–	4414	N/A	F32 #1 Ph-B Block/Unblock	0-1	–
508	–	4414	N/A	F32 #1 Ph-C Block/Unblock	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
509	–	4414	N/A	F32 #1 All Phases Block/Unblock	0-1	–
510	–	4414	N/A	F32 #2 Ph-A Block/Unblock	0-1	–
511	–	4414	N/A	F32 #2 Ph-B Block/Unblock	0-1	–
512	–	4414	N/A	F32 #2 Ph-C Block/Unblock	0-1	–
513	–	4414	N/A	F32 #2 All Phases Block/Unblock	0-1	–
514	–	4414	N/A	F32 #3 Ph-A Block/Unblock	0-1	–
515	–	4414	N/A	F32 #3 Ph-B Block/Unblock	0-1	–
516	–	4414	N/A	F32 #3 Ph-C Block/Unblock	0-1	–
517	–	4414	N/A	F32 #3 All Phases Block/Unblock	0-1	–
518	–	4414	N/A	F32 #4 Ph-A Block/Unblock	0-1	–
519	–	4414	N/A	F32 #4 Ph-B Block/Unblock	0-1	–
520	–	4414	N/A	F32 #4 Ph-C Block/Unblock	0-1	–
521	–	4414	N/A	F32 #4 All Phases Block/Unblock	0-1	–
522	–	3942	N/A	All F67 Block/Unblock	0-1	–
523	–	4419	N/A	F67 #1 Ph-A Block/Unblock	0-1	–
524	–	4419	N/A	F67 #1 Ph-B Block/Unblock	0-1	–
525	–	4419	N/A	F67 #1 Ph-C Block/Unblock	0-1	–
526	–	4419	N/A	F67 #1 All Phases Block/Unblock	0-1	–
527	–	4419	N/A	F67 #2 Ph-A Block/Unblock	0-1	–
528	–	4420	N/A	F67 #2 Ph-B Block/Unblock	0-1	–
529	–	4420	N/A	F67 #2 Ph-C Block/Unblock	0-1	–
530	–	4474	N/A	F67 #2 All Phases Block/Unblock	0-1	–
531	–	4420	N/A	F67 #3 Ph-A Block/Unblock	0-1	–
532	–	4420	N/A	F67 #3 Ph-B Block/Unblock	0-1	–
533	–	4420	N/A	F67 #3 Ph-C Block/Unblock	0-1	–
534	–	4420	N/A	F67 #3 All Phases Block/Unblock	0-1	–
535	–	4420	N/A	F67 #4 Ph-A Block/Unblock	0-1	–
536	–	4420	N/A	F67 #4 Ph-B Block/Unblock	0-1	–
537	–	4420	N/A	F67 #4 Ph-C Block/Unblock	0-1	–
538	–	4420	N/A	F67 #4 All Phases Block/Unblock	0-1	–
539	–	4420	N/A	F67 #5 Ph-A Block/Unblock	0-1	–
540	–	4420	N/A	F67 #5 Ph-B Block/Unblock	0-1	–
541	–	4420	N/A	F67 #5 Ph-C Block/Unblock	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
542	–	4420	N/A	F67 #5 All Phases Block/Unblock	0-1	–
543	–	4420	N/A	F67N #1 Block/Unblock	0-1	–
544	–	4420	N/A	F67N #2 Block/Unblock	0-1	–
545	–	4420	N/A	F67N #3 Block/Unblock	0-1	–
546	–	4420	N/A	F67N #4 Block/Unblock	0-1	–
547	–	4420	N/A	F67N #5 Block/Unblock	0-1	–
548	–	4421	N/A	F67G #1 Block/Unblock	0-1	–
549	–	4421	N/A	F67G #2 Block/Unblock	0-1	–
550	–	4421	N/A	F67G #3 Block/Unblock	0-1	–
551	–	4421	N/A	F67G #4 Block/Unblock	0-1	–
552	–	4421	N/A	F67G #5 Block/Unblock	0-1	–
553	–	4421	N/A	F67Q #1 Block/Unblock	0-1	–
554	–	4421	N/A	F67Q #2 Block/Unblock	0-1	–
555	–	4421	N/A	F67Q #3 Block/Unblock	0-1	–
556	–	4421	N/A	F67Q #4 Block/Unblock	0-1	–
557	–	4421	N/A	F67Q #5 Block/Unblock	0-1	–
558	–	3942	N/A	F25 Synchrocheck Magnitude Block/Unblock	0-1	–
559	–	3942	N/A	F25 Synchrocheck Angle Block/Unblock	0-1	–
560	–	3942	N/A	F25 Synchrocheck Freq Block/Unblock	0-1	–
561	–	4421	N/A	F25 Block/Unblock	0-1	–
562	–	3942	N/A	F79 Seq Coordination Block/Unblock	0-1	–
563	–	3942	N/A	F79/50P HCL Block/Unblock	0-1	–
564	–	3942	N/A	F79/50N or F79/50G Block/Unblock	0-1	–
565	–	3942	N/A	F79/50P And F79/50G/50N HCL Block/Unblock	0-1	–
566	–	4421	N/A	F74 Trip Coil Block/Unblock	0-1	–
567	–	4421	N/A	F74 Close Coil Block/Unblock	0-1	–
568	–	4422	N/A	F60FL Fuse Failure Block/Unblock	0-1	–
569	–	7086	N/A	F27 #1 Ph-A Enable/Disable	0-1	–
570	–	7091	N/A	F27 #1 Ph-B Enable/Disable	0-1	–
571	–	7096	N/A	F27 #1 Ph-C Enable/Disable	0-1	–
572	–	7062	N/A	F27 #1 3-Ph Enable/Disable	0-1	–
573	–	7101	N/A	F27 #2 Ph-A Enable/Disable	0-1	–
574	–	7106	N/A	F27 #2 Ph-B Enable/Disable	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
575	–	7111	N/A	F27 #2 Ph-C Enable/Disable	0-1	–
576	–	7068	N/A	F27 #2 3-Ph Enable/Disable	0-1	–
577	–	7116	N/A	F27 #3 Ph-A Enable/Disable	0-1	–
578	–	7121	N/A	F27 #3 Ph-B Enable/Disable	0-1	–
579	–	7126	N/A	F27 #3 Ph-C Enable/Disable	0-1	–
580	–	7074	N/A	F27 #3 3-Ph Enable/Disable	0-1	–
581	–	7131	N/A	F27 #4 Ph-A Enable/Disable	0-1	–
582	–	7136	N/A	F27 #4 Ph-B Enable/Disable	0-1	–
583	–	7141	N/A	F27 #4 Ph-C Enable/Disable	0-1	–
584	–	7080	N/A	F27 #4 3-Ph Enable/Disable	0-1	–
585	–	46352	N/A	F27 #5 3-Ph Enable/Disable	0-1	–
586	–	46357	N/A	F27 #6 3-Ph Enable/Disable	0-1	–
587	–	46362	N/A	F27 #7 3-Ph Enable/Disable	0-1	–
588	–	46367	N/A	F27 #8 3-Ph Enable/Disable	0-1	–
589	–	8702	N/A	F27PP Ph-A Enable/Disable	0-1	–
590	–	8707	N/A	F27PP Ph-B Enable/Disable	0-1	–
591	–	8712	N/A	F27PP Ph-C Enable/Disable	0-1	–
592	–	8696	N/A	F27PP 3-Ph Enable/Disable	0-1	–
593	–	31013	N/A	27 BSVS Ph-A Enable/Disable	0-1	–
594	–	31016	N/A	27 BSVS Ph-B Enable/Disable	0-1	–
595	–	31019	N/A	27 BSVS Ph-C Enable/Disable	0-1	–
596	–	8500	N/A	27 BSVS 3-Ph Enable/Disable	0-1	–
597	–	8026	N/A	F59 #1 Ph-A Enable/Disable	0-1	–
598	–	8031	N/A	F59 #1 Ph-B Enable/Disable	0-1	–
599	–	8036	N/A	F59 #1 Ph-C Enable/Disable	0-1	–
600	–	8002	N/A	F59 #1 3-Ph Enable/Disable	0-1	–
601	–	8041	N/A	F59 #2 Ph-A Enable/Disable	0-1	–
602	–	8046	N/A	F59 #2 Ph-B Enable/Disable	0-1	–
603	–	8051	N/A	F59 #2 Ph-C Enable/Disable	0-1	–
604	–	8008	N/A	F59 #2 3-Ph Enable/Disable	0-1	–
605	–	8056	N/A	F59 #3 Ph-A Enable/Disable	0-1	–
606	–	8061	N/A	F59 #3 Ph-B Enable/Disable	0-1	–
607	–	8066	N/A	F59 #3 Ph-C Enable/Disable	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
608	—	8014	N/A	F59 #3 3-Ph Enable/Disable	0-1	—
609	—	8071	N/A	F59 #4 Ph-A Enable/Disable	0-1	—
610	—	8076	N/A	F59 #4 Ph-B Enable/Disable	0-1	—
611	—	8081	N/A	F59 #4 Ph-C Enable/Disable	0-1	—
612	—	8020	N/A	F59 #4 3-Ph Enable/Disable	0-1	—
613	—	46372	N/A	F59 #5 3-Ph Enable/Disable	0-1	—
614	—	46377	N/A	F59 #6 3-Ph Enable/Disable	0-1	—
615	—	46382	N/A	F59 #7 3-Ph Enable/Disable	0-1	—
616	—	46387	N/A	F59 #8 3-Ph Enable/Disable	0-1	—
617	—	8750	N/A	F59PP Ph-A Enable/Disable	0-1	—
618	—	8755	N/A	F59PP Ph-B Enable/Disable	0-1	—
619	—	8760	N/A	F59PP Ph-C Enable/Disable	0-1	—
620	—	8744	N/A	F59PP 3-Ph Enable/Disable	0-1	—
621	—	8128	N/A	F59N #1 Enable/Disable	0-1	—
622	—	8134	N/A	F59N #2 Enable/Disable	0-1	—
623	—	8112	N/A	47 Negative Seq Overvoltage Enable/Disable	0-1	—
624	—	9150	N/A	59I Ph-A Enable/Disable	0-1	—
625	—	9155	N/A	59I Ph-B Enable/Disable	0-1	—
626	—	9160	N/A	59I Ph-C Enable/Disable	0-1	—
627	—	9144	N/A	59I 3-Ph Enable/Disable	0-1	—
628	—	7304	N/A	F50P #1 Ph-A Enable/Disable	0-1	—
629	—	7309	N/A	F50P #1 Ph-B Enable/Disable	0-1	—
630	—	7314	N/A	F50P #1 Ph-C Enable/Disable	0-1	—
631	—	7274	N/A	F50P #1 3-Ph Enable/Disable	0-1	—
632	—	7319	N/A	F50P #2 Ph-A Enable/Disable	0-1	—
633	—	7324	N/A	F50P #2 Ph-B Enable/Disable	0-1	—
634	—	7329	N/A	F50P #2 Ph-C Enable/Disable	0-1	—
635	—	7280	N/A	F50P #2 3-Ph Enable/Disable	0-1	—
636	—	7334	N/A	F50P #3 Ph-A Enable/Disable	0-1	—
637	—	7339	N/A	F50P #3 Ph-B Enable/Disable	0-1	—
638	—	7344	N/A	F50P #3 Ph-C Enable/Disable	0-1	—
639	—	7286	N/A	F50P #3 3-Ph Enable/Disable	0-1	—
640	—	7349	N/A	F50P #4 Ph-A Enable/Disable	0-1	—

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
641	–	7354	N/A	F50P #4 Ph-B Enable/Disable	0-1	–
642	–	7359	N/A	F50P #4 Ph-C Enable/Disable	0-1	–
643	–	7292	N/A	F50P #4 3-Ph Enable/Disable	0-1	–
644	–	7364	N/A	F50P #5 Ph-A Enable/Disable	0-1	–
645	–	7369	N/A	F50P #5 Ph-B Enable/Disable	0-1	–
646	–	7374	N/A	F50P #5 Ph-C Enable/Disable	0-1	–
647	–	7298	N/A	F50P #5 3-Ph Enable/Disable	0-1	–
648	–	7462	N/A	F46DT #1 Enable/Disable	0-1	–
649	–	7467	N/A	F46DT #2 Enable/Disable	0-1	–
650	–	7472	N/A	F46DT #3 Enable/Disable	0-1	–
651	–	7477	N/A	F46DT #4 Enable/Disable	0-1	–
652	–	7482	N/A	F46DT #5 Enable/Disable	0-1	–
653	–	7426	N/A	F50G #1 Enable/Disable	0-1	–
654	–	7431	N/A	F50G #2 Enable/Disable	0-1	–
655	–	7436	N/A	F50G #3 Enable/Disable	0-1	–
656	–	7441	N/A	F50G #4 Enable/Disable	0-1	–
657	–	7446	N/A	F50G #5 Enable/Disable	0-1	–
658	–	8856	N/A	F50GS #1 Enable/Disable	0-1	–
659	–	8861	N/A	F50GS #2 Enable/Disable	0-1	–
660	–	8866	N/A	F50GS #3 Enable/Disable	0-1	–
661	–	8871	N/A	F50GS #4 Enable/Disable	0-1	–
662	–	8876	N/A	F50GS #5 Enable/Disable	0-1	–
663	–	7390	N/A	F50N #1 Enable/Disable	0-1	–
664	–	7395	N/A	F50N #2 Enable/Disable	0-1	–
665	–	7400	N/A	F50N #3 Enable/Disable	0-1	–
666	–	7405	N/A	F50N #4 Enable/Disable	0-1	–
667	–	7410	N/A	F50N #5 Enable/Disable	0-1	–
668	–	7568	N/A	F51P #1 Ph-A Enable/Disable	0-1	–
669	–	7581	N/A	F51P #1 Ph-B Enable/Disable	0-1	–
670	–	7594	N/A	F51P #1 Ph-C Enable/Disable	0-1	–
671	–	7498	N/A	F51P #1 3-Ph Enable/Disable	0-1	–
672	–	7607	N/A	F51P #2 Ph-A Enable/Disable	0-1	–
673	–	7620	N/A	F51P #2 Ph-B Enable/Disable	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
674	–	7633	N/A	F51P #2 Ph-C Enable/Disable	0-1	–
675	–	7512	N/A	F51P #2 3-Ph Enable/Disable	0-1	–
676	–	7646	N/A	F51P #3 Ph-A Enable/Disable	0-1	–
677	–	7659	N/A	F51P #3 Ph-B Enable/Disable	0-1	–
678	–	7672	N/A	F51P #3 Ph-C Enable/Disable	0-1	–
679	–	7526	N/A	F51P #3 3-Ph Enable/Disable	0-1	–
680	–	7685	N/A	F51P #4 Ph-A Enable/Disable	0-1	–
681	–	7698	N/A	F51P #4 Ph-B Enable/Disable	0-1	–
682	–	7711	N/A	F51P #4 Ph-C Enable/Disable	0-1	–
683	–	7540	N/A	F51P #4 3-Ph Enable/Disable	0-1	–
684	–	7724	N/A	F51P #5 Ph-A Enable/Disable	0-1	–
685	–	7737	N/A	F51P #5 Ph-B Enable/Disable	0-1	–
686	–	7750	N/A	F51P #5 Ph-C Enable/Disable	0-1	–
687	–	7554	N/A	F51P #5 3-Ph Enable/Disable	0-1	–
688	–	7926	N/A	F46IT #1 Enable/Disable	0-1	–
689	–	7939	N/A	F46IT #2 Enable/Disable	0-1	–
690	–	7952	N/A	F46IT #3 Enable/Disable	0-1	–
691	–	7965	N/A	F46IT #4 Enable/Disable	0-1	–
692	–	7978	N/A	F46IT #5 Enable/Disable	0-1	–
693	–	7850	N/A	F51G #1 Enable/Disable	0-1	–
694	–	7863	N/A	F51G #2 Enable/Disable	0-1	–
695	–	7876	N/A	F51G #3 Enable/Disable	0-1	–
696	–	7899	N/A	F51G #4 Enable/Disable	0-1	–
697	–	7902	N/A	F51G #5 Enable/Disable	0-1	–
698	–	8892	N/A	F51GS #1 Enable/Disable	0-1	–
699	–	8905	N/A	F51GS #2 Enable/Disable	0-1	–
700	–	8918	N/A	F51GS #3 Enable/Disable	0-1	–
701	–	8931	N/A	F51GS #4 Enable/Disable	0-1	–
702	–	8944	N/A	F51GS #5 Enable/Disable	0-1	–
703	–	7774	N/A	F51N #1 Enable/Disable	0-1	–
704	–	7787	N/A	F51N #2 Enable/Disable	0-1	–
705	–	7800	N/A	F51N #3 Enable/Disable	0-1	–
706	–	7813	N/A	F51N #4 Enable/Disable	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
707	–	7826	N/A	F51N #5 Enable/Disable	0-1	–
708	–	8189	N/A	F67 #1 Ph-A Enable/Disable	0-1	–
709	–	8229	N/A	F67 #1 Ph-B Enable/Disable	0-1	–
710	–	8269	N/A	F67 #1 Ph-C Enable/Disable	0-1	–
711	–	8144	N/A	F67 #1 3-Ph Enable/Disable	0-1	–
712	–	8197	N/A	F67 #2 Ph-A Enable/Disable	0-1	–
713	–	8237	N/A	F67 #2 Ph-B Enable/Disable	0-1	–
714	–	8277	N/A	F67 #2 Ph-C Enable/Disable	0-1	–
715	–	8153	N/A	F67 #2 3-Ph Enable/Disable	0-1	–
716	–	8205	N/A	F67 #3 Ph-A Enable/Disable	0-1	–
717	–	8245	N/A	F67 #3 Ph-B Enable/Disable	0-1	–
718	–	8585	N/A	F67 #3 Ph-C Enable/Disable	0-1	–
719	–	8162	N/A	F67 #3 3-Ph Enable/Disable	0-1	–
720	–	8213	N/A	F67 #4 Ph-A Enable/Disable	0-1	–
721	–	8253	N/A	F67 #4 Ph-B Enable/Disable	0-1	–
722	–	8293	N/A	F67 #4 Ph-C Enable/Disable	0-1	–
723	–	8171	N/A	F67 #4 3-Ph Enable/Disable	0-1	–
724	–	8221	N/A	F67 #5 Ph-A Enable/Disable	0-1	–
725	–	8261	N/A	F67 #5 Ph-B Enable/Disable	0-1	–
726	–	8301	N/A	F67 #5 Ph-C Enable/Disable	0-1	–
727	–	8180	N/A	F67 #5 3-Ph Enable/Disable	0-1	–
728	–	8420	N/A	F67Q #1 Enable/Disable	0-1	–
729	–	8428	N/A	F67Q #2 Enable/Disable	0-1	–
730	–	8436	N/A	F67Q #3 Enable/Disable	0-1	–
731	–	8444	N/A	F67Q #4 Enable/Disable	0-1	–
732	–	8452	N/A	F67Q #5 Enable/Disable	0-1	–
733	–	8370	N/A	F67G #1 Enable/Disable	0-1	–
734	–	8378	N/A	F67G #2 Enable/Disable	0-1	–
735	–	8386	N/A	F67G #3 Enable/Disable	0-1	–
736	–	8394	N/A	F67G #4 Enable/Disable	0-1	–
737	–	8402	N/A	F67G #5 Enable/Disable	0-1	–
738	–	8968	N/A	F67GS #1 Enable/Disable	0-1	–
739	–	8976	N/A	F67GS #2 Enable/Disable	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
740	–	8984	N/A	F67GS #3 Enable/Disable	0-1	–
741	–	8992	N/A	F67GS #4 Enable/Disable	0-1	–
742	–	9000	N/A	F67GS #5 Enable/Disable	0-1	–
743	–	8320	N/A	F67N #1 Enable/Disable	0-1	–
744	–	8328	N/A	F67N #2 Enable/Disable	0-1	–
745	–	8336	N/A	F67N #3 Enable/Disable	0-1	–
746	–	8344	N/A	F67N #4 Enable/Disable	0-1	–
747	–	8352	N/A	F67N #5 Enable/Disable	0-1	–
748	–	7022	N/A	F25 Enable/Disable	0-1	–
749	–	7184	N/A	F32 #1 Ph-A Enable/Disable	0-1	–
750	–	7191	N/A	F32 #1 Ph-B Enable/Disable	0-1	–
751	–	7198	N/A	F32 #1 Ph-C Enable/Disable	0-1	–
752	–	7152	N/A	F32 #1 3-Ph Enable/Disable	0-1	–
753	–	7205	N/A	F32 #2 Ph-A Enable/Disable	0-1	–
754	–	7212	N/A	F32 #2 Ph-B Enable/Disable	0-1	–
755	–	7219	N/A	F32 #2 Ph-C Enable/Disable	0-1	–
756	–	7160	N/A	F32 #2 3-Ph Enable/Disable	0-1	–
757	–	7226	N/A	F32 #3 Ph-A Enable/Disable	0-1	–
758	–	7233	N/A	F32 #3 Ph-B Enable/Disable	0-1	–
759	–	7240	N/A	F32 #3 Ph-C Enable/Disable	0-1	–
760	–	7168	N/A	F32 #3 3-Ph Enable/Disable	0-1	–
761	–	7247	N/A	F32 #4 Ph-A Enable/Disable	0-1	–
762	–	7254	N/A	F32 #4 Ph-B Enable/Disable	0-1	–
763	–	7261	N/A	F32 #4 Ph-C Enable/Disable	0-1	–
764	–	7176	N/A	F32 #4 3-Ph Enable/Disable	0-1	–
765	–	8814	N/A	F50BF Enable/Disable	0-1	–
766	–	8564	N/A	F81 #1 Enable/Disable	0-1	–
767	–	8573	N/A	F81 #2 Enable/Disable	0-1	–
768	–	8582	N/A	F81 #3 Enable/Disable	0-1	–
769	–	8591	N/A	F81 #4 Enable/Disable	0-1	–
770	–	46712	N/A	F81 #5 Enable/Disable	0-1	–
771	–	46721	N/A	F81 #6 Enable/Disable	0-1	–
772	–	46730	N/A	F81 #7 Enable/Disable	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
773	–	46739	N/A	F81 #8 Enable/Disable	0-1	–
774	–	8674	N/A	F81R #1 Enable/Disable	0-1	–
775	–	8683	N/A	F81R #2 Enable/Disable	0-1	–
776	–	46748	N/A	F81R #3 Enable/Disable	0-1	–
777	–	46758	N/A	F81R #4 Enable/Disable	0-1	–
778	–	46768	N/A	F81R #5 Enable/Disable	0-1	–
779	–	46778	N/A	F81R #6 Enable/Disable	0-1	–
780	–	46788	N/A	F81R #7 Enable/Disable	0-1	–
781	–	46798	N/A	F81R #8 Enable/Disable	0-1	–
782	–	47482	N/A	F50P Minimum Trip Pickup in HLT	0-1	–
783	–	47482	N/A	F50N Minimum Trip Pickup in HLT	0-1	–
784	–	47482	N/A	F50G/GS Minimum Trip Pickup in HLT	0-1	–
785	–	47482	N/A	F51P Minimum Trip Pickup in HLT	0-1	–
786	–	47482	N/A	F51N Minimum Trip Pickup in HLT	0-1	–
787	–	47482	N/A	F51G/GS Minimum Trip Pickup in HLT	0-1	–
788	–	46808	N/A	F46BC Enable/Disable	0-1	–
789	–	8479	N/A	Enable All HCL	0-1	–
790	–	8479	N/A	HCL Ph#1 Enable/Disable	0-1	–
791	–	8484	N/A	HCL Ph#2 Enable/Disable	0-1	–
792	–	8489	N/A	HCL Ph#3 Enable/Disable	0-1	–
793	–	8494	N/A	HCL Ph#4 Enable/Disable	0-1	–
794	–	8499	N/A	HCL Ph#5 Enable/Disable	0-1	–
795	–	8479	N/A	HCL N/G/GS#1 Enable/Disable	0-1	–
796	–	8484	N/A	HCL N/G/GS#2 Enable/Disable	0-1	–
797	–	8489	N/A	HCL N/G/GS#3 Enable/Disable	0-1	–
798	–	8494	N/A	HCL N/G/GS#4 Enable/Disable	0-1	–
799	–	8499	N/A	HCL N/G/GS#5 Enable/Disable	0-1	–
800	–	28061	N/A	Close Block 3-Ph Freq Enable/Disable	0-1	–
801	–	28061	N/A	Close Block 3-Ph Volt Upper Limit Enable/Disable	0-1	–
802	–	28061	N/A	Close Block 3-Ph Volt Lower Limit Enable/Disable	0-1	–
803	–	28061	N/A	Close Block 3-Ph Volt Upper and Lower Limit Enable/Disable	0-1	–
804	–	28070	N/A	Close Block Ph-A Freq Enable/Disable	0-1	–
805	–	28079	N/A	Close Block Ph-B Freq Enable/Disable	0-1	–

Table 2 Binary Output Points List (CROB)

Binary Output Points List (CROB)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
806	–	28088	N/A	Close Block Ph-C Freq Enable/Disable	0-1	–
807	–	28070	N/A	Close Block Ph-A Volt Upper Limit Enable/Disable	0-1	–
808	–	28079	N/A	Close Block Ph-B Volt Upper Limit Enable/Disable	0-1	–
809	–	28088	N/A	Close Block Ph-C Volt Upper Limit Enable/Disable	0-1	–
810	–	28070	N/A	Close Block Ph-A Volt Lower Limit Enable/Disable	0-1	–
811	–	28079	N/A	Close Block Ph-B Volt Lower Limit Enable/Disable	0-1	–
812	–	28088	N/A	Close Block Ph-C Volt Lower Limit Enable/Disable	0-1	–
813	–	28070	N/A	Close Block Ph-A Volt Upper and Lower Limit Enable/Disable	0-1	–
814	–	28079	N/A	Close Block Ph-B Volt Upper and Lower Limit Enable/Disable	0-1	–
815	–	28088	N/A	Close Block Ph-C Volt Upper and Lower Limit Enable/Disable	0-1	–
816	–	2002	N/A	Reset Peak Demands	0-1	–
817	–	1403	N/A	Reset Front Panel Targets ONLY	0-1	–
818	–	62	N/A	Source Orientation Vy	0-1	–
819	–	62	N/A	Source Orientation Vz	0-1	–
820	–	4701	N/A	Loop Scheme Reset	0-1	–
821	–	–	N/A	Close Block ABC Freq Enable/Disable	–	–
822	–	–	N/A	Close Block ABC Volt Upper Limit Enable/Disable	–	–
823	–	–	N/A	Close Block ABC Volt Lower Limit Enable/Disable	–	–
824	–	–	N/A	Close Block ABC Volt Upper and Lower Limit Enable/Disable	–	–
825	–	1538,1539	N/A	Activate Blocking Func. S10	–	–
826	–	1538,1539	N/A	Activate Blocking Func. S11	–	–
827	–	1538,1539	N/A	Activate Blocking Func. S12	–	–
828	–	1538,1539	N/A	Activate Blocking Func. S13	–	–
829	–	1538,1539	N/A	Activate Blocking Func. S14	–	–
830	–	1538,1539	N/A	Activate Blocking Func. S15	–	–
831	–	1538,1539	N/A	Activate Blocking Func. S16	–	–
832	–	1538,1539	N/A	Activate Blocking Func. S17	–	–
833	--	29127	N/A	SGL Enable Target Reset	–	–

Table 2 Binary Output Points List (CROB)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
0	0	1743	—	Current IP address	0-4294967295	—
1	0	1745	—	Current net mask address	0-4294967295	—
2	0	1747	—	Current gateway address	0-4294967295	—
3	1	31	Hz	Nominal Frequency	0-1	—
4	1	32	A	Secondary CT Rating	0-1	—
5	1	2200	—	Real component of Ph-A current phasor	—	—
6	1	2201	—	Reactive component of Ph-A current phasor	—	—
7	1	2202	—	Real component of Ph-B current phasor	—	—
8	1	2203	—	Reactive component of Ph-B current phasor	—	—
9	1	2204	—	Real component of Ph-C current phasor	—	—
10	1	2205	—	Reactive component of Ph-C current phasor	—	—
11	1	2206	—	Real component of ground current phasor	—	—
12	1	2207	—	Reactive component of ground current phasor	—	—
13	1	2208	—	Real component of Ph-A voltage phasor	—	—
14	1	2209	—	Reactive component of Ph-A voltage phasor	—	—
15	1	2210	—	Real component of Ph-B voltage phasor	—	—
16	1	2211	—	Reactive component of Ph-B voltage phasor	—	—
17	1	2212	—	Real component of Ph-C voltage phasor	—	—
18	1	2213	—	Reactive component of Ph-C voltage phasor	—	—
19	1	1940	—	Real component of Ph-B voltage phasor load side	—	—
20	1	1941	—	Reactive component of Ph-B voltage phasor load side	—	—
21	1	1956	—	Real component of Ph-C voltage phasor load side	—	—
22	1	1957	—	Reactive component of Ph-C voltage phasor load side	—	—
23	1	2214	—	Real component of Vz1 phasor/Ph-A load side	—	—
24	1	2215	—	Reactive component of Vz1 phasor/Ph-A load side	—	—
25	1	2216	—	Sec Magnitude of Positive Sequence V1 voltage	—	—
26	1	2217	—	Sec Magnitude of Negative Sequence V2 voltage	—	—
27	1	2218	—	Sec Magnitude of Zero Sequence V0 voltage	—	—
28	1	1972	—	Sec Magnitude of Positive Sequence V1 voltage load side	—	—
29	1	1973	—	Sec Magnitude of Negative Sequence V2 voltage load side	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
30	1	1974	—	Sec Magnitude of Zero Sequence V0 voltage load side	—	—
31	1	2219	—	Sec Magnitude of Positive Sequence I1 current	—	—
32	1	2220	—	Sec Magnitude of Negative Sequence I2 current	—	—
33	1	2221	—	Sec Magnitude of Zero Sequence I0 current	—	—
34	1	1975	—	Magnitude of Residual current	—	—
35	1	2222	Hz	Measured frequency	—	—
36	1	4654	Hz	Measured frequency on Z-side	—	—
37	1	2223	Hz/s	Rate of change of frequency	—	—
38	1	4655	Hz/s	Rate of change of frequency on Z-side	—	—
39	1	4657	ohm	Positive seq impedance, Real part	—	—
40	1	4659	ohm	Positive seq impedance, imaginary part	—	—
41	1	2225	W	Real Ph-A Power in Sec Watts	—	—
42	1	2227	var	Reactive Ph-A Power in Sec var	—	—
43	1	2229	W	Real Ph-B Power in Sec Watts	—	—
44	1	2231	var	Reactive Ph-B Power in Sec var	—	—
45	1	2233	W	Real Ph-C Power in Sec Watts	—	—
46	1	2235	var	Reactive Ph-C Power in Sec var	—	—
47	1	2237	W	Real average Power in Sec Watts	—	—
48	1	2239	var	Reactive average Power in Sec var	—	—
49	1	2241	VA	Apparent Ph-A Power in Sec VA	—	—
50	1	2243	VA	Apparent Ph-B Power in Sec VA	—	—
51	1	2245	VA	Apparent Ph-C Power in Sec VA	—	—
52	1	2247	VA	Apparent 3-PH Power in Sec VA	—	—
53	1	1884	deg	Phase Angle Voltage Phase A Y-side in deg	—	—
54	1	1885	deg	Phase Angle Voltage Phase B Y-side in deg	—	—
55	1	1886	deg	Phase Angle Voltage Phase C Y-side in deg	—	—
56	1	1887	deg	Phase Angle Voltage Ph-A Z-side/Vx in deg	—	—
57	1	1955	deg	Phase Angle Voltage Ph-B Z-side in deg	—	—
58	1	1971	deg	Phase Angle Voltage Ph-C Z-side in deg	—	—
59	1	1561	deg	Positive Seq. Voltage Ph Angle Y-side in deg	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
60	1	1562	deg	Negative Seq. Voltage Ph Angle Y-side in deg	–	–
61	1	1563	deg	Zero Seq. Voltage Ph Angle Y-side in deg	–	–
62	1	1564	deg	Positive Seq. Voltage Ph Angle Z-side in deg	–	–
63	1	1565	deg	Negative Seq. Voltage Ph Angle Z-side in deg	–	–
64	1	1566	deg	Zero Seq. Voltage Ph Angle Z-side in deg	–	–
65	1	1888	deg	Phase Angle Current Phase A in deg	–	–
66	1	1889	deg	Phase Angle Current Phase B in deg	–	–
67	1	1890	deg	Phase Angle Current Phase C in deg	–	–
68	1	1891	deg	Phase Angle Current Ground in deg	–	–
69	1	1558	deg	Positive Seq. Current Ph Angle in deg	–	–
70	1	1559	deg	Negative Seq. Current Ph Angle in deg	–	–
71	1	1560	deg	Zero Seq. Current Ph Angle in deg	–	–
72	1	2249	–	Phase A power factor	–	–
73	1	2250	–	Phase B power factor	–	–
74	1	2251	–	Phase C power factor	–	–
75	1	2252	–	Three Phase power factor	–	–
76	1	2275	A	Sec Phase A Current Magnitude	–	–
77	1	2276	A	Sec Phase B Current Magnitude	–	–
78	1	2277	A	Sec Phase C Current Magnitude	–	–
79	1	2278	A	Sec Ground Current Magnitude	–	–
80	1	2279	V	Sec Phase A Voltage Magnitude	–	–
81	1	2280	V	Sec Phase B Voltage Magnitude	–	–
82	1	2281	V	Sec Phase C Voltage Magnitude	–	–
83	1	1942	V	Sec Phase B Voltage Magnitude load side	–	–
84	1	1958	V	Sec Phase C Voltage Magnitude load side	–	–
85	1	2282	V	Sec Sync Voltage/Ph-A load side Magnitude	–	–
86	1	2283	A	Demand Phase A Current	–	–
87	1	2284	A	Demand Phase B Current	–	–
88	1	2285	A	Demand Phase C Current	–	–
89	1	2286	A	Demand Ground Current	–	–

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
90	1	2287	V	Demand Phase A Voltage	–	–
91	1	2288	V	Demand Phase B Voltage	–	–
92	1	2289	V	Demand Phase C Voltage	–	–
93	1	2290	V	Demand Sync/Ph-A load side Voltage	–	–
94	1	2291	–	Demand Phase A Real Power	–	–
95	1	2293	–	Demand Phase B Real Power	–	–
96	1	2295	–	Demand Phase C Real Power	–	–
97	1	2297	–	Demand 3-Phase Real Power	–	–
98	1	2299	–	Demand Phase A Reactive Power	–	–
99	1	2301	–	Demand Phase B Reactive Power	–	–
100	1	2303	–	Demand Phase C Reactive Power	–	–
101	1	2305	–	Demand 3-Phase Reactive Power	–	–
102	1	2307	–	Demand Phase A Apparent Power	–	–
103	1	2309	–	Demand Phase B Apparent Power	–	–
104	1	2311	–	Demand Phase C Apparent Power	–	–
105	1	2313	–	Demand 3-Phase Apparent Power	–	–
106	1	2315	A	Maximum Phase A Current	–	–
107	1	2316	A	Maximum Phase B Current	–	–
108	1	2317	A	Maximum Phase C Current	–	–
109	1	2318	A	Maximum Ground Current	–	–
110	1	2319	V	Maximum Phase A Voltage	–	–
111	1	2320	V	Maximum Phase B Voltage	–	–
112	1	2321	V	Maximum Phase C Voltage	–	–
113	1	2322	V	Maximum Sync/Ph-A load side Voltage	–	–
114	1	2323	W	Maximum Phase A Real Power	–	–
115	1	2325	W	Maximum Phase B Real Power	–	–
116	1	2327	W	Maximum Phase C Real Power	–	–
117	1	2329	W	Maximum 3-Phase Real Power	–	–
118	1	2331	var	Maximum Phase A Reactive Power	–	–
119	1	2333	var	Maximum Phase B Reactive Power	–	–

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
120	1	2335	var	Maximum Phase C Reactive Power	–	–
121	1	2337	var	Maximum 3-Phase Reactive Power	–	–
122	1	2339	VA	Maximum Phase A Apparent Power	–	–
123	1	2341	VA	Maximum Phase B Apparent Power	–	–
124	1	2343	VA	Maximum Phase C Apparent Power	–	–
125	1	2345	VA	Maximum 3-Phase Apparent Power	–	–
126	1	2347	–	Time of Max Ph-A Current Demand	–	–
127	1	2349	–	Time of Max Ph-B Current Demand	–	–
128	1	2351	–	Time of Max Ph-C Current Demand	–	–
129	1	2353	–	Time of Max Ground Current Demand	–	–
130	1	2355	–	Time of Max Ph-A Voltage Demand	–	–
131	1	2357	–	Time of Max Ph-B Voltage Demand	–	–
132	1	2359	–	Time of Max Ph-C Voltage Demand	–	–
133	1	2361	–	Time of Max sync/PhA load side Voltage Demand	–	–
134	1	2363	–	Time of Max Ph-A Real Power Demand	–	–
135	1	2365	–	Time of Max Ph-B Real Power Demand	–	–
136	1	2367	–	Time of Max Ph-C Real Power Demand	–	–
137	1	2369	–	Time of Max 3-Ph Real Power Demand	–	–
138	1	2371	–	Time of Max Ph-A Reactive Power Demand	–	–
139	1	2373	–	Time of Max Ph-B Reactive Power Demand	–	–
140	1	2375	–	Time of Max Ph-C Reactive Power Demand	–	–
141	1	2377	–	Time of Max 3-Ph Reactive Power Demand	–	–
142	1	2379	–	Time of Max Ph-A Apparent Power Demand	–	–
143	1	2381	–	Time of Max Ph-B Apparent Power Demand	–	–
144	1	2383	–	Time of Max Ph-C Apparent Power Demand	–	–
145	1	2385	–	Time of Max 3-Ph Apparent Power Demand	–	–
146	1	2387	A	Minimum Phase A Current	–	–
147	1	2388	A	Minimum Phase B Current	–	–
148	1	2389	A	Minimum Phase C Current	–	–
149	1	2390	A	Minimum Ground Current	–	–

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
150	1	2391	V	Minimum Phase A Voltage	–	–
151	1	2392	V	Minimum Phase B Voltage	–	–
152	1	2393	V	Minimum Phase C Voltage	–	–
153	1	2394	V	Minimum Sync/Ph-A load side Voltage	–	–
154	1	2395	W	Minimum Phase A Real Power	–	–
155	1	2397	W	Minimum Phase B Real Power	–	–
156	1	2399	W	Minimum Phase C Real Power	–	–
157	1	2401	W	Minimum 3-Phase Real Power	–	–
158	1	2403	var	Minimum Phase A Reactive Power	–	–
159	1	2405	var	Minimum Phase B Reactive Power	–	–
160	1	2407	var	Minimum Phase C Reactive Power	–	–
161	1	2409	var	Minimum 3-Phase Reactive Power	–	–
162	1	2411	VA	Minimum Phase A Apparent Power	–	–
163	1	2413	VA	Minimum Phase B Apparent Power	–	–
164	1	2415	VA	Minimum Phase C Apparent Power	–	–
165	1	2417	VA	Minimum 3-Phase Apparent Power	–	–
166	1	2419	–	Time of Min Ph-A Current Demand	–	–
167	1	2421	–	Time of Min Ph-B Current Demand	–	–
168	1	2423	–	Time of Min Ph-C Current Demand	–	–
169	1	2425	–	Time of Min Ground Current Demand	–	–
170	1	2427	–	Time of Min Ph-A Voltage Demand	–	–
171	1	2429	–	Time of Min Ph-B Voltage Demand	–	–
172	1	2431	–	Time of Min Ph-C Voltage Demand	–	–
173	1	2433	–	Time of Min Sync/Ph-A load side Voltage Demand	–	–
174	1	2435	–	Time of Min Ph-A Real Power Demand	–	–
175	1	2437	–	Time of Min Ph-B Real Power Demand	–	–
176	1	2439	–	Time of Min Ph-C Real Power Demand	–	–
177	1	2441	–	Time of Min 3-Ph Real Power Demand	–	–
178	1	2443	–	Time of Min Ph-A Reactive Power Demand	–	–
179	1	2445	–	Time of Min Ph-B Reactive Power Demand	–	–

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
180	1	2447	—	Time of Min Ph-C Reactive Power Demand	—	—
181	1	2449	—	Time of Min 3-Ph Reactive Power Demand	—	—
182	1	2451	—	Time of Min Ph-A Apparent Power Demand	—	—
183	1	2453	—	Time of Min Ph-B Apparent Power Demand	—	—
184	1	2455	—	Time of Min Ph-C Apparent Power Demand	—	—
185	1	2457	—	Time of Min 3-Ph Apparent Power Demand	—	—
186	1	1908	kWh	Sec Real Energy kWh Ph-A Fwd	—	—
187	1	2461	—	Time of Real Energy Ph-A Fwd	—	—
188	1	1910	kWh	Sec Real Energy kWh Ph-A Rev	—	—
189	1	2465	—	Time of Real Energy Ph-A Rev	—	—
190	1	1912	kVh	Sec Reactive Energy kVarh Ph-A Lead	—	—
191	1	2469	—	Time of Reactive Energy Ph-A Lead	—	—
192	1	1914	kVh	Sec Reactive Energy kVarh Ph-A Lag	—	—
193	1	2473		Time of Reactive Energy Ph-A Lag	—	—
194	1	1916	kWh	Sec Real Energy kWh Ph-B Fwd	—	—
195	1	2477		Time of Real Energy Ph-B Fwd	—	—
196	1	1918	kWh	Sec Real Energy kWh Ph-B Rev	—	—
197	1	2481	—	Time of Real Energy Ph-B Rev	—	—
198	1	1920	kVh	Sec Reactive Energy kVarh Ph-B Lead	—	—
199	1	2485	—	Time of Reactive Energy Ph-B Lead	—	—
200	1	1922	kVh	Sec Reactive Energy kVarh Ph-B Lag	—	—
201	1	2489	—	Time of Reactive Energy Ph-B Lag	—	—
202	1	1924	kWh	Sec Real Energy kWh Ph-C Fwd	—	—
203	1	2493	—	Time of Real Energy Ph-C Fwd	—	—
204	1	1926	kWh	Sec Real Energy kWh Ph-C Rev	—	—
205	1	2497	—	Time of Real Energy Ph-C Rev	—	—
206	1	1928	kVh	Sec Reactive Energy kVarh Ph-C Lead	—	—
207	1	2501	—	Time of Reactive Energy Ph-C Lead	—	—
208	1	1930	kVh	Sec Reactive Energy kVarh Ph-C Lag	—	—
209	1	2505		Time of Reactive Energy Ph-C Lag	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
210	1	4902	—	Sec Real Energy kWh 3-Ph Fwd	—	—
211	1	4904	—	Sec Real Energy kWh 3-Ph Rev	—	—
212	1	4906	—	Sec Reactive Energy kVarh 3-Ph Lead	—	—
213	1	4908	—	Sec Reactive Energy kVarh 3-Ph Lag	—	—
214	1	4918	—	Pri Real Energy kWh Ph-A Fwd	—	—
215	1	4924	—	Pri Real Energy kWh Ph-A Rev	—	—
216	1	4930	—	Pri Reactive Energy kVarh Ph-A Lead	—	—
217	1	4936	—	Pri Reactive Energy kVarh Ph-A Lag	—	—
218	1	4920	—	Pri Real Energy kWh Ph-B Fwd	—	—
219	1	4926	—	Pri Real Energy kWh Ph-B Rev	—	—
220	1	4932	—	Pri Reactive Energy kVarh Ph-B Lead	—	—
221	1	4938	—	Pri Reactive Energy kVarh Ph-B Lag	—	—
222	1	4922	—	Pri Real Energy kWh Ph-C Fwd	—	—
223	1	4928	—	Pri Real Energy kWh Ph-C Rev	—	—
224	1	4934	—	Pri Reactive Energy kVarh Ph-C Lead	—	—
225	1	4940	—	Pri Reactive Energy kVarh Ph-C Lag	—	—
226	1	4910	—	Pri Real Energy kWh 3-Ph Fwd	—	—
227	1	4912	—	Pri Real Energy kWh 3-Ph Rev	—	—
228	1	4914	—	Pri Reactive Energy kVarh 3-Ph Lead	—	—
229	1	4916	—	Pri Reactive Energy kVarh 3-Ph Lag	—	—
230	1	2461	—	Last Energy Reset Time	—	—
231	1	1800	A	Primary Phase A Current Magnitude	—	—
232	1	1802	A	Primary Phase B Current Magnitude	—	—
233	1	1804	A	Primary Phase C Current Magnitude	—	—
234	1	1806	A	Primary Ground Current Magnitude	—	—
235	1	1840	A	Primary Magnitude of Positive Sequence current	—	—
236	1	1842	A	Primary Magnitude of Negative Sequence current	—	—
237	1	1844	A	Primary Magnitude of Zero Sequence current	—	—
238	1	2012	—	Primary Magnitude of 3-Ph Residual current	—	—
239	1	1808	V	Primary Phase A Voltage Magnitude	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
240	1	1810	V	Primary Phase B Voltage Magnitude	–	–
241	1	1812	V	Primary Phase C Voltage Magnitude	–	–
242	1	1950	V	Primary Phase B Voltage Magnitude load side	–	–
243	1	1966	V	Primary Phase C Voltage Magnitude load side	–	–
244	1	1814	V	Primary Sync Voltage/Ph-A load side Magnitude	–	–
245	1	1846	V	Primary Magnitude of Positive Sequence voltage	–	–
246	1	1848	V	Primary Magnitude of Negative Sequence voltage	–	–
247	1	1850	V	Primary Magnitude of Zero Sequence voltage	–	–
248	1	4168	V	Primary Magnitude of Positive Sequence voltage load side	–	–
249	1	4170	V	Primary Magnitude of Negative Sequence voltage load side	–	–
250	1	4172	V	Primary Magnitude of Zero Sequence voltage load side	–	–
251	1	2047	–	Primary Magnitude of 3-Ph Residual voltage	–	–
252	1	1514	W	Primary Real Ph-A Power in Watts	–	–
253	1	1516	var	Primary Reactive Ph-A Power in var	–	–
254	1	1518	W	Primary Real Ph-B Power in Watts	–	–
255	1	1520	var	Primary Reactive Ph-B Power in var	–	–
256	1	1522	W	Primary Real Ph-C Power in Watts	–	–
257	1	1524	var	Primary Reactive Ph-C Power in var	–	–
258	1	1526	W	Primary Real average Power in Watts	–	–
259	1	1528	var	Primary Reactive average Power in var	–	–
260	1	1530	VA	Primary Magnitude of Ph-A apparent power in va	–	–
261	1	1532	VA	Primary Magnitude of Ph-B apparent power in va	–	–
262	1	1534	VA	Primary Magnitude of Ph-C apparent power in va	–	–
263	1	1536	VA	Primary Magnitude of average apparent power in va	–	–
264	1	3796	V	Battery Voltage	–	–
265	1	2513	–	Sequence Coordination	–	–
266	1	3769	%	Phase A current Total Harmonic Distortion	–	–
267	1	3384	%	Current 1st Harmonic A	–	–
268	1	3390	%	Current 2nd Harmonic A	–	–
269	1	3396	%	Current 3rd Harmonic A	–	–

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
270	1	3402	%	Current 4th Harmonic A	–	–
271	1	3408	%	Current 5th Harmonic A	–	–
272	1	3414	%	Current 6th Harmonic A	–	–
273	1	3420	%	Current 7th Harmonic A	–	–
274	1	3426	%	Current 8th Harmonic A	–	–
275	1	3432	%	Current 9th Harmonic A	–	–
276	1	3438	%	Current 10th Harmonic A	–	–
277	1	3444	%	Current 11th Harmonic A	–	–
278	1	3450	%	Current 12th Harmonic A	–	–
279	1	3456	%	Current 13th Harmonic A	–	–
280	1	3462	%	Current 14th Harmonic A	–	–
281	1	3468	%	Current 15th Harmonic A	–	–
282	1	3474	%	Current 16th Harmonic A	–	–
283	1	3480	%	Current 17th Harmonic A	–	–
284	1	3486	%	Current 18th Harmonic A	–	–
285	1	3492	%	Current 19th Harmonic A	–	–
286	1	3498	%	Current 20th Harmonic A	–	–
287	1	3504	%	Current 21st Harmonic A	–	–
288	1	3510	%	Current 22nd Harmonic A	–	–
289	1	3516	%	Current 23rd Harmonic A	–	–
290	1	3522	%	Current 24th Harmonic A	–	–
291	1	3528	%	Current 25th Harmonic A	–	–
292	1	3534	%	Current 26th Harmonic A	–	–
293	1	3540	%	Current 27th Harmonic A	–	–
294	1	3546	%	Current 28th Harmonic A	–	–
295	1	3552	%	Current 29th Harmonic A	–	–
296	1	3558	%	Current 30th Harmonic A	–	–
297	1	3564	%	Current 31st Harmonic A	–	–
298	1	3570	%	Current 32nd Harmonic A	–	–
299	1	3576	%	Current 33rd Harmonic A	–	–

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
300	1	3582	%	Current 34th Harmonic A	—	—
301	1	3588	%	Current 35th Harmonic A	—	—
302	1	3594	%	Current 36th Harmonic A	—	—
303	1	3600	%	Current 37th Harmonic A	—	—
304	1	3606	%	Current 38th Harmonic A	—	—
305	1	3612	%	Current 39th Harmonic A	—	—
306	1	3618	%	Current 40th Harmonic A	—	—
307	1	3624	%	Current 41st Harmonic A	—	—
308	1	3630	%	Current 42nd Harmonic A	—	—
309	1	3636	%	Current 43rd Harmonic A	—	—
310	1	3642	%	Current 44th Harmonic A	—	—
311	1	3648	%	Current 45th Harmonic A	—	—
312	1	3654	%	Current 46th Harmonic A	—	—
313	1	3660	%	Current 47th Harmonic A	—	—
314	1	3666	%	Current 48th Harmonic A	—	—
315	1	3672	%	Current 49th Harmonic A	—	—
316	1	3678	%	Current 50th Harmonic A	—	—
317	1	3684	%	Current 51st Harmonic A	—	—
318	1	3690	%	Current 52nd Harmonic A	—	—
319	1	3696	%	Current 53rd Harmonic A	—	—
320	1	3702	%	Current 54th Harmonic A	—	—
321	1	3708	%	Current 55th Harmonic A	—	—
322	1	3714	%	Current 56th Harmonic A	—	—
323	1	3720	%	Current 57th Harmonic A	—	—
324	1	3726	%	Current 58th Harmonic A	—	—
325	1	3732	%	Current 59th Harmonic A	—	—
326	1	3738	%	Current 60th Harmonic A	—	—
327	1	3744	%	Current 61st Harmonic A	—	—
328	1	3750	%	Current 62nd Harmonic A	—	—
329	1	3756	%	Current 63rd Harmonic A	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
330	1	3770	%	Phase B current Total Harmonic Distortion	—	—
331	1	3385	%	Current 1st Harmonic B	—	—
332	1	3391	%	Current 2nd Harmonic B	—	—
333	1	3397	%	Current 3rd Harmonic B	—	—
334	1	3403	%	Current 4th Harmonic B	—	—
335	1	3409	%	Current 5th Harmonic B	—	—
336	1	3415	%	Current 6th Harmonic B	—	—
337	1	3421	%	Current 7th Harmonic B	—	—
338	1	3427	%	Current 8th Harmonic B	—	—
339	1	3433	%	Current 9th Harmonic B	—	—
340	1	3439	%	Current 10th Harmonic B	—	—
341	1	3445	%	Current 11th Harmonic B	—	—
342	1	3451	%	Current 12th Harmonic B	—	—
343	1	3457	%	Current 13th Harmonic B	—	—
344	1	3463	%	Current 14th Harmonic B	—	—
345	1	3469	%	Current 15th Harmonic B	—	—
346	1	3475	%	Current 16th Harmonic B	—	—
347	1	3481	%	Current 17th Harmonic B	—	—
348	1	3487	%	Current 18th Harmonic B	—	—
349	1	3493	%	Current 19th Harmonic B	—	—
350	1	3499	%	Current 20th Harmonic B	—	—
351	1	3505	%	Current 21st Harmonic B	—	—
352	1	3511	%	Current 22nd Harmonic B	—	—
353	1	3517	%	Current 23rd Harmonic B	—	—
354	1	3523	%	Current 24th Harmonic B	—	—
355	1	3529	%	Current 25th Harmonic B	—	—
356	1	3535	%	Current 26th Harmonic B	—	—
357	1	3541	%	Current 27th Harmonic B	—	—
358	1	3547	%	Current 28th Harmonic B	—	—
359	1	3553	%	Current 29th Harmonic B	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
360	1	3559	%	Current 30th Harmonic B	–	–
361	1	3565	%	Current 31st Harmonic B	–	–
362	1	3571	%	Current 32nd Harmonic B	–	–
363	1	3577	%	Current 33rd Harmonic B	–	–
364	1	3583	%	Current 34th Harmonic B	–	–
365	1	3589	%	Current 35th Harmonic B	–	–
366	1	3595	%	Current 36th Harmonic B	–	–
367	1	3601	%	Current 37th Harmonic B	–	–
368	1	3607	%	Current 38th Harmonic B	–	–
369	1	3613	%	Current 39th Harmonic B	–	–
370	1	3619	%	Current 40th Harmonic B	–	–
371	1	3625	%	Current 41st Harmonic B	–	–
372	1	3631	%	Current 42nd Harmonic B	–	–
373	1	3637	%	Current 43rd Harmonic B	–	–
374	1	3643	%	Current 44th Harmonic B	–	–
375	1	3649	%	Current 45th Harmonic B	–	–
376	1	3655	%	Current 46th Harmonic B	–	–
377	1	3661	%	Current 47th Harmonic B	–	–
378	1	3667	%	Current 48th Harmonic B	–	–
379	1	3673	%	Current 49th Harmonic B	–	–
380	1	3679	%	Current 50th Harmonic B	–	–
381	1	3685	%	Current 51st Harmonic B	–	–
382	1	3691	%	Current 52nd Harmonic B	–	–
383	1	3697	%	Current 53rd Harmonic B	–	–
384	1	3703	%	Current 54th Harmonic B	–	–
385	1	3709	%	Current 55th Harmonic B	–	–
386	1	3715	%	Current 56th Harmonic B	–	–
387	1	3721	%	Current 57th Harmonic B	–	–
388	1	3727	%	Current 58th Harmonic B	–	–
389	1	3733	%	Current 59th Harmonic B	–	–

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
390	1	3739	%	Current 60th Harmonic B	—	—
391	1	3745	%	Current 61st Harmonic B	—	—
392	1	3751	%	Current 62nd Harmonic B	—	—
393	1	3757	%	Current 63rd Harmonic B	—	—
394	1	3771	%	Phase C current Total Harmonic Distortion	—	—
395	1	3386	%	Current 1st Harmonic C	—	—
396	1	3392	%	Current 2nd Harmonic C	—	—
397	1	3398	%	Current 3rd Harmonic C	—	—
398	1	3404	%	Current 4th Harmonic C	—	—
399	1	3410	%	Current 5th Harmonic C	—	—
400	1	3416	%	Current 6th Harmonic C	—	—
401	1	3422	%	Current 7th Harmonic C	—	—
402	1	3428	%	Current 8th Harmonic C	—	—
403	1	3434	%	Current 9th Harmonic C	—	—
404	1	3440	%	Current 10th Harmonic C	—	—
405	1	3446	%	Current 11th Harmonic C	—	—
406	1	3452	%	Current 12th Harmonic C	—	—
407	1	3458	%	Current 13th Harmonic C	—	—
408	1	3464	%	Current 14th Harmonic C	—	—
409	1	3470	%	Current 15th Harmonic C	—	—
410	1	3476	%	Current 16th Harmonic C	—	—
411	1	3482	%	Current 17th Harmonic C	—	—
412	1	3488	%	Current 18th Harmonic C	—	—
413	1	3494	%	Current 19th Harmonic C	—	—
414	1	3500	%	Current 20th Harmonic C	—	—
415	1	3506	%	Current 21st Harmonic C	—	—
416	1	3512	%	Current 22nd Harmonic C	—	—
417	1	3518	%	Current 23rd Harmonic C	—	—
418	1	3524	%	Current 24th Harmonic C	—	—
419	1	3530	%	Current 25th Harmonic C	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
420	1	3536	%	Current 26th Harmonic C	—	—
421	1	3542	%	Current 27th Harmonic C	—	—
422	1	3548	%	Current 28th Harmonic C	—	—
423	1	3554	%	Current 29th Harmonic C	—	—
424	1	3560	%	Current 30th Harmonic C	—	—
425	1	3566	%	Current 31st Harmonic C	—	—
426	1	3572	%	Current 32nd Harmonic C	—	—
427	1	3578	%	Current 33rd Harmonic C	—	—
428	1	3584	%	Current 34th Harmonic C	—	—
429	1	3590	%	Current 35th Harmonic C	—	—
430	1	3596	%	Current 36th Harmonic C	—	—
431	1	3602	%	Current 37th Harmonic C	—	—
432	1	3608	%	Current 38th Harmonic C	—	—
433	1	3614	%	Current 39th Harmonic C	—	—
434	1	3620	%	Current 40th Harmonic C	—	—
435	1	3626	%	Current 41st Harmonic C	—	—
436	1	3632	%	Current 42nd Harmonic C	—	—
437	1	3638	%	Current 43rd Harmonic C	—	—
438	1	3644	%	Current 44th Harmonic C	—	—
439	1	3650	%	Current 45th Harmonic C	—	—
440	1	3656	%	Current 46th Harmonic C	—	—
441	1	3662	%	Current 47th Harmonic C	—	—
442	1	3668	%	Current 48th Harmonic C	—	—
443	1	3674	%	Current 49th Harmonic C	—	—
444	1	3680	%	Current 50th Harmonic C	—	—
445	1	3686	%	Current 51st Harmonic C	—	—
446	1	3692	%	Current 52nd Harmonic C	—	—
447	1	3698	%	Current 53rd Harmonic C	—	—
448	1	3704	%	Current 54th Harmonic C	—	—
449	1	3710	%	Current 55th Harmonic C	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
450	1	3716	%	Current 56th Harmonic C	—	—
451	1	3722	%	Current 57th Harmonic C	—	—
452	1	3728	%	Current 58th Harmonic C	—	—
453	1	3734	%	Current 59th Harmonic C	—	—
454	1	3740	%	Current 60th Harmonic C	—	—
455	1	3746	%	Current 61st Harmonic C	—	—
456	1	3752	%	Current 62nd Harmonic C	—	—
457	1	3758	%	Current 63rd Harmonic C	—	—
458	1	3772	%	Phase A Voltage Total Harmonic Distortion	—	—
459	1	3387	%	Voltage 1st Harmonic A	—	—
460	1	3393	%	Voltage 2nd Harmonic A	—	—
461	1	3399	%	Voltage 3rd Harmonic A	—	—
462	1	3405	%	Voltage 4th Harmonic A	—	—
463	1	3411	%	Voltage 5th Harmonic A	—	—
464	1	3417	%	Voltage 6th Harmonic A	—	—
465	1	3423	%	Voltage 7th Harmonic A	—	—
466	1	3429	%	Voltage 8th Harmonic A	—	—
467	1	3435	%	Voltage 9th Harmonic A	—	—
468	1	3441	%	Voltage 10th Harmonic A	—	—
469	1	3447	%	Voltage 11th Harmonic A	—	—
470	1	3453	%	Voltage 12th Harmonic A	—	—
471	1	3459	%	Voltage 13th Harmonic A	—	—
472	1	3465	%	Voltage 14th Harmonic A	—	—
473	1	3471	%	Voltage 15th Harmonic A	—	—
474	1	3477	%	Voltage 16th Harmonic A	—	—
475	1	3483	%	Voltage 17th Harmonic A	—	—
476	1	3489	%	Voltage 18th Harmonic A	—	—
477	1	3495	%	Voltage 19th Harmonic A	—	—
478	1	3501	%	Voltage 20th Harmonic A	—	—
479	1	3507	%	Voltage 21st Harmonic A	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
480	1	3513	%	Voltage 22nd Harmonic A	—	—
481	1	3519	%	Voltage 23rd Harmonic A	—	—
482	1	3525	%	Voltage 24th Harmonic A	—	—
483	1	3531	%	Voltage 25th Harmonic A	—	—
484	1	3537	%	Voltage 26th Harmonic A	—	—
485	1	3543	%	Voltage 27th Harmonic A	—	—
486	1	3549	%	Voltage 28th Harmonic A	—	—
487	1	3555	%	Voltage 29th Harmonic A	—	—
488	1	3561	%	Voltage 30th Harmonic A	—	—
489	1	3567	%	Voltage 31st Harmonic A	—	—
490	1	3573	%	Voltage 32nd Harmonic A	—	—
491	1	3579	%	Voltage 33rd Harmonic A	—	—
492	1	3585	%	Voltage 34th Harmonic A	—	—
493	1	3591	%	Voltage 35th Harmonic A	—	—
494	1	3597	%	Voltage 36th Harmonic A	—	—
495	1	3603	%	Voltage 37th Harmonic A	—	—
496	1	3609	%	Voltage 38th Harmonic A	—	—
497	1	3615	%	Voltage 39th Harmonic A	—	—
498	1	3621	%	Voltage 40th Harmonic A	—	—
499	1	3627	%	Voltage 41st Harmonic A	—	—
500	1	3633	%	Voltage 42nd Harmonic A	—	—
501	1	3639	%	Voltage 43rd Harmonic A	—	—
502	1	3645	%	Voltage 44th Harmonic A	—	—
503	1	3651	%	Voltage 45th Harmonic A	—	—
504	1	3657	%	Voltage 46th Harmonic A	—	—
505	1	3663	%	Voltage 47th Harmonic A	—	—
506	1	3669	%	Voltage 48th Harmonic A	—	—
507	1	3675	%	Voltage 49th Harmonic A	—	—
508	1	3681	%	Voltage 50th Harmonic A	—	—
509	1	3687	%	Voltage 51st Harmonic A	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
510	1	3693	%	Voltage 52nd Harmonic A	—	—
511	1	3699	%	Voltage 53rd Harmonic A	—	—
512	1	3705	%	Voltage 54th Harmonic A	—	—
513	1	3711	%	Voltage 55th Harmonic A	—	—
514	1	3717	%	Voltage 56th Harmonic A	—	—
515	1	3723	%	Voltage 57th Harmonic A	—	—
516	1	3729	%	Voltage 58th Harmonic A	—	—
517	1	3735	%	Voltage 59th Harmonic A	—	—
518	1	3741	%	Voltage 60th Harmonic A	—	—
519	1	3747	%	Voltage 61st Harmonic A	—	—
520	1	3753	%	Voltage 62nd Harmonic A	—	—
521	1	3759	%	Voltage 63rd Harmonic A	—	—
522	1	3773	%	Phase B Voltage Total Harmonic Distortion	—	—
523	1	3388	%	Voltage 1st Harmonic B	—	—
524	1	3394	%	Voltage 2nd Harmonic B	—	—
525	1	3400	%	Voltage 3rd Harmonic B	—	—
526	1	3406	%	Voltage 4th Harmonic B	—	—
527	1	3412	%	Voltage 5th Harmonic B	—	—
528	1	3418	%	Voltage 6th Harmonic B	—	—
529	1	3424	%	Voltage 7th Harmonic B	—	—
530	1	3430	%	Voltage 8th Harmonic B	—	—
531	1	3436	%	Voltage 9th Harmonic B	—	—
532	1	3442	%	Voltage 10th Harmonic B	—	—
533	1	3448	%	Voltage 11th Harmonic B	—	—
534	1	3454	%	Voltage 12th Harmonic B	—	—
535	1	3460	%	Voltage 13th Harmonic B	—	—
536	1	3466	%	Voltage 14th Harmonic B	—	—
537	1	3472	%	Voltage 15th Harmonic B	—	—
538	1	3478	%	Voltage 16th Harmonic B	—	—
539	1	3484	%	Voltage 17th Harmonic B	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
540	1	3490	%	Voltage 18th Harmonic B	—	—
541	1	3496	%	Voltage 19th Harmonic B	—	—
542	1	3502	%	Voltage 20th Harmonic B	—	—
543	1	3508	%	Voltage 21st Harmonic B	—	—
544	1	3514	%	Voltage 22nd Harmonic B	—	—
545	1	3520	%	Voltage 23rd Harmonic B	—	—
546	1	3526	%	Voltage 24th Harmonic B	—	—
547	1	3532	%	Voltage 25th Harmonic B	—	—
548	1	3538	%	Voltage 26th Harmonic B	—	—
549	1	3544	%	Voltage 27th Harmonic B	—	—
550	1	3550	%	Voltage 28th Harmonic B	—	—
551	1	3556	%	Voltage 29th Harmonic B	—	—
552	1	3562	%	Voltage 30th Harmonic B	—	—
553	1	3568	%	Voltage 31st Harmonic B	—	—
554	1	3574	%	Voltage 32nd Harmonic B	—	—
555	1	3580	%	Voltage 33rd Harmonic B	—	—
556	1	3586	%	Voltage 34th Harmonic B	—	—
557	1	3592	%	Voltage 35th Harmonic B	—	—
558	1	3598	%	Voltage 36th Harmonic B	—	—
559	1	3604	%	Voltage 37th Harmonic B	—	—
560	1	3610	%	Voltage 38th Harmonic B	—	—
561	1	3616	%	Voltage 39th Harmonic B	—	—
562	1	3622	%	Voltage 40th Harmonic B	—	—
563	1	3628	%	Voltage 41st Harmonic B	—	—
564	1	3634	%	Voltage 42nd Harmonic B	—	—
565	1	3640	%	Voltage 43rd Harmonic B	—	—
566	1	3646	%	Voltage 44th Harmonic B	—	—
567	1	3652	%	Voltage 45th Harmonic B	—	—
568	1	3658	%	Voltage 46th Harmonic B	—	—
569	1	3664	%	Voltage 47th Harmonic B	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
570	1	3670	%	Voltage 48th Harmonic B	—	—
571	1	3676	%	Voltage 49th Harmonic B	—	—
572	1	3682	%	Voltage 50th Harmonic B	—	—
573	1	3688	%	Voltage 51st Harmonic B	—	—
574	1	3694	%	Voltage 52nd Harmonic B	—	—
575	1	3700	%	Voltage 53rd Harmonic B	—	—
576	1	3706	%	Voltage 54th Harmonic B	—	—
577	1	3712	%	Voltage 55th Harmonic B	—	—
578	1	3718	%	Voltage 56th Harmonic B	—	—
579	1	3724	%	Voltage 57th Harmonic B	—	—
580	1	3730	%	Voltage 58th Harmonic B	—	—
581	1	3736	%	Voltage 59th Harmonic B	—	—
582	1	3742	%	Voltage 60th Harmonic B	—	—
583	1	3748	%	Voltage 61st Harmonic B	—	—
584	1	3754	%	Voltage 62nd Harmonic B	—	—
585	1	3760	%	Voltage 63rd Harmonic B	—	—
586	1	3774	%	Phase C Voltage Total Harmonic Distortion	—	—
587	1	3389	%	Voltage 1st Harmonic C	—	—
588	1	3395	%	Voltage 2nd Harmonic C	—	—
589	1	3401	%	Voltage 3rd Harmonic C	—	—
590	1	3407	%	Voltage 4th Harmonic C	—	—
591	1	3413	%	Voltage 5th Harmonic C	—	—
592	1	3419	%	Voltage 6th Harmonic C	—	—
593	1	3425	%	Voltage 7th Harmonic C	—	—
594	1	3431	%	Voltage 8th Harmonic C	—	—
595	1	3437	%	Voltage 9th Harmonic C	—	—
596	1	3443	%	Voltage 10th Harmonic C	—	—
597	1	3449	%	Voltage 11th Harmonic C	—	—
598	1	3455	%	Voltage 12th Harmonic C	—	—
599	1	3461	%	Voltage 13th Harmonic C	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
600	1	3467	%	Voltage 14th Harmonic C	—	—
601	1	3473	%	Voltage 15th Harmonic C	—	—
602	1	3479	%	Voltage 16th Harmonic C	—	—
603	1	3485	%	Voltage 17th Harmonic C	—	—
604	1	3491	%	Voltage 18th Harmonic C	—	—
605	1	3497	%	Voltage 19th Harmonic C	—	—
606	1	3503	%	Voltage 20th Harmonic C	—	—
607	1	3509	%	Voltage 21st Harmonic C	—	—
608	1	3515	%	Voltage 22nd Harmonic C	—	—
609	1	3521	%	Voltage 23rd Harmonic C	—	—
610	1	3527	%	Voltage 24th Harmonic C	—	—
611	1	3533	%	Voltage 25th Harmonic C	—	—
612	1	3539	%	Voltage 26th Harmonic C	—	—
613	1	3545	%	Voltage 27th Harmonic C	—	—
614	1	3551	%	Voltage 28th Harmonic C	—	—
615	1	3557	%	Voltage 29th Harmonic C	—	—
616	1	3563	%	Voltage 30th Harmonic C	—	—
617	1	3569	%	Voltage 31st Harmonic C	—	—
618	1	3575	%	Voltage 32nd Harmonic C	—	—
619	1	3581	%	Voltage 33rd Harmonic C	—	—
620	1	3587	%	Voltage 34th Harmonic C	—	—
621	1	3593	%	Voltage 35th Harmonic C	—	—
622	1	3599	%	Voltage 36th Harmonic C	—	—
623	1	3605	%	Voltage 37th Harmonic C	—	—
624	1	3611	%	Voltage 38th Harmonic C	—	—
625	1	3617	%	Voltage 39th Harmonic C	—	—
626	1	3623	%	Voltage 40th Harmonic C	—	—
627	1	3629	%	Voltage 41st Harmonic C	—	—
628	1	3635	%	Voltage 42nd Harmonic C	—	—
629	1	3641	%	Voltage 43rd Harmonic C	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
630	1	3647	%	Voltage 44th Harmonic C	—	—
631	1	3653	%	Voltage 45th Harmonic C	—	—
632	1	3659	%	Voltage 46th Harmonic C	—	—
633	1	3665	%	Voltage 47th Harmonic C	—	—
634	1	3671	%	Voltage 48th Harmonic C	—	—
635	1	3677	%	Voltage 49th Harmonic C	—	—
636	1	3683	%	Voltage 50th Harmonic C	—	
637	1	3689	%	Voltage 51st Harmonic C	—	—
638	1	3695	%	Voltage 52nd Harmonic C	—	—
639	1	3701	%	Voltage 53rd Harmonic C	—	—
640	1	3707	%	Voltage 54th Harmonic C	—	—
641	1	3713	%	Voltage 55th Harmonic C	—	—
642	1	3719	%	Voltage 56th Harmonic C	—	
643	1	3725	%	Voltage 57th Harmonic C	—	
644	1	3731	%	Voltage 58th Harmonic C	—	
645	1	3737	%	Voltage 59th Harmonic C	—	
646	1	3743	%	Voltage 60th Harmonic C	—	
647	1	3749	%	Voltage 61st Harmonic C	—	
648	1	3755	%	Voltage 62nd Harmonic C	—	—
649	1	3761	%	Voltage 63rd Harmonic C	—	—
650	1	3765	—	Read ONLY value for the Active Profile	—	—
651	1	3790	—	Angle diff between reference Ph voltage and system voltage	—	—
652	1	3791	Hz	Frequency diff between reference Ph voltage and system voltage	—	—
653	1	3792	V	Voltage diff between reference Ph voltage and system voltage	—	—
654	1	3769	%	Total Harmonic Distortion of Phase A current	—	—
655	1	3770	%	Total Harmonic Distortion of Phase B current	—	—
656	1	3771	%	Total Harmonic Distortion of Phase C current	—	—
657	1	3772	%	Total Harmonic Distortion of Phase A voltage	—	—
658	1	3773	%	Total Harmonic Distortion of Phase B voltage	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
659	1	3774	%	Total Harmonic Distortion of Phase C voltage	–	–
660	1	1932	%	Total Demand Distortion of Phase A current	–	–
661	1	1933	%	Total Demand Distortion of Phase B current	–	–
662	1	1934	%	Total Demand Distortion of Phase C current	–	–
663	1	3961	–	UTC Time	0-4294967295	–
664	1	3963	–	Local Time	0-4294967295	–
665	1	1976	–	Pressure Metering	–	–
666	1	1991	–	IEC61850 Server Status	–	–
667	1	2009	s	Sectionalizer Reset Timer 3-Ph/Ph-A	–	–
668	1	2010	s	Sectionalizer Reset Timer Ph-B	–	–
669	1	2011	s	Sectionalizer Reset Timer Ph-C	–	–
670	1	2043	s	Sectionalizer Current Restraint Active Timer Ph-A	–	–
671	1	2044	s	Sectionalizer Current Restraint Active Timer Ph-B	–	–
672	1	2045	Sec	Sectionalizer Current Restraint Active Timer Ph-C	–	–
673	1	2046	Sec	Sectionalizer Ground Current Restraint Block	–	–
674	1	1977	Sec	Breaker Accumulator Phase A	–	–
675	1	1979	Sec	Breaker Accumulator Phase B	–	–
676	1	1981	Sec	Breaker Accumulator Phase C	–	–
677	1	3797	–	Battery Amps	–	–
678	1	2055	–	Fault Element	–	–
679	1	2090		79 Fault Duration 3-Ph Ganged	–	–
680	1	2090	–	79 Fault Duration Phase A	–	–
681	1	2092	–	79 Fault Duration Phase B	–	–
682	1	2094	–	79 Fault Duration Phase C	–	–
683	1	1988	miles	Fault Location	–	–
684	1	1990	–	Fault Type	–	–
685	1	2059	A	Fault Pri Amps A	–	–
686	1	2061	A	Fault Pri Amps B	–	–
687	1	2063	A	Fault Pri Amps C	–	–
688	1	2069	deg	Fault Pri Amps A Phase Angle	–	–

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
689	1	2070	deg	Fault Pri Amps B Phase Angle	–	–
690	1	2071	deg	Fault Pri Amps C Phase Angle	–	–
691	1	2065	A	Fault Pri Amps Ground	–	–
692	1	2067	A	Fault Pri Amps Max	–	–
693	1	2072	V	Fault Voltage Phase A	–	–
694	1	2073	V	Fault Voltage Phase B	–	–
695	1	2074	V	Fault Voltage Phase C	–	–
696	0	300	–	Firmware Version	–	–
697	1	80	–	Recloser Type(if 3-Phase Ganged Unit)	–	–
698	1	84	–	Recloser Type(if Independent Phase Capable Unit)	–	–
699	1	3794	–	Control Alarm Code	–	–
700	1	108	–	Manufacturer	0-7	–
701	1	7499	–	Recloser Mode Phase MTT		–
702	1	7775	–	Recloser Mode Ground (N) MTT	–	–
703	1	7427	–	Recloser Mode Ground (G/GS) MTT	–	–
704	1	9799	–	Alt 1 Recloser Mode Phase MTT	–	–
705	1	10075	–	Alt 1 Recloser Mode Ground (N) MTT	–	–
706	1	9727	–	Alt 1 Recloser Mode Ground (G/GS) MTT	–	–
707	1	12099	–	Alt 2 Recloser Mode Phase MTT	–	–
708	1	12375	–	Alt 2 Recloser Mode Ground (N) MTT	–	–
709	1	12027	–	Alt 2 Recloser Mode Ground (G/GS) MTT	–	–
710	1	14399	–	Switch Mode Phase MTT	–	–
711	1	14675	–	Switch Mode Ground (N) MTT	–	–
712	1	14327	–	Switch Mode Ground (G/GS) MTT	–	–
713	1	16699	–	Sectionalizer Mode Phase MTT	–	–
714	1	16975	–	Sectionalizer Mode Ground (N) MTT	–	–
715	1	16627	–	Sectionalizer Mode Ground (G/GS) MTT	–	–
716	1	18999	–	DG Mode Phase MTT	–	–
717	1	19275	–	DG Mode Ground (N) MTT	–	–
718	1	18927	–	DG Mode Ground (G/GS) MTT	–	–

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
719	—	6821	—	Battery Charger Num Test in 24 Hrs	1-24	—
720	1	4899	—	32H Capacitor Volatge	—	—
721	1	4900	—	32H Charger Temperature	—	—
722	1	4698	—	Anlg Out Port 1 Error Status	0-65535	—
723	1	2190	—	Anlg Out Port 1 FW Version	0-65535	—
724	1	2188	—	Port 1 Module ID installed	0-65535	—
725	1	4699	—	Anlg Out Port 4 Error Status	0-65535	—
726	1	2191	—	Anlg Out Port 4 FW Version	0-65535	—
727	1	2189	—	Port 4 Module ID installed	0-65535	—
728	1	4757	—	Primary Line Voltage VABy	0	—
729	1	4759	—	Primary Line Voltage VBCy	0	—
730	1	4761	—	Primary Line Voltage VACy	0	—
731	1	4763	—	Primary Line Voltage VABz	0	—
732	1	4765	—	Primary Line Voltage VBCz	0	—
733	1	4767	—	Primary Line Voltage VACz	0	—
734	1	4788	A	Fault Sec Amps A	—	—
735	1	4790	A	Fault Sec Amps B	—	—
736	1	4792	A	Fault Sec Amps C	—	—
737	1	4796	A	Fault Sec Amps N	—	—
738	1	4794	A	Fault Sec Amps G/GS	—	—
739	1	4772	—	Fault Type 1stT	—	—
740	1	4773	A	Fault Pri Amps A 1stT	—	—
741	1	4775	A	Fault Pri Amps B 1stT	—	—
742	1	4777	A	Fault Pri Amps C 1stT	—	—
743	1	4779	A	Fault Pri Amps G 1stT	—	—
744	1	4781	A	Fault Pri Amps N 1stT	—	—
745	1	4783	A	Fault Sec Amps A 1stT	—	—
746	1	4784	A	Fault Sec Amps B 1stT	—	—
747	1	4785	A	Fault Sec Amps C 1stT	—	—
748	1	4786	A	Fault Sec Amps G 1stT	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
749	1	4787	A	Fault Sec Amps N 1stT	—	—
750	1	8775	A	FHLT50P MTT Active Profile	—	—
751	1	8805	A	FHLT50N MTT Active Profile	—	—
752	1	8778	A	FHLT50 Gnd MTT Active Profile	—	—
753	1	8802	A	FHLT50 Gnd Sens MTT Active Profile	—	—
754	1	8781	A	FHLT51P MTT Active Profile	—	—
755	1	8808	A	FHLT51N MTT Active Profile	—	—
756	1	8792	A	FHLT51 Gnd MTT Active Profile	—	—
757	1	8803	A	FHLT51 Gnd Sens MTT Active Profile	—	—
758	1	8565	Hz	F81#1 Pickup	—	—
759	1	8566	s	F81#1 Def Time	—	—
760	1	8567	Hz	F81#1 Hysteresis	—	—
761	1	8568	V	F81#1 Min Voltage	—	—
762	1	8569	A	F81#1 Min Load	—	—
763	1	8570	—	F81#1 Block Options	—	—
764	1	8574	Hz	F81#2 Pickup	—	—
765	1	8575	s	F81#2 Def Time	—	—
766	1	8576	Hz	F81#2 Hysteresis	—	—
767	1	8577	V	F81#2 Min Voltage	—	—
768	1	8578	A	F81#2 Min Load	—	—
769	1	8579	—	F81#2 Block Options	—	—
770	1	8583	Hz	F81#3 Pickup	—	—
771	1	8584	s	F81#3 Def Time	—	—
772	1	8585	Hz	F81#3 Hysteresis	—	—
773	1	8586	V	F81#3 Min Voltage	—	—
774	1	8587	A	F81#3 Min Load	—	—
775	1	8588	—	F81#3 Block Options	—	—
776	1	8592	Hz	F81#4 Pickup	—	—
777	1	8593	s	F81#4 Def Time	—	—
778	1	8594	Hz	F81#4 Hysteresis	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
779	1	8595	V	F81#4 Min Voltage	—	—
780	1	8596	A	F81#4 Min Load	—	—
781	1	8597	—	F81#4 Block Options	—	—
782	1	46713	Hz	F81#5 Pickup	—	—
783	1	46714	s	F81#5 Def Time	—	—
784	1	46715	Hz	F81#5 Hysteresis	—	—
785	1	45716	V	F81#5 Min Voltage	—	—
786	1	46717	A	F81#5 Min Load	—	—
787	1	46718	—	F81#5 Block Options	—	—
788	1	46722	Hz	F81#6 Pickup	—	—
789	1	46723	s	F81#6 Def Time	—	—
790	1	46724	Hz	F81#6 Hysteresis	—	—
791	1	46725	V	F81#6 Min Voltage	—	—
792	1	46726	A	F81#6 Min Load	—	—
793	1	46727	—	F81#6 Block Options	—	—
794	1	46731	Hz	F81#7 Pickup	—	—
795	1	46732	s	F81#7 Def Time	—	—
796	1	46733	Hz	F81#7 Hysteresis	—	—
797	1	46734	V	F81#7 Min Voltage	—	—
798	1	46735	A	F81#7 Min Load	—	—
799	1	46736	—	F81#7 Block Options	—	—
800	1	46740	Hz	F81#8 Pickup	—	—
801	1	46741	s	F81#8 Def Time	—	—
802	1	46742	Hz	F81#8 Hysteresis	—	—
803	1	46743	V	F81#8 Min Voltage	—	—
804	1	46744	A	F81#8 Min Load	—	—
805	1	46745	—	F81#8 Block Options	—	—
806	1	8675	Hz/s	F81R#1 Pickup	—	—
807	1	8676	s	F81R#1 Def Time	—	—
808	1	8677	Hz	F81R#1 Max Frequency	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
809	1	8678	A	F81R#1 Min Current	—	—
810	1	8679	V	F81R#1 Min Voltage	—	—
811	1	8680	—	F81R#1 Pickup Cycle Number	—	—
812	1	8690	—	F81R#1 Operating Voltage	—	—
813	1	8684	Hz/s	F81R#2 Pickup	—	—
814	1	8685	s	F81R#2 Def Time	—	—
815	1	8686	Hz	F81R#2 Max Frequency	—	—
816	1	8687	A	F81R#2 Min Current	—	—
817	1	8688	V	F81R#2 Min Voltage	—	—
818	1	8689	—	F81R#2 Pickup Cycle Number	—	—
819	1	8691	—	F81R#2 Operating Voltage	—	—
820	1	46749	Hz/s	F81R#3 Pickup	—	—
821	1	46750	s	F81R#3 Def Time	—	—
822	1	46751	Hz	F81R#3 Max Frequency	—	—
823	1	46752	A	F81R#3 Min Current	—	—
824	1	46753	V	F81R#3 Min Voltage	—	—
825	1	46754	—	F81R#3 Pickup Cycle Number	—	—
826	1	46755	—	F81R#3 Operating Voltage	—	—
827	1	46759	Hz/s	F81R#4 Pickup	—	—
828	1	46760	s	F81R#4 Def Time	—	—
829	1	46761	Hz	F81R#4 Max Frequency	—	—
830	1	46762	A	F81R#4 Min Current	—	—
831	1	46763	V	F81R#4 Min Voltage	—	—
832	1	46764	—	F81R#4 Pickup Cycle Number	—	—
833	1	46765	—	F81R#4 Operating Voltage	—	—
834	1	46769	Hz/s	F81R#5 Pickup	—	—
835	1	46770	s	F81R#5 Def Time	—	—
836	1	46771	Hz	F81R#5 Max Frequency	—	—
837	1	46772	A	F81R#5 Min Current	—	—
838	1	46773	V	F81R#5 Min Voltage	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
839	1	46774	—	F81R#5 Pickup Cycle Number	—	—
840	1	46775	—	F81R#5 Operating Voltage	—	—
841	1	46779	Hz/s	F81R#6 Pickup	—	—
842	1	46780	s	F81R#6 Def Time	—	—
843	1	46781	Hz	F81R#6 Max Frequency	—	—
844	1	46782	A	F81R#6 Min Current	—	—
845	1	46783	V	F81R#6 Min Voltage	—	—
846	1	46784	—	F81R#6 Pickup Cycle Number	—	—
847	1	46785	—	F81R#6 Operating Voltage	—	—
848	1	46789	Hz/s	F81R#7 Pickup	—	—
849	1	46790	s	F81R#7 Def Time	—	—
850	1	46791	Hz	F81R#7 Max Frequency	—	—
851	1	46792	A	F81R#7 Min Current	—	—
852	1	46793	V	F81R#7 Min Voltage	—	—
853	1	46794	—	F81R#7 Pickup Cycle Number	—	—
854	1	46795	—	F81R#7 Operating Voltage	—	—
855	1	46799	Hz/s	F81R#8 Pickup	—	—
856	1	46800	s	F81R#8 Def Time	—	—
857	1	46801	Hz	F81R#8 Max Frequency	—	—
858	1	46802	A	F81R#8 Min Current	—	—
859	1	46803	V	F81R#8 Min Voltage	—	—
860	1	46804	—	F81R#8 Pickup Cycle Number	—	—
861	1	46805	—	F81R#8 Operating Voltage	—	—
862	1	4861	%	F46BC I2/I1 Ratio Secondary	—	—
863	1	4862	%	F46BC I2/I1 Ratio Primary	—	—
864	1	46809	%	F46BC I2/I1 Ratio Setting	—	—
865	1	46810	s	F46BC Time Delay Setting	—	—
866	1	46811	A	F46BC Min I2 Level Setting	—	—
867	1	4871	s	FCLP Open To Arm Timer	—	—
868	1	4872	s	FCLP Override Active Timer	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
869	1	4873	s	FCLP Initiate Active Timer	–	–
870	1	4874	s	FCLP BMT Phase Timer	–	–
871	1	4875	s	FCLP BMT Ground Timer	–	–
872	1	4896	S	FCLP BMT Residual Timer	–	–
873	1	4876	A	FCLP Min Phase Pickup	–	–
874	1	4877	A	FCLP Min Ground Pickup	–	–
875	1	4895	A	FCLP Min Residual Pickup	–	–
876	1	4885	%	FBM percent life used Ph-A	–	–
877	1	4886	%	FBM percent life used Ph-B	–	–
878	1	4887	%	FBM percent life used Ph-C	–	–
879	1	4888	%	FBM percent life Remaining Ph-A	–	–
880	1	4889	%	FBM percent life Remaining Ph-B	–	–
881	1	4890	%	FBM percent life Remaining Ph-C	–	–
882	1	7275	A	F50 3-Ph MTT Active profile	0-1	–
883	1	7305	A	F50 Ph-A MTT Active profile	0-1	–
884	1	7310	A	F50 Ph-B MTT Active profile	0-1	–
885	1	7315	A	F50 Ph-C MTT Active profile	0-1	–
886	1	7427	A	F50 Gnd MTT Active profile	0-1	–
887	1	7391	A	F50 N MTT Active profile	0-1	–
888	1	7499	A	F51 3-Ph MTT Active profile	0-1	–
889	1	7569	A	F51 Ph-A MTT Active profile	0-1	–
890	1	7582	A	F51 Ph-B MTT Active profile	0-1	–
891	1	7595	A	F51 Ph-C MTT Active profile	0-1	–
892	1	7851	A	F51 Gnd MTT Active profile	0-1	–
893	1	7775	A	F51 N MTT Active profile	0-1	–
894	1	4878	s	Fault Time Phase A	–	–
895	1	4879	s	Fault Time Phase B	–	–
896	1	4880	s	Fault Time Phase C	–	–
897	1	7275	A	F50 3-Ph MTT Profile 1	–	–
898	1	9575	A	F50 3-Ph MTT Profile 2	–	–

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
899	1	11875	A	F50 3-Ph MTT Profile 3	–	–
900	1	14175	A	F50 3-Ph MTT Profile 4	–	–
901	1	16475	A	F50 3-Ph MTT Profile 5	–	–
902	1	18775	A	F50 3-Ph MTT Profile 6	–	–
903	1	21075	A	F50 3-Ph MTT Profile 7	–	–
904	1	23375	A	F50 3-Ph MTT Profile 8	–	–
905	1	7305	A	F50 Ph-A MTT Profile 1	–	–
906	1	7310	A	F50 Ph-B MTT Profile 1	–	–
907	1	7315	A	F50 Ph-C MTT Profile 1	–	–
908	1	9605	A	F50 Ph-A MTT Profile 2	–	–
909	1	9610	A	F50 Ph-B MTT Profile 2	–	–
910	1	9615	A	F50 Ph-C MTT Profile 2	–	–
911	1	11905	A	F50 Ph-A MTT Profile 3	–	–
912	1	11910	A	F50 Ph-B MTT Profile 3	–	–
913	1	11915	A	F50 Ph-C MTT Profile 3	–	–
914	1	14205	A	F50 Ph-A MTT Profile 4	–	–
915	1	14210	A	F50 Ph-B MTT Profile 4	–	–
916	1	14215	A	F50 Ph-C MTT Profile 4	–	–
917	1	16505	A	F50 Ph-A MTT Profile 5	–	–
918	1	16510	A	F50 Ph-B MTT Profile 5	–	–
919	1	16515	A	F50 Ph-C MTT Profile 5	–	–
920	1	18805	A	F50 Ph-A MTT Profile 6	–	–
921	1	18810	A	F50 Ph-B MTT Profile 6	–	–
922	1	18815	A	F50 Ph-C MTT Profile 6	–	–
923	1	21105	A	F50 Ph-A MTT Profile 7	–	–
924	1	21110	A	F50 Ph-B MTT Profile 7	–	–
925	1	21115	A	F50 Ph-C MTT Profile 7	–	–
926	1	23405	A	F50 Ph-A MTT Profile 8	–	–
927	1	23410	A	F50 Ph-B MTT Profile 8	–	–
928	1	23415	A	F50 Ph-C MTT Profile 8	–	–

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
929	1	7391	A	F50N MTT Profile 1	—	—
930	1	9691	A	F50N MTT Profile 2	—	—
931	1	11991	A	F50N MTT Profile 3	—	—
932	1	14291	A	F50N MTT Profile 4	—	—
933	1	16591	A	F50N MTT Profile 5	—	—
934	1	18891	A	F50N MTT Profile 6	—	—
935	1	21191	A	F50N MTT Profile 7	—	—
936	1	23491	A	F50N MTT Profile 8	—	—
937	1	7427	A	F50G/GS MTT Profile 1	—	—
938	1	9727	A	F50G/GS MTT Profile 2	—	—
939	1	12027	A	F50G/GS MTT Profile 3	—	—
940	1	14327	A	F50G/GS MTT Profile 4	—	—
941	1	16627	A	F50G/GS MTT Profile 5	—	—
942	1	18927	A	F50G/GS MTT Profile 6	—	—
943	1	21227	A	F50G/GS MTT Profile 7	—	—
944	1	23527	A	F50G/GS MTT Profile 8	—	—
945	1	7499	A	F51 3-Ph MTT Profile 1	—	—
946	1	9799	A	F51 3-Ph MTT Profile 2	—	—
947	1	12099	A	F51 3-Ph MTT Profile 3	—	—
948	1	14399	A	F51 3-Ph MTT Profile 4	—	—
949	1	16699	A	F51 3-Ph MTT Profile 5	—	—
950	1	18999	A	F51 3-Ph MTT Profile 6	—	—
951	1	21299	A	F51 3-Ph MTT Profile 7	—	—
952	1	23599	A	F51 3-Ph MTT Profile 8	—	—
953	1	7569	A	F51 Ph-A MTT Profile 1	—	—
954	1	7582	A	F51 Ph-B MTT Profile 1	—	—
955	1	7595	A	F51 Ph-C MTT Profile 1	—	—
956	1	9869	A	F51 Ph-A MTT Profile 2	—	—
957	1	9882	A	F51 Ph-B MTT Profile 2	—	—
958	1	9895	A	F51 Ph-C MTT Profile 2	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
959	1	12169	A	F51 Ph-A MTT Profile 3	—	—
960	1	12182	A	F51 Ph-B MTT Profile 3	—	—
961	1	12195	A	F51 Ph-C MTT Profile 3	—	—
962	1	14469	A	F51 Ph-A MTT Profile 4	—	—
963	1	14482	A	F51 Ph-B MTT Profile 4	—	—
964	1	14495	A	F51 Ph-C MTT Profile 4	—	—
965	1	16769	A	F51 Ph-A MTT Profile 5	—	—
966	1	16782	A	F51 Ph-B MTT Profile 5	—	—
967	1	16795	A	F51 Ph-C MTT Profile 5	—	—
968	1	19069	A	F51 Ph-A MTT Profile 6	—	—
969	1	19082	A	F51 Ph-B MTT Profile 6	—	—
970	1	19095	A	F51 Ph-C MTT Profile 6	—	—
971	1	21369	A	F51 Ph-A MTT Profile 7	—	—
972	1	21382	A	F51 Ph-B MTT Profile 7	—	—
973	1	21395	A	F51 Ph-C MTT Profile 7	—	—
974	1	23669	A	F51 Ph-A MTT Profile 8	—	—
975	1	23682	A	F51 Ph-B MTT Profile 8	—	—
976	1	23695	A	F51 Ph-C MTT Profile 8	—	—
977	1	7775	A	F51N MTT Profile 1	—	—
978	1	10075	A	F51N MTT Profile 2	—	—
979	1	12375	A	F51N MTT Profile 3	—	—
980	1	14675	A	F51N MTT Profile 4	—	—
981	1	16975	A	F51N MTT Profile 5	—	—
982	1	19275	A	F51N MTT Profile 6	—	—
983	1	21575	A	F51N MTT Profile 7	—	—
984	1	23875	A	F51N MTT Profile 8	—	—
985	1	7851	A	F51G/GS MTT Profile 1	—	—
986	1	10151	A	F51G/GS MTT Profile 2	—	—
987	1	12451	A	F51G/GS MTT Profile 3	—	—
988	1	14751	A	F51G/GS MTT Profile 4	—	—

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
989	1	17051	A	F51G/GS MTT Profile 5	–	–
990	1	19351	A	F51G/GS MTT Profile 6	–	–
991	1	21651	A	F51G/GS MTT Profile 7	–	–
992	1	23951	A	F51G/GS MTT Profile 8	–	–
993	1	5028-5029	N/A	Power Target #1 Threshold Secondary	10000-10000	–
994	1	5030-5031	N/A	Power Target #2 Threshold Secondary	10000-10000	–
995	1	5032-5033	N/A	Power Target #3 Threshold Secondary	10000-10000	–
996	1	5034-5035	N/A	Power Target #4 Threshold Secondary	10000-10000	–
997	1	5036-5037	N/A	Power Target #5 Threshold Secondary	10000-10000	–
998	1	5038-5039	N/A	Power Target #6 Threshold Secondary	10000-10000	–
999	1	5040-5041	N/A	Power Target #7 Threshold Secondary	10000-10000	–
1000	1	5042-5043	N/A	Power Target #8 Threshold Secondary	10000-10000	–
1001	1	5044-5045	N/A	Power Target #9 Threshold Secondary	10000-10000	–
1002	1	5046-5047	N/A	Power Target #10 Threshold Secondary	10000-10000	–
1003	1	5048-5049	N/A	Power Target #11 Threshold Secondary	10000-10000	–
1004	1	5050-5051	N/A	Power Target #12 Threshold Secondary	10000-10000	–
1005	1	1166-1167	N/A	Operation Counters A	0-99999999	–
1006	1	1168-1169	N/A	Operation Counters B	0-99999999	–
1007	1	1170-1171	N/A	Operation Counters C	0-99999999	–
1008	1	3914	N/A	Serial Number	–	–
1009	1	3775	N/A	Control Temperature	–	–
1010	1	4998-4999	N/A	Peak Demand Current Magnitude Phase A	–	–
1011	1	5052-5053	N/A	Peak Demand Current Magnitude Phase B	–	–
1012	1	5054-5055	N/A	Peak Demand Current Magnitude Phase C	–	–
1013	1	5056-5057	N/A	Peak Demand Current Magnitude Ig	–	–
1014	1	29053	N/A	SIG Reset Timer	–	–
1015	1	29054	N/A	SIG Time Delay	–	–
1016	1	29065	N/A	SIG Reset Latch Timer	–	–
1017	1	29055	N/A	SIG Window	–	–
1018	1	29120	N/A	SIG PreTrigger Delay	–	–

Table 3 Analog Input List (Status)

Analog Input List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1019	10	29126	N/A	SGI Delta Ig 3rd Harmonic Pickup	–	–
1020	1000	29122	N/A	SGI Ig Fund OverCurrent Threshold Pickup	–	–
1021	100	29123	N/A	SGI Ig Fund OverCurrent Threshold Definite Time	–	–
1022	1	29057	N/A	SGI Init Logic	–	–
1023	1	29058	N/A	SGI Reset Logic	–	–
1024	10000	29059	N/A	SGI Delta Ig Pickup	–	–
1025	100	29060	N/A	SGI Delta Ig Dropout Time Delay	–	–
1026	1	29061	N/A	SGI Direction Angle	–	–
1027	1	29062	N/A	SGI Direction Angle Band	–	–
1028	100	29063	N/A	SGI Direction OverCurrent Definite Time	–	–
1029	1	29066	N/A	SGI Pre-Trigger Averaging Window	–	–
1030	10	29124	N/A	SGI Ig 3rd Harmonic Supervision Pickup	–	–
1031	100	29125	N/A	SGI Ig 3rd Harmonic Supervision Definite Time	–	–
1032	10	29108	N/A	SGI IG Fundamental	–	–
1033	10	29109	N/A	SGI IG 3rd Harmonic	–	–
1034	10	29105	N/A	SGI Delta IG Fundamental	–	–
1035	10	29104	N/A	SGI Delta IG 3rd Harmonic	–	–

Table 3 Analog Input List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
0	1	41	N/A	Neutral Operating Current	0-1	—
1	1	28	N/A	Setting Units	0-1	—
2	1	650	N/A	State of the Inputs Bit 0 – Input 1 Bit 6 – Input 7 Bit 1 – Input 2 Bit 7 – Input 8 Bit 2 – Input 3 Bit 8 – Input 9 Bit 3 – Input 4 Bit 9 – Input 10 Bit 4 – Input 5 Bit 10 – Input 11 Bit 5 – Input 6 Bit 11 – Input 12	0-4095	—
3	1	79	N/A	Adapter Chassis Option	0-1	—
4	1	692	N/A	Input 1 Configuration	0-21	—
5	1	693	N/A	Input 2 Configuration	0-21	—
6	1	694	N/A	Input 3 Configuration	0-21	—
7	1	695	N/A	Input 4 Configuration	0-21	—
8	1	696	N/A	Input 5 Configuration	0-21	—
9	1	697	N/A	Input 6 Configuration	0-21	—
10	1	698	N/A	Input 7 Configuration	0-21	—
11	1	699	N/A	Input 8 Configuration	0-21	—
12	1	700	N/A	Input 9 Configuration	0-21	—
13	1	701	N/A	Input 10 Configuration	0-21	—
14	1	702	N/A	Input 11 Configuration	0-21	—
15	1	703	N/A	Input 12 Configuration	0-21	—
16	1	705	N/A	Breaker Status Delay	0-65535	—
17	1	1051	N/A	Output 1 Configuration 0 – Trip A 7 – Hot Line Tag 1 – Trip B 8 – Alarm 2 – Trip C 9 – Battery Alarm 3 – Close A 10 – Trip ABC 4 – Close B 11 – Close ABC 5 – Close C 12 – Trip Status 6 – General 13 – Close Status	0-13	—
18	1	1052	N/A	Output 2 Configuration 0 – Trip A 7 – Hot Line Tag 1 – Trip B 8 – Alarm 2 – Trip C 9 – Battery Alarm 3 – Close A 10 – Trip ABC 4 – Close B 11 – Close ABC 5 – Close C 12 – Trip Status 6 – General 13 – Close Status	0-13	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
19	1	1053	N/A	Output 3 Configuration 0 – Trip A 7 – Hot Line Tag 1 – Trip B 8 – Alarm 2 – Trip C 9 – Battery Alarm 3 – Close A 10 – Trip ABC 4 – Close B 11 – Close ABC 5 – Close C 12 – Trip Status 6 – General 13 – Close Status	0-13	–
20	1	1054	N/A	Output 4 Configuration 0 – Trip A 7 – Hot Line Tag 1 – Trip B 8 – Alarm 2 – Trip C 9 – Battery Alarm 3 – Close A 10 – Trip ABC 4 – Close B 11 – Close ABC 5 – Close C 12 – Trip Status 6 – General 13 – Close Status	0-13	–
21	1	1055	N/A	Output 5 Configuration 0 – Trip A 7 – Hot Line Tag 1 – Trip B 8 – Alarm 2 – Trip C 9 – Battery Alarm 3 – Close A 10 – Trip ABC 4 – Close B 11 – Close ABC 5 – Close C 12 – Trip Status 6 – General 13 – Close Status	0-13	–
22	1	1056	N/A	Output 6 Configuration 0 – Trip A 7 – Hot Line Tag 1 – Trip B 8 – Alarm 2 – Trip C 9 – Battery Alarm 3 – Close A 10 – Trip ABC 4 – Close B 11 – Close ABC 5 – Close C 12 – Trip Status 6 – General 13 – Close Status	0-13	–
23	1	1057	N/A	Output 7 Configuration 0 – Trip A 7 – Hot Line Tag 1 – Trip B 8 – Alarm 2 – Trip C 9 – Battery Alarm 3 – Close A 10 – Trip ABC 4 – Close B 11 – Close ABC 5 – Close C 12 – Trip Status 6 – General 13 – Close Status	0-13	–
24	1	1058	N/A	Output 8 Configuration 0 – Trip A 7 – Hot Line Tag 1 – Trip B 8 – Alarm 2 – Trip C 9 – Battery Alarm 3 – Close A 10 – Trip ABC 4 – Close B 11 – Close ABC 5 – Close C 12 – Trip Status 6 – General 13 – Close Status	0-13	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
25	1	1059	N/A	Output 9 Configuration 0 – Trip A 7 – Hot Line Tag 1 – Trip B 8 – Alarm 2 – Trip C 9 – Battery Alarm 3 – Close A 10 – Trip ABC 4 – Close B 11 – Close ABC 5 – Close C 12 – Trip Status 6 – General 13 – Close Status	0-13	–
26	1	1060	N/A	Output 10 Configuration 0 – Trip A 7 – Hot Line Tag 1 – Trip B 8 – Alarm 2 – Trip C 9 – Battery Alarm 3 – Close A 10 – Trip ABC 4 – Close B 11 – Close ABC 5 – Close C 12 – Trip Status 6 – General 13 – Close Status	0-13	–
27	1	1061	N/A	Output 11 Configuration 0 – Trip A 7 – Hot Line Tag 1 – Trip B 8 – Alarm 2 – Trip C 9 – Battery Alarm 3 – Close A 10 – Trip ABC 4 – Close B 11 – Close ABC 5 – Close C 12 – Trip Status 6 – General 13 – Close Status	0-13	–
28	1	1062	N/A	Output 12 Configuration 0 – Trip A 7 – Hot Line Tag 1 – Trip B 8 – Alarm 2 – Trip C 9 – Battery Alarm 3 – Close A 10 – Trip ABC 4 – Close B 11 – Close ABC 5 – Close C 12 – Trip Status 6 – General 13 – Close Status	0-13	–
29	1	85	N/A	Enable CT Polarity Reversal	0-7	–
30	1	651	N/A	52b Phase A Input	2-65535	–
31	1	652	N/A	52b Phase B Input	2-65535	–
32	1	653	N/A	52b Phase C Input	2-65535	–
33	1	654	N/A	52a Phase A Input	1-65535	–
34	1	655	N/A	52a Phase B Input	1-65535	–
35	1	656	N/A	52a Phase C Input	1-65535	–
36	1	657	N/A	69 Lockout Input	4-65535	–
37	1	672	N/A	69 Lockout Ph-A Input	0-65535	–
38	1	673	N/A	69 Lockout Ph-B Input	0-65535	–
39	1	674	N/A	69 Lockout Ph-C Input	0-65535	–
40	1	658	N/A	Gas Pressure Input function	0-65535	–
41	1	659	N/A	General Input function	0-65535	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
42	1	660	N/A	Hot Line Tag Input function	0-4095	—
43	1	661	N/A	Profile Switching Input function	0-4095	—
44	1	663	N/A	GW Mode Input function	0-1	—
45	1	675	N/A	GW Mode Ph-A Input function	0-1	—
46	1	676	N/A	GW Mode Ph-B Input function	0-1	—
47	1	677	N/A	GW Mode Ph-C Input function	0-1	—
48	1	664	N/A	IPSLOGIC-1 Input function	0-57344	—
49	1	665	N/A	IPSLOGIC-2 Input function	0-57344	—
50	1	666	N/A	IPSLOGIC-3 Input function	0-57344	—
51	1	667	N/A	IPSLOGIC-4 Input function	0-57344	—
52	1	668	N/A	IPSLOGIC-5 Input function	0-57344	—
53	1	669	N/A	IPSLOGIC-6 Input function	0-57344	—
54	1	670	N/A	IPSLOGIC-7 Input function	0-57344	—
55	1	671	N/A	IPSLOGIC-8 Input function	0-57344	—
56	1	678	N/A	Battery Alarm Input	0-2048	—
57	1	679	N/A	AC Status	0-1024	—
58	1	1027	N/A	Trip Phase A Output	1-65535	—
59	1	1028	N/A	Trip Phase B Output	1-65535	—
60	1	1029	N/A	Trip Phase C Output	1-65535	—
61	1	1030	N/A	Close Phase A Output	2-65535	—
62	1	1031	N/A	Close Phase B Output	2-65535	—
63	1	1032	N/A	Close Phase C Output	2-65535	—
64	1	1033	N/A	General Output function	0-65535	—
65	1	1034	N/A	Hot Line Tag Output function	0-65535	—
66	1	1035	N/A	Alarm output function	0-65535	—
67	1	1050	N/A	Battery test Output function	0-65535	—
68	1	1036	N/A	Enable Remote Control for Output	0-65535	—
69	1	1749	N/A	Enable HLT using MAC address	0-1	—
70	1	3959	N/A	Enable/Disable Day Light Saving	0-1	—
71	1	21	N/A	Phase CT Ratio	1-10000	—
72	1	22	N/A	Ground CT Ratio	1-10000	—
73	1	23	N/A	Phase PT Ratio	1-10000	—
74	1	24	N/A	Breaker Operation type	0	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
75	1	25	N/A	VT Configuration Bit 0 – Wye or Line to Ground Bit 1 – Line-To-Line Bit 2 – LG-To-LL Bit 3 – Phantom Wye A Bit 4 – Phantom Wye B Bit 5 – Phantom Wye C Bit 6 – Phantom Wye AB Bit 7 – Phantom Wye BC Bit 8 – Phantom Wye CA Bit 9 – Open Delta AB Bit 10 – Open Delta BC Bit 11 – Open Delta CA	0-8	–
76	1	26	N/A	Default Active Profile 0 – Profile 1 1 – Profile 2 2 – Profile 3 3 – Profile 4 4 – Profile 5 5 – Profile 6 6 – Profile 7 7 – Profile 8	0-7	–
77	1	27	N/A	Phase Rotation Bit 0 – ABC Bit 1 – ACB	0-1	–
78	1	29	N/A	Nominal Voltage setting	1-65535	–
79	1	30	N/A	Nominal Current setting	1-65535	–
80	1	44	N/A	Bushing Assignment 0 – ABC 1 – ACB 2 – BAC 3 – BCA 4 – CAB 5 – CBA	0-6	–
81	1	42	N/A	Vz1 Usage	0-7	–
82	1	64	N/A	69 Lockout Operation type	0-1	–
83	1	65	N/A	Enable/Disable no reset before Close	0-1	–
84	1	86	N/A	External Command Enable/Disable	0-1	–
85	1	47	N/A	LEA primary nominal voltage	2000-40000	–
86	100	48	N/A	LEA Y-side PHA o/p voltage	10-1200	–
87	10	49	N/A	LEA Y-side PHA Ph shift	-1800-1800	–
88	100	50	N/A	LEA Z-side PHA o/p voltage	10-1200	–
89	10	51	N/A	LEA Z-side PHA Ph shift	-1800-1800	–
90	100	52	N/A	LEA Y-side PHB o/p voltage	10-1200	–
91	10	53	N/A	LEA Y-side PHB Ph shift	-1800-1800	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
92	100	54	N/A	LEA Z-side PHB o/p voltage	10-1200	—
93	10	55	N/A	LEA Z-side PHB Ph shift	-1800-1800	—
94	100	56	N/A	LEA Y-side PHC o/p voltage	10-1200	—
95	10	57	N/A	LEA Y-side PHC Ph shift	-1800-1800	—
96	100	58	N/A	LEA Z-side PHC o/p voltage	10-1200	—
97	10	59	N/A	LEA Z-side PHC Ph shift	-1800-1800	—
98	100	60	N/A	LEA calib voltage low	10-1200	—
99	100	61	N/A	LEA calib voltage high	6000-30000	—
100	1	62	N/A	LEA orientation voltage setting	0-1	—
101	1	1003	N/A	Output 1 Characteristic 0 – Direct 1 – Latched 2 – Pulsed	0-6	—
102	1	1004	N/A	Output 2 Characteristic 0 – Direct 1 – Latched 2 – Pulsed	0-7	—
103	1	1005	N/A	Output 3 Characteristic 0 – Direct 1 – Latched 2 – Pulsed	0-2	—
104	1	1006	N/A	Output 4 Characteristic 0 – Direct 1 – Latched 2 – Pulsed	0-2	—
105	1	1007	N/A	Output 5 Characteristic 0 – Direct 1 – Latched 2 – Pulsed	0-2	—
106	1	1008	N/A	Output 6 Characteristic 0 – Direct 1 – Latched 2 – Pulsed	0-2	—
107	1	1009	N/A	Output 7 Characteristic 0 – Direct 1 – Latched 2 – Pulsed	0-2	—
108	1	1010	N/A	Output 8 Characteristic 0 – Direct 1 – Latched 2 – Pulsed	0-2	—
109	1	1011	N/A	Output 9 Characteristic 0 – Direct 1 – Latched 2 – Pulsed	0-2	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
110	1	1012	N/A	Output 10 Characteristic 0 – Direct 1 – Latched 2 – Pulsed	0-2	–
111	1	1013	N/A	Output 11 Characteristic 0 – Direct 1 – Latched 2 – Pulsed	0-2	–
112	1	1014	N/A	Output 12 Characteristic 0 – Direct 1 – Latched 2 – Pulsed	0-2	–
113	100	1015	Sec	Output 1 Pulse width/Seal-in time	1-13600	–
114	100	1016	Sec	Output 2 Pulse width/Seal-in time	1-13600	–
115	100	1017	Sec	Output 3 Pulse width/Seal-in time	1-13600	–
116	100	1018	Sec	Output 4 Pulse width/Seal-in time	1-13600	–
117	100	1019	Sec	Output 5 Pulse width/Seal-in time	1-13600	–
118	100	1020	Sec	Output 6 Pulse width/Seal-in time	1-13600	–
119	100	1021	Sec	Output 7 Pulse width/Seal-in time	1-13600	–
120	100	1022	Sec	Output 8 Pulse width/Seal-in time	1-13600	–
121	100	1023	Sec	Output 9 Pulse width/Seal-in time	1-13600	–
122	100	1024	Sec	Output 10 Pulse width/Seal-in time	1-13600	–
123	100	1025	Sec	Output 11 Pulse width/Seal-in time	1-13600	–
124	100	1026	Sec	Output 12 Pulse width/Seal-in time	1-13600	–
125	1	1049	Sec	Manual Close Delay	0-90	–
126	1	3960	N/A	TIME ZONE	-12-13	–
127	1	5000	N/A	Demand Interval	0-2	–
128	100	2003	N/A	Max Demand IA setting	2-10000	–
129	100	2004	N/A	Max Demand IB setting	2-10000	–
130	100	2005	N/A	Max Demand IC setting	2-10000	–
131	1	5000	N/A	Datalog Interval	0-3600	–
132	1	6500	N/A	Oscillograph sample rate	0-3	–
133	1	6501	N/A	Oscillograph post trigger delay	5-95	–
134	1	6502	N/A	Length of Oscillograph	60-480	–
135	1	6700	N/A	System Alarms	–	–
136	1	5556	N/A	Fault Recorder Show Current Only	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
137	1	7023	N/A	F25 Reference Phase Bit 0 – Phase A Bit 1 – Phase B Bit 2 – Phase C	0-2	–
138	10	7025	V,Sec	F25 Line Min Voltage,UndVol Perm	0-20000	–
139	10	7026	V,Sec	F25 Bus Min Voltage,UndVol Perm	0-20000	–
140	100	7027	Sec	F25 Min Time,Sync Check Perm	100-60000	–
141	100	7028	V,Sec	F25 Min Voltage,Sync Check Perm	1000-30000	–
142	100	7029	Degree	F25 Angle Diff,Sync Check Perm	0-9000	–
143	100	7030	V,Sec	F25 Magn Diff,Sync Check Perm	0-30000	–
144	100	7031	Hz	F25 Freq Diff,Sync Check Perm	0-500	–
145	100	7032	V l-g,Sec	F25 Max Voltage,Sync Check Perm	1000-30000	–
146	1	46820	N/A	F25 Blocking Input Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
147	1	7021	N/A	F25 Output Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
148	1	7022	N/A	Enable/Disable F25 Enable – 1, Disable – 0	0-1	–
149	1	7024	N/A	Enable F25 DeadLogic, Ang diff, Freq diff, Mag diff	-	–
150	1	7033	N/A	F25 Operating voltage side	0-63	–
151	100	7063	V,Sec	F27 Pickup 3-Ph #1	1000-30000	–
152	100	7064	Sec	F27 Definite Time 3-Ph #1	0-60000	–
153	100	7069	V,Sec	F27 Pickup 3-Ph #2	1000-30000	–
154	100	7070	Sec	F27 Definite Time 3-Ph #2	0-60000	–
155	100	7075	V,Sec	F27 Pickup 3-Ph #3	1000-30000	–
156	100	7076	Sec	F27 Definite Time 3-Ph #3	0-60000	–
157	100	7081	V,Sec	F27 Pickup 3-Ph #4	1000-30000	–
158	100	7082	Sec	F27 Definite Time 3-Ph #4	0-60000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
159	100	7087	V Sec	F27 Pickup Ph-A #1	1000-30000	—
160	100	7088	Sec	F27 Definite Time Ph-A #1	0-60000	—
161	100	7092	V,Sec	F27 Pickup Ph-B #1	1000-30000	—
162	100	7093	Sec	F27 Definite Time Ph-B #1	0-60000	—
163	100	7097	V ,Sec	F27 Pickup Ph-C #1	1000-30000	—
164	100	7098	Sec	F27 Definite Time Ph-C #1	0-60000	—
165	100	7102	V,Sec	F27 Pickup Ph-A #2	1000-30000	—
166	100	7103	Sec	F27 Definite Time Ph-A #2	0-60000	—
167	100	7107	V,Sec	F27 Pickup Ph-B #2	1000-30000	—
168	100	7108	Sec	F27 Definite Time Ph-B #2	0-60000	—
169	100	7112	V,Sec	F27 Pickup Ph-C #2	1000-30000	—
170	100	7113	Sec	F27 Definite Time Ph-C #2	0-60000	—
171	100	7117	V,Sec	F27 Pickup Ph-A #3	1000-30000	—
172	100	7118	Sec	F27 Definite Time Ph-A #3	0-60000	—
173	100	7122	V,Sec	F27 Pickup Ph-B #3	1000-30000	—
174	100	7123	Sec	F27 Definite Time Ph-B #3	0-60000	—
175	100	7127	V,Sec	F27 Pickup Ph-C #3	1000-30000	—
176	100	7128	Sec	F27 Definite Time Ph-C #3	0-60000	—
177	100	7132	V ,Sec	F27 Pickup Ph-A #4	1000-30000	—
178	100	7133	Sec	F27 Definite Time Ph-A #4	0-60000	—
179	100	7137	V ,Sec	F27 Pickup Ph-B #4	1000-30000	—
180	100	7138	Sec	F27 Definite Time Ph-B #4	0-60000	—
181	100	7142	V,Sec	F27 Pickup Ph-C #4	1000-30000	—
182	100	7143	Sec	F27 Definite Time Ph-C #4	0-60000	—
183	100	46353	V,Sec	F27#5 3 phases Pickup	1000-30000	—
184	100	46354	s	F27#5 3 phases Definite Time	0-60000	—
185	100	46358	V,Sec	F27#6 3 phases Pickup	1000-30000	—
186	100	46359	s	F27#6 3 phases Definite Time	0-60000	—
187	100	46363	V,Sec	F27#7 3 phases Pickup	1000-30000	—
188	100	46364	s	F27#7 3 phases Definite Time	0-60000	—
189	100	46368	V,Sec	F27#8 3 phases Pickup	1000-30000	—
190	100	46369	s	F27#8 3 phases Definite Time	0-60000	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
191	1	46824	N/A	F27 Blocking Inputs, 3-Ph #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
192	1	7061	N/A	F27 Output, 3-Ph #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
193	1	7062	N/A	Enable/Disable F27 3-Ph #1 Enable – 1, Disable – 0	0-1	–
194	1	7144	N/A	F27 Operating voltage side #1 Vy-0, Vz-1	0-1	–
195	1	46826	N/A	F27 Blocking Inputs, 3-Ph #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
196	1	7067	N/A	F27 Outputs, 3-Ph #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
197	1	7068	N/A	Enable/Disable F27 3-Ph #2 Enable – 1, Disable – 0	0-1	–
198	1	7144	N/A	F27 Operating voltage side #2 Vy-0, Vz-1	0-1	–
199	1	46828	N/A	F27 Blocking Inputs, 3-Ph #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
200	1	7073	N/A	F27 Output, 3-Ph #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
201	1	7074	N/A	Enable/Disable F27 3-Ph #3 Enable – 1, Disable – 0	0-1	–
202	1	7144	N/A	F27 Operating voltage side #3 Vy-0, Vz-1	0-1	–
203	1	46830	N/A	F27 Blocking Inputs, 3-Ph #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
204	1	7079	N/A	F27 Output, 3-Ph #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
205	1	7080	N/A	Enable/Disable F27 3-Ph #4 Enable – 1, Disable – 0	0-1	–
206	1	7144	N/A	F27 Operating voltage side #4 Vz-1,Vy-0	0-1	–
207	1	46832	N/A	F27 Blocking Inputs, Ph-A #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
208	1	7085	N/A	F27 Output, Ph-A #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
209	1	7086	N/A	Enable/Disable F27 Ph-A #1 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
210	1	46838	N/A	F27 Blocking Inputs, Ph-A #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
211	1	7100	N/A	F27 Outputs, Ph-A #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
212	1	7101	N/A	Enable/Disable F27 Ph-A #2 Enable – 1, Disable – 0	0-1	–
213	1	46844	N/A	F27 Blocking Inputs, Ph-A #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
214	1	7115	N/A	F27 Output, Ph-A #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
215	1	7116	N/A	Enable/Disable F27 Ph-A #3 Enable – 1, Disable – 0	0-1	–
216	1	46850	N/A	F27 Blocking Inputs, Ph-A #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
217	1	7130	N/A	F27 Output, Ph-A #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
218	1	7131	N/A	Enable/Disable F27 Ph-A #4 Enable – 1, Disable – 0	0-1	–
219	1	46834	N/A	F27 Blocking Inputs, Ph-B #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
220	1	7090	N/A	F27 Output, Ph-B #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
221	1	7091	N/A	Enable/Disable F27 Ph-B #1 Enable – 1, Disable – 0	0-1	–
222	1	46840	N/A	F27 Blocking Inputs, Ph-B #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
223	1	7105	N/A	F27 Outputs, Ph-B #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
224	1	7106	N/A	Enable/Disable F27 Ph-B #2 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
225	1	46846	N/A	F27 Blocking Inputs, Ph-B #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
226	1	7120	N/A	F27 Outputs, Ph-B #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
227	1	7121	N/A	Enable/Disable F27 Ph-B #3 Enable – 1, Disable – 0	0-1	–
228	1	46852	N/A	F27 Blocking Inputs, Ph-B #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
229	1	7135	N/A	F27 Outputs, Ph-B #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
230	1	7136	N/A	Enable/Disable F27 Ph-B #4 Enable – 1, Disable – 0	0-1	–
231	1	46836	N/A	F27 Blocking Inputs, Ph-C #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
232	1	7095	N/A	F27 Outputs, Ph-C #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
233	1	7096	N/A	Enable/Disable F27 Ph-C #1 Enable – 1, Disable – 0	0-1	–
234	1	46842	N/A	F27 Blocking Inputs, Ph-C #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
235	1	7110	N/A	F27 Outputs, Ph-C #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
236	1	7111	N/A	Enable/Disable F27 Ph-C #2 Enable – 1, Disable – 0	0-1	–
237	1	46848	N/A	F27 Blocking Inputs, Ph-C #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
238	1	7125	N/A	F27 Outputs, Ph-C #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
239	1	7126	N/A	Enable/Disable F27 Ph-C #3 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
240	1	46852	N/A	F27 Blocking Inputs, Ph-C #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
241	1	7140	N/A	F27 Outputs, Ph-C #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
242	1	7141	N/A	Enable/Disable F27 Ph-C #4 Enable – 1, Disable – 0	0-1	–
243	1	7144	N/A	F27#5 3 phases Operating voltage side Vy-0, Vz-1	0-1	–
244	1	7144	N/A	F27#6 3 phases Operating voltage side Vy-0, Vz-1	0-1	–
245	1	7144	N/A	F27#7 3 phases Operating voltage side Vy-0, Vz-1	0-1	–
246	1	7144	N/A	F27#8 3 phases Operating voltage side Vy-0, Vz-1	0-1	–
247	1	46856	N/A	F27#5 3 phases Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-65535	–
248	1	46351	N/A	F27#5 3 phases Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
249	1	46352	N/A	Enable/Disable F27#5 3 phases Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
250	1	46858	N/A	F27#6 3 phases Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-65535	–
251	1	46356	N/A	F27#6 3 phases Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
252	1	46357	N/A	Enable/Disable F27#6 3 phases Enable – 1, Disable – 0	0-1	–
253	1	46860	N/A	F27#7 3 phases Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-65535	–
254	1	46361	N/A	F27#7 3 phases Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
255	1	46362	N/A	Enable/Disable F27#7 3 phases Enable – 1, Disable – 0	0-1	–
256	1	46862	N/A	F27#8 3 phases Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-65535	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
257	1	46366	N/A	F27#8 3 phases Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
258	1	46367	N/A	Enable/Disable F27#8 3 phases Enable – 1, Disable – 0	0-1	–
259	100	8697	V ,Sec	F27PP Pickup 3-Ph	1000-30000	–
260	100	8698	Sec	F27PP Definite time 3-Ph	0-60000	–
261	100	8703	V ,Sec	F27PP Pickup Ph-A	1000-30000	–
262	100	8704	Sec	F27PP Definite time Ph-A	0-60000	–
263	100	8708	V ,Sec	F27PP Pickup Ph-B	1000-30000	–
264	100	8709	Sec	F27PP Definite time Ph-B	0-60000	–
265	100	8713	V ,Sec	F27PP Pickup Ph-C	1000-30000	–
266	100	8714	Sec	F27PP Definite time Ph-C	0-60000	–
267	1	47206	V ,Sec	F27PP Blocking Inputs 3-Ph Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
268	1	8695	Sec	F27PP Outputs 3-Ph Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
269	1	8696	N/A	Enable/Disable F27PP 3-Ph Enable – 1, Disable – 0	0-1	–
270	1	8715	N/A	F27PP Operating voltage side 0 – Y Side 1 – Z Side	-	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
271	1	47208	N/A	F27PP Blocking Inputs Ph-AB Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
272	1	8701	N/A	F27PP Outputs Ph-AB Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
273	1	8702	N/A	Enable/Disable F27PP Ph-AB Enable – 1, Disable – 0	0-1	–
274	1	47210	N/A	F27PP Blocking Inputs Ph-BC Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
275	1	8706	N/A	F27PP Outputs Ph-BC Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
276	1	8707	N/A	Enable/Disable F27PP Ph-BC Enable – 1, Disable – 0	0-1	–
277	1	47212	N/A	F27PP Blocking Inputs Ph-CA Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
278	1	8711	N/A	F27PP Outputs Ph-CA Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
279	1	8712	N/A	Enable/Disable F27PP Ph-CA Enable – 1, Disable – 0	0-1	–
280	100	8729	V,Sec	F27Vz1 Pickup	1000-30000	–
281	100	8730	Sec	F27Vz1 Definite time	0-60000	–
282	1	47214	N/A	F27Vz1 Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
283	1	8727	N/A	F27Vz1 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
284	1	8728	N/A	Enable/Disable F27Vz1 Enable – 1, Disable – 0	0-1	–
285	100	7153	pu	F32#1 Pickup 3-Ph	-300-300	–
286	100	7155	Sec	F32#1 Definite Time 3-Ph	0-60000	–
287	1	7157	N/A	F32#1 Operating Power 3-Ph 0-Real and under power 1-Reactive 2-over power 3-Reactive and over power	0-3	–
288	100	7185	pu	F32#1 Pickup Ph-A	-300-300	–
289	100	7187	Sec	F32#1 Definite Time Ph-A	0-60000	–
290	1	7188	N/A	F32#1 Operating Power Ph-A 0-Real and under power 1-Reactive 2-over power 3-Reactive and over power	0-3	–
291	100	7192	pu	F32#1 Pickup Ph-B	-300-300	–
292	100	7194	Sec	F32#1 Definite Time Ph-B	0-60000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
293	1	7195	N/A	F32#1 Operating Power Ph-B 0-Real and under power 1-Reactive 2-over power 3-Reactive and over power	0-3	—
294	100	7199	pu	F32#1 Pickup Ph-C	-300-300	—
295	100	7201	Sec	F32#1 Definite Time Ph-C	0-60000	—
296	1	7202	N/A	F32#1 Operating Power Ph-C 0-Real and under power 1-Reactive 2-over power 3-Reactive and over power	0-3	—
297	100	7161	pu	F32#2 Pickup 3-Ph	-300-300	—
298	100	7163	Sec	F32#2 Definite Time 3-Ph	0-60000	—
299	1	7165	N/A	F32#2 Operating Power 3-Ph 0-Real and under power 1-Reactive 2-over power 3-Reactive and over power	0-3	—
300	100	7206	pu	F32#2 Pickup Ph-A	-300-300	—
301	100	7208	Sec	F32#2 Definite Time Ph-A	0-60000	—
302	1	7209	N/A	F32#2 Operating Power Ph-A 0-Real and under power 1-Reactive 2-over power 3-Reactive and over power	0-3	—
303	100	7213	pu	F32#2 Pickup Ph-B	-300-300	—
304	100	7215	Sec	F32#2 Definite Time Ph-B	0-60000	—
305	1	7216	N/A	F32#2 Operating Power Ph-B 0-Real and under power 1-Reactive 2-over power 3-Reactive and over power	0-3	—
306	100	7220	pu	F32#2 Pickup Ph-C	-300-300	—
307	100	7222	Sec	F32#2 Definite Time Ph-C	0-60000	—
308	1	7223	N/A	F32#2 Operating Power Ph-C 0-Real and under power 1-Reactive 2-over power 3-Reactive and over power	0-3	—
309	100	7169	pu	F32#3 Pickup 3-Ph	-300-300	—
310	100	7171	Sec	F32#3 Definite Time 3-Ph	0-60000	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
311	1	7173	N/A	F32#3 Operating Power 3-Ph 0-Real and under power 1-Reactive 2-over power 3-Reactive and over power	0-3	—
312	100	7227	pu	F32#3 Pickup Ph-A	-300-300	—
313	100	7229	Sec	F32#3 Definite Time Ph-A	0-60000	—
314	1	7230	N/A	F32#3 Operating Power Ph-A 0-Real and under power 1-Reactive 2-over power 3-Reactive and over power	0-3	—
315	100	7234	pu	F32#3 Pickup Ph-B	-300-300	—
316	100	7236	Sec	F32#3 Definite Time Ph-B	0-60000	—
317	1	7237	N/A	F32#3 Operating Power Ph-B 0-Real and under power 1-Reactive 2-over power 3-Reactive and over power	0-3	—
318	100	7241	pu	F32#3 Pickup Ph-C	-300-300	—
319	100	7243	Sec	F32#3 Definite Time Ph-C	0-60000	—
320	1	7244	N/A	F32#3 Operating Power Ph-C 0-Real and under power 1-Reactive 2-over power 3-Reactive and over power	0-3	—
321	100	7177	pu	F32#4 Pickup 3-Ph	-300-300	—
322	100	7179	Sec	F32#4 Definite Time 3-Ph	0-60000	—
323	1	7181	N/A	F32#4 Operating Power 3-Ph 0-Real and under power 1-Reactive 2-over power 3-Reactive and over power	0-3	—
324	100	7248	pu	F32#4 Pickup Ph-A	-300-300	—
325	100	7250	Sec	F32#4 Definite Time Ph-A	0-60000	—
326	1	7251	N/A	F32#4 Operating Power Ph-A	0-3	—
327	100	7255	pu	F32#4 Pickup Ph-B	-300-300	—
328	100	7257	Sec	F32#4 Definite Time Ph-B	0-60000	—
329	1	7258	N/A	F32#4 Operating Power Ph-B 0-Real and under power 1-Reactive 2-over power 3-Reactive and over power	0-3	—
330	100	7262	pu	F32#4 Pickup Ph-C	-300-300	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
331	100	7264	Sec	F32#4 Definite Time Ph-C	0-60000	—
332	1	7265	N/A	F32#4 Operating Power Ph-C 0-Real and under power 1-Reactive 2-over power 3-Reactive and over power	0-3	—
333	1	46864	N/A	F32 Blocking Inputs, 3-Ph #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	—
334	1	7151	N/A	F32 Outputs, 3-Ph #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	—
335	1	7152	N/A	Enable/Disable F32 3-Ph #1 Enable – 1, Disable – 0	0-1	—
336	1	46866	N/A	F32 Blocking Inputs, 3-Ph #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	—
337	1	7159	N/A	F32 Outputs, 3-Ph #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	—
338	1	7160	N/A	Enable/Disable F32 3-Ph #2 Enable – 1, Disable – 0	0-1	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
339	1	46868	N/A	F32 Blocking Inputs, 3-Ph #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
340	1	7167	N/A	F32 Outputs, 3-Ph #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
341	1	7168	N/A	Enable/Disable F32 3-Ph #3 Enable – 1, Disable – 0	0-1	–
342	1	46870	N/A	F32 Blocking Inputs, 3-Ph #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
343	1	7175	N/A	F32 Outputs, 3-Ph #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
344	1	7176	N/A	Enable/Disable F32 3-Ph #4 Enable – 1, Disable – 0	0-1	–
345	1	46872	N/A	F32 Blocking Inputs, Ph-A #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
346	1	7183	N/A	F32 Outputs, Ph-A #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
347	1	7184	N/A	Enable/Disable F32 Ph-A #1 Enable – 1, Disable – 0	0-1	–
348	1	46878	N/A	F32 Blocking Inputs, Ph-A #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
349	1	7204	N/A	F32 Outputs, Ph-A #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
350	1	7205	N/A	Enable/Disable F32 Ph-A #2 Enable – 1, Disable – 0	0-1	–
351	1	46884	N/A	F32 Blocking Inputs, Ph-A #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
352	1	7225	N/A	F32 Outputs, Ph-A #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
353	1	7226	N/A	Enable/Disable F32 Ph-A #3 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
354	1	46890	N/A	F32 Blocking Inputs, Ph-A #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
355	1	7246	N/A	F32 Outputs, Ph-A #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
356	1	7247	N/A	Enable/Disable F32 Ph-A #4 Enable – 1, Disable – 0	0-1	–
357	1	46874	N/A	F32 Blocking Inputs, Ph-B #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
358	1	7190	N/A	F32 Outputs, Ph-B #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
359	1	7191	N/A	Enable/Disable F32 Ph-B #1 Enable – 1, Disable – 0	0-1	–
360	1	46880	N/A	F32 Blocking Inputs, Ph-B #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
361	1	7211	N/A	F32 Outputs, Ph-B #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
362	1	7212	N/A	Enable/Disable F32 Ph-B #2 Enable – 1, Disable – 0	0-1	–
363	1	46886	N/A	F32 Blocking Inputs, Ph-B #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
364	1	7225	N/A	F32 Outputs, Ph-B #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
365	1	7226	N/A	Enable/Disable F32 Ph-B #3 Enable – 1, Disable – 0	0-1	–
366	1	46892	N/A	F32 Blocking Inputs, Ph-B #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
367	1	7253	N/A	F32 Outputs, Ph-B #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
368	1	7254	N/A	Enable/Disable F32 Ph-B #4 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
369	1	46876	N/A	F32 Blocking Inputs, Ph-C #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
370	1	7197	N/A	F32 Outputs, Ph-C #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
371	1	7198	N/A	Enable/Disable F32 Ph-C #1 Enable – 1, Disable – 0	0-1	–
372	1	46882	N/A	F32 Blocking Inputs, Ph-C #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
373	1	7218	N/A	F32 Outputs, Ph-C #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
374	1	7219	N/A	Enable/Disable F32 Ph-C #2 Enable – 1, Disable – 0	0-1	–
375	1	46888	N/A	F32 Blocking Inputs, Ph-C #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
376	1	7239	N/A	F32 Outputs, Ph-C #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
377	1	7240	N/A	Enable/Disable F32 Ph-C #3 Enable – 1, Disable – 0	0-1	–
378	1	46894	N/A	F32 Blocking Inputs, Ph-C #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
379	1	7260	N/A	F32 Outputs, Ph-C #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
380	1	7261	N/A	Enable/Disable F32 Ph-C #4 Enable – 1, Disable – 0	0-1	–
381	1	46014	N/A	Enable/Disable F40 #1 Enable – 1, Disable – 0	0-1	–
382	1	46896	N/A	F40 Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-65535	–
383	1	46013	N/A	F40 Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
384	10	46015	Ohm	F40 Circle Diameter #1	5-5000	–
385	10	46016	Ohm	F40 Offset #1	-2500-2500	–
386	100	46017	Sec	F40 Time Delay #1	1-30000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
387	1	46020	N/A	Enable/Disable F40 #2 Enable – 1, Disable – 0	0-1	–
388	1	46898	N/A	F40 Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-65535	–
389	1	46019	N/A	F40 Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
390	10	46021	Ohm	F40 Circle Diameter #2	5-5000	–
391	10	46022	Ohm	F40 Offset #2	-2500-2500	–
392	100	46023	Sec	F40 Time Delay #2	1-30000	–
393	1	46026	N/A	Enable/Disable F40VC #1 Enable – 1, Disable – 0	0-1	–
394	1	46900	N/A	F40 Voltage Control Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-65535	–
395	1	46025	N/A	F40 Voltage Control Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
396	100	46027	Sec	F40 Voltage Control Time Delay #1	1-30000	–
397	1	46030	N/A	Enable/Disable F40VC #2 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
398	1	46902	N/A	F40 Voltage Control Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-65535	–
399	1	46029	N/A	F40 Voltage Control Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
400	100	46031	Sec	F40 Voltage Control Time Delay #2	1-30000	–
401	–	46032	V	F40 Voltage Control in Volts	5-180	–
402	–	46033	Deg	F40 Directional Element in Degrees	0-20	–
403	100	7275	A	F50 Pickup 3-Ph #1	2-2000	–
404	100	7276	Sec	F50 Definite Time 3-Ph #1	0-60000	–
405	100	7281	A	F50 Pickup 3-Ph #2	2-2000	–
406	100	7282	Sec	F50 Definite Time 3-Ph #2	0-60000	–
407	100	7287	A	F50 Pickup 3-Ph #3	2-2000	–
408	100	7288	Sec	F50 Definite Time 3-Ph #3	0-60000	–
409	100	7293	A	F50 Pickup 3-Ph #4	2-2000	–
410	100	7294	Sec	F50 Definite Time 3-Ph #4	0-60000	–
411	100	7299	A	F50 Pickup 3-Ph #5	2-2000	–
412	100	7300	Sec	F50 Definite Time 3-Ph #5	0-60000	–
413	100	7305	A	F50 Pickup Ph-A #1	2-2000	–
414	100	7306	Sec	F50 Definite Time Ph-A #1	0-60000	–
415	100	7310	A	F50 Pickup Ph-B #1	2-2000	–
416	100	7311	Sec	F50 Definite Time Ph-B #1	0-60000	–
417	100	7315	A	F50 Pickup Ph-C #1	2-2000	–
418	100	7316	Sec	F50 Definite Time Ph-C #1	0-60000	–
419	100	7320	A	F50 Pickup Ph-A #2	2-2000	–
420	100	7321	Sec	F50 Definite Time Ph-A #2	0-60000	–
421	100	7325	A	F50 Pickup Ph-B #2	2-2000	–
422	100	7326	Sec	F50 Definite Time Ph-B #2	0-60000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
423	100	7330	A	F50 Pickup Ph-C #2	2-2000	—
424	100	7331	Sec	F50 Definite Time Ph-C #2	0-60000	—
425	100	7335	A	F50 Pickup Ph-A #3	2-2000	—
426	100	7336	Sec	F50 Definite Time Ph-A #3	0-60000	—
427	100	7340	A	F50 Pickup Ph-B #3	2-2000	—
428	100	7341	Sec	F50 Definite Time Ph-B #3	0-60000	—
429	100	7345	A	F50 Pickup Ph-C #3	2-2000	—
430	100	7346	Sec	F50 Definite Time Ph-C #3	0-60000	—
431	100	7350	A	F50 Pickup Ph-A #4	2-2000	—
432	100	7351	Sec	F50 Definite Time Ph-A #4	0-60000	—
433	100	7355	A	F50 Pickup Ph-B #4	2-2000	—
434	100	7356	Sec	F50 Definite Time Ph-B #4	0-60000	—
435	100	7360	A	F50 Pickup Ph-C #4	2-2000	—
436	100	7361	Sec	F50 Definite Time Ph-C #4	0-60000	—
437	100	7365	A	F50 Pickup Ph-A #5	2-2000	—
438	100	7366	Sec	F50 Definite Time Ph-A #5	0-60000	—
439	100	7370	A	F50 Pickup Ph-B #5	2-2000	—
440	100	7371	Sec	F50 Definite Time Ph-B #5	0-60000	—
441	100	7375	A	F50 Pickup Ph-C #5	2-2000	—
442	100	7376	Sec	F50 Definite Time Ph-C #5	0-60000	—
443	1	46904	N/A	F50 Blocking Inputs, 3-Ph #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	—
444	1	7273	N/A	F50 Outputs, 3-Ph #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	—
445	1	7274	N/A	Enable/Disable F50 3-Ph #1 Enable – 1, Disable – 0	0-1	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
446	1	46906	N/A	F50 Blocking Inputs, 3-Ph #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
447	1	7279	N/A	F50 Outputs, 3-Ph #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
448	1	7280	N/A	Enable/Disable F50 3-Ph #2 Enable – 1, Disable – 0	0-1	–
449	1	46908	N/A	F50 Blocking Inputs, 3-Ph #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
450	1	7285	N/A	F50 Outputs, 3-Ph #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
451	1	7286	N/A	Enable/Disable F50 3-Ph #3 Enable – 1, Disable – 0	0-1	–
452	1	46910	N/A	F50 Blocking Inputs, 3-Ph #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
453	1	7291	N/A	F50 Outputs, 3-Ph #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
454	1	7292	N/A	Enable/Disable F50 3-Ph #4 Enable – 1, Disable – 0	0-1	–
455	1	46912	N/A	F50 Blocking Inputs, 3-Ph #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
456	1	7297	N/A	F50 Outputs, 3-Ph #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
457	1	7298	N/A	Enable/Disable F50 3-Ph #5 Enable – 1, Disable – 0	0-1	–
458	1	46914	N/A	F50 Blocking Inputs, Ph-A #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
459	1	7303	N/A	F50 Outputs, Ph-A #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
460	1	7304	N/A	Enable/Disable F50 Ph-A #1 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
461	1	46920	N/A	F50 Blocking Inputs, Ph-A #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
462	1	7318	N/A	F50 Outputs, Ph-A #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
463	1	7319	N/A	Enable/Disable F50 Ph-A #2 Enable – 1, Disable – 0	0-1	–
464	1	46926	N/A	F50 Blocking Inputs, Ph-A #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
465	1	7333	N/A	F50 Outputs, Ph-A #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
466	1	7334	N/A	Enable/Disable F50 Ph-A #3 Enable – 1, Disable – 0	0-1	–
467	1	46932	N/A	F50 Blocking Inputs, Ph-A #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
468	1	7348	N/A	F50 Outputs, Ph-A #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
469	1	7349	N/A	Enable/Disable F50 Ph-A #4 Enable – 1, Disable – 0	0-1	–
470	1	46938	N/A	F50 Blocking Inputs, Ph-A #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
471	1	7363	N/A	F50 Outputs, Ph-A #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
472	1	7364	N/A	Enable/Disable F50 Ph-A #5 Enable – 1, Disable – 0	0-1	–
473	1	46916	N/A	F50 Blocking Inputs, Ph-B #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
474	1	7308	N/A	F50 Outputs, Ph-B #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
475	1	7309	N/A	Enable/Disable F50 Ph-B #1 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
476	1	46922	N/A	F50 Blocking Inputs, Ph-B #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
477	1	7323	N/A	F50 Outputs, Ph-B #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
478	1	7324	N/A	Enable/Disable F50 Ph-B #2 Enable – 1, Disable – 0	0-1	–
479	1	46928	N/A	F50 Blocking Inputs, Ph-B #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
480	1	7338	N/A	F50 Outputs, Ph-B #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
481	1	7339	N/A	Enable/Disable F50 Ph-B #3 Enable – 1, Disable – 0	0-1	–
482	1	46934	N/A	F50 Blocking Inputs, Ph-B #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
483	1	7353	N/A	F50 Outputs, Ph-B #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
484	1	7354	N/A	Enable/Disable F50 Ph-B #4 Enable – 1, Disable – 0	0-1	–
485	1	46940	N/A	F50 Blocking Inputs, Ph-B #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
486	1	7368	N/A	F50 Outputs, Ph-B #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
487	1	7369	N/A	Enable/Disable F50 Ph-B #5 Enable – 1, Disable – 0	0-1	–
488	1	46918	N/A	F50 Blocking Inputs, Ph-C #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
489	1	7313	N/A	F50 Outputs, Ph-C #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
490	1	7314	N/A	Enable/Disable F50 Ph-C #1 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
491	1	46924	N/A	F50 Blocking Inputs, Ph-C #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
492	1	7328	N/A	F50 Outputs, Ph-C #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
493	1	7329	N/A	Enable/Disable F50 Ph-C #2 Enable – 1, Disable – 0	0-1	–
494	1	46930	N/A	F50 Blocking Inputs, Ph-C #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
495	1	7343	N/A	F50 Outputs, Ph-C #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
496	1	7344	N/A	Enable/Disable F50 Ph-C #3 Enable – 1, Disable – 0	0-1	–
497	1	46936	N/A	F50 Blocking Inputs, Ph-C #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
498	1	7358	N/A	F50 Outputs, Ph-C #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
499	1	7359	N/A	Enable/Disable F50 Ph-C #4 Enable – 1, Disable – 0	0-1	–
500	1	46942	N/A	F50 Blocking Inputs, Ph-C #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
501	1	7373	N/A	F50 Outputs, Ph-C #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
502	1	7374	N/A	Enable/Disable F50 Ph-C #5 Enable – 1, Disable – 0	0-1	–
503	100	7391	A	F50N Pickup #1	2-2000	–
504	100	7392	Sec	F50N Definite Time #1	0-60000	–
505	100	7396	A	F50N Pickup #2	2-2000	–
506	100	7397	Sec	F50N Definite Time #2	0-60000	–
507	100	7401	A	F50N Pickup #3	2-2000	–
508	100	7402	Sec	F50N Definite Time #3	0-60000	–
509	100	7406	A	F50N Pickup #4	2-2000	–
510	100	7407	Sec	F50N Definite Time #4	0-60000	–
511	100	7411	A	F50N Pickup #5	2-2000	–
512	100	7412	Sec	F50N Definite Time #5	0-60000	–
513	1	46944	N/A	F50N Blocking Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
514	1	7389	N/A	F50N Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
515	1	7390	N/A	Enable/Disable F50N #1 Enable – 1, Disable – 0	0-1	–
516	1	46946	N/A	F50N Blocking Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
517	1	7394	N/A	F50N Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
518	1	7395	N/A	Enable/Disable F50N #2 Enable – 1, Disable – 0	0-1	–
519	1	46948	N/A	F50N Blocking Inputs #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
520	1	7399	N/A	F50N Outputs #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
521	1	7400	N/A	Enable/Disable F50N #3 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
522	1	46950	N/A	F50N Blocking Inputs #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
523	1	7404	N/A	F50N Outputs #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
524	1	7405	N/A	Enable/Disable F50N #4 Enable – 1, Disable – 0	0-1	–
525	1	46952	N/A	F50N Blocking Inputs #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
526	1	7409	N/A	F50N Outputs #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
527	1	7410	N/A	Enable/Disable F50N #5 Enable – 1, Disable – 0	0-1	–
528	100	7427	A	F50G Pickup #1	2-2000	–
529	100	7428	Sec	F50G Definite Time #1	0-6000	–
530	100	7432	A	F50G Pickup #2	2-2000	–
531	100	7433	Sec	F50G Definite Time #2	0-6000	–
532	100	7437	A	F50G Pickup #3	2-2000	–
533	100	7438	Sec	F50G Definite Time #3	0-6000	–
534	100	7442	A	F50G Pickup #4	2-2000	–
535	100	7443	Sec	F50G Definite Time #4	0-6000	–
536	100	7447	A	F50G Pickup #5	2-2000	–
537	100	7448	Sec	F50G Definite Time #5	0-6000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
538	1	46954	N/A	F50G Blocking Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
539	1	7425	N/A	F50G Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
540	1	7426	N/A	Enable/Disable F50G #1 Enable – 1, Disable – 0	0-1	–
541	1	46956	N/A	F50G Blocking Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
542	1	7430	N/A	F50G Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
543	1	7431	N/A	Enable/Disable F50G #2 Enable – 1, Disable – 0	0-1	–
544	1	46958	N/A	F50G Blocking Inputs #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
545	1	7435	N/A	F50G Outputs #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
546	1	7436	N/A	Enable/Disable F50G #3 Enable – 1, Disable – 0	0-1	–
547	1	46960	N/A	F50G Blocking Inputs #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
548	1	7440	N/A	F50G Outputs #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
549	1	7441	N/A	Enable/Disable F50G #4 Enable – 1, Disable – 0	0-1	–
550	1	46962	N/A	F50G Blocking Inputs #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
551	1	7445	N/A	F50G Outputs #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
552	1	7446	N/A	Enable/Disable F50G #5 Enable – 1, Disable – 0	0-1	–
553	100	7463	A	F46DT Pickup #1	2-2000	–
554	100	7464	Sec	F46DT Definite Time #1	0-60000	–
555	100	7468	A	F46DT Pickup #2	2-2000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
556	100	7469	Sec	F46DT Definite Time #2	0-60000	—
557	100	7473	A	F46DT Pickup #3	2-2000	—
558	100	7474	Sec	F46DT Definite Time #3	0-60000	—
559	100	7478	A	F46DT Pickup #4	2-2000	—
560	100	7479	Sec	F46DT Definite Time #4	0-60000	—
561	100	7483	A	F46DT Pickup #5	2-2000	—
562	100	7484	Sec	F46DT Definite Time #5	0-60000	—
563	1	46964	N/A	F46DT Blocking Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	—
564	1	7461	N/A	F46DT Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	—
565	1	7462	N/A	Enable/Disable F46DT #1 Enable – 1, Disable – 0	0-1	—
566	1	46966	N/A	F46DT Blocking Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	—
567	1	7466	N/A	F46DT Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	—
568	1	7467	N/A	Enable/Disable F46DT #2 Enable – 1, Disable – 0	0-1	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
569	1	46968	N/A	F46DT Blocking Inputs #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
570	1	7471	N/A	F46DT Outputs #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
571	1	7472	N/A	Enable/Disable F46DT #3 Enable – 1, Disable – 0	0-1	–
572	1	46970	N/A	F46DT Blocking Inputs #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
573	1	7476	N/A	F46DT Outputs #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
574	1	7477	N/A	Enable/Disable F46DT #4 Enable – 1, Disable – 0	0-1	–
575	1	46972	N/A	F46DT Blocking Inputs #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
576	1	7481	N/A	F46DT Outputs #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
577	1	7482	N/A	Enable/Disable F46DT #5 Enable – 1, Disable – 0	0-1	–
578	1	46974	N/A	F51 Blocking Inputs, 3-Ph #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
579	1	7497	N/A	F51 Outputs, 3-Ph #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
580	1	7498	N/A	Enable/Disable F51 3-Ph #1 Enable – 1, Disable – 0	0-1	–
581	1	7501	N/A	Enable Electromechanical Reset for F51, 3-Ph #1 Enable – 1, Disable – 0	0-1	–
582	1	46976	N/A	F51 Blocking Inputs, 3-Ph #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
583	1	7511	N/A	F51 Outputs, 3-Ph #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
584	1	7512	N/A	Enable/Disable F51 3-Ph #2 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
585	1	7515	N/A	Enable Electromechanical Reset for F51, 3-Ph #2 Enable – 1, Disable – 0	0-1	–
586	1	46978	N/A	F51 Blocking Inputs, 3-Ph #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
587	1	7525	N/A	F51 Outputs, 3-Ph #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
588	1	7526	N/A	Enable/Disable F51 3-Ph #3 Enable – 1, Disable – 0	0-1	–
589	1	7529	N/A	Enable Electromechanical Reset for F51, 3-Ph #3 Enable – 1, Disable – 0	0-1	–
590	1	46980	N/A	F51 Blocking Inputs, 3-Ph #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
591	1	7539	N/A	F51 Outputs, 3-Ph #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
592	1	7540	N/A	Enable/Disable F51 3-Ph #4 Enable – 1, Disable – 0	0-1	–
593	1	7543	N/A	Enable Electromechanical Reset for F51, 3-Ph #4 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
594	1	46982	N/A	F51 Blocking Inputs, 3-Ph #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
595	1	7553	N/A	F51 Outputs, 3-Ph #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
596	1	7554	N/A	Enable/Disable F51 3-Ph #5 Enable – 1, Disable – 0	0-1	–
597	1	7557	N/A	Enable Electromechanical Reset for F51, 3-Ph #5 Enable – 1, Disable – 0	0-1	–
598	1	46984	N/A	F51 Blocking Inputs, Ph-A #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
599	1	7567	N/A	F51 Outputs, Ph-A #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
600	1	7568	N/A	Enable/Disable F51 Ph-A #1 Enable – 1, Disable – 0	0-1	–
601	1	7571	N/A	Enable Electromechanical Reset for F51, Ph-A #1 Enable – 1, Disable – 0	0-1	–
602	1	46990	N/A	F51 Blocking Inputs, Ph-A #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
603	1	7606	N/A	F51 Outputs, Ph-A #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
604	1	7607	N/A	Enable/Disable F51 Ph-A #2 Enable – 1, Disable – 0	0-1	–
605	1	7610	N/A	Enable Electromechanical Reset for F51, Ph-A #2 Enable – 1, Disable – 0	0-1	–
606	1	46996	N/A	F51 Blocking Inputs, Ph-A #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
607	1	7645	N/A	F51 Outputs, Ph-A #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
608	1	7646	N/A	Enable/Disable F51 Ph-A #3 Enable – 1, Disable – 0	0-1	–
609	1	7649	N/A	Enable Electromechanical Reset for F51, Ph-A #3 Enable – 1, Disable – 0	0-1	–
610	1	47002	N/A	F51 Blocking Inputs, Ph-A #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
611	1	7684	N/A	F51 Outputs, Ph-A #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
612	1	7685	N/A	Enable/Disable F51 Ph-A #4 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
613	1	7688	N/A	Enable Electromechanical Reset for F51, Ph-A #4 Enable – 1, Disable – 0	0-1	–
614	1	47008	N/A	F51 Blocking Inputs, Ph-A #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
615	1	7723	N/A	F51 Outputs, Ph-A #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
616	1	7724	N/A	Enable/Disable F51 Ph-A #5 Enable – 1, Disable – 0	0-1	–
617	1	7727	N/A	Enable Electromechanical Reset for F51, Ph-A #5 Enable – 1, Disable – 0	0-1	–
618	1	46986	N/A	F51 Blocking Inputs, Ph-B #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
619	1	7580	N/A	F51 Outputs, Ph-B #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
620	1	7581	N/A	Enable/Disable F51 Ph-B #1 Enable – 1, Disable – 0	0-1	–
621	1	7584	N/A	Enable Electromechanical Reset for F51, Ph-B #1 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
622	1	46992	N/A	F51 Blocking Inputs, Ph-B #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
623	1	7619	N/A	F51 Outputs, Ph-B #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
624	1	7620	N/A	Enable/Disable F51 Ph-B #2 Enable – 1, Disable – 0	0-1	–
625	1	7623	N/A	Enable Electromechanical Reset for F51, Ph-B #2 Enable – 1, Disable – 0	0-1	–
626	1	46998	N/A	F51 Blocking Inputs, Ph-B #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
627	1	7658	N/A	F51 Outputs, Ph-B #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
628	1	7659	N/A	Enable/Disable F51 Ph-B #3 Enable – 1, Disable – 0	0-1	–
629	1	7662	N/A	Enable Electromechanical Reset for F51, Ph-B #3 Enable – 1, Disable – 0	0-1	–
630	1	47004	N/A	F51 Blocking Inputs, Ph-B #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
631	1	7697	N/A	F51 Outputs, Ph-B #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
632	1	7698	N/A	Enable/Disable F51 Ph-B #4 Enable – 1, Disable – 0	0-1	–
633	1	7701	N/A	Enable Electromechanical Reset for F51, Ph-B #4 Enable – 1, Disable – 0	0-1	–
634	1	47014	N/A	F51 Blocking Inputs, Ph-B #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
635	1	7736	N/A	F51 Outputs, Ph-B #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
636	1	7737	N/A	Enable/Disable F51 Ph-B #5 Enable – 1, Disable – 0	0-1	–
637	1	7740	N/A	Enable Electromechanical Reset for F51, Ph-B #5 Enable – 1, Disable – 0	0-1	–
638	1	46988	N/A	F51 Blocking Inputs, Ph-C #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
639	1	7593	N/A	F51 Outputs, Ph-C #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
640	1	7594	N/A	Enable/Disable F51 Ph-C #1 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
641	1	7597	N/A	Enable Electromechanical Reset for F51, Ph-C #1 Enable – 1, Disable – 0	0-1	–
642	1	46994	N/A	F51 Blocking Inputs, Ph-C #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
643	1	7632	N/A	F51 Outputs, Ph-C #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
644	1	7633	N/A	Enable/Disable F51 Ph-C #2 Enable – 1, Disable – 0	0-1	–
645	1	7636	N/A	Enable Electromechanical Reset for F51, Ph-C #2 Enable – 1, Disable – 0	0-1	–
646	1	47000	N/A	F51 Blocking Inputs, Ph-C #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
647	1	7671	N/A	F51 Outputs, Ph-C #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
648	1	7672	N/A	Enable/Disable F51 Ph-C #3 Enable – 1, Disable – 0	0-1	–
649	1	7675	N/A	Enable Electromechanical Reset for F51, Ph-C #3 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
650	1	47006	N/A	F51 Blocking Inputs, Ph-C #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
651	1	7710	N/A	F51 Outputs, Ph-C #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
652	1	7711	N/A	Enable/Disable F51 Ph-C #4 Enable – 1, Disable – 0	0-1	–
653	1	7714	N/A	Enable Electromechanical Reset for F51, Ph-C #4 Enable – 1, Disable – 0	0-1	–
654	1	47012	N/A	F51 Blocking Inputs, Ph-C #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12- In FL Bit 5 – In 6 Bit 13- In V1 Bit 6 – In 7 Bit 14- In V2 Bit 7 – In 8 Bit 15- In V3	0-8191	–
655	1	7749	N/A	F51 Outputs, Ph-C #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
656	1	7750	N/A	Enable/Disable F51 Ph-C #5 Enable – 1, Disable – 0	0-1	–
657	1	7753	N/A	Enable Electromechanical Reset for F51, Ph-C #5 Enable – 1, Disable – 0	0-1	–
658	100	7499	A	F51 Pickup 3-Ph #1	2-320	–
659	1	7500	N/A	F51 Curve Selection 3-ph #1	0-71	–
660	1000	7502	Sec	F51 Reset Co-eff 3-Ph #1	1-30000	–
661	100	7504	N/A	F51 Time Multiplier 3-Ph #1	5-100	–
662	100	7505	Sec	F51 Time Adder 3-Ph #1	0-3000	–
663	100	7506	Sec	F51 Min response time adder 3-Ph #1	0-100	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
664	1	30717	N/A	F51 Voltage control/restraint enable 3-Ph #1 Disable – 0, Voltage control – 1, Restraint – 2	0-2	–
665	10	30718	%	F51 Voltage control/restraint pickup 3-Ph #1	40-1500	–
666	100	7513	A	F51 Pickup 3-Ph #2	2-320	–
667	1	7514	N/A	F51 Curve Selection 3-ph #2	0-71	–
668	1000	7516	Sec	F51 Reset Co-eff 3-Ph #2	1-30000	–
669	100	7518	N/A	F51 Time Multiplier 3-Ph #2	5-100	–
670	100	7519	Sec	F51 Time Adder 3-Ph #2	0-3000	–
671	100	7520	Sec	F51 Min response time adder 3-Ph #2	0-100	–
672	1	30719	N/A	F51 Voltage control/restraint enable 3-Ph #2 Disable – 0, Voltage control – 1, Restraint – 2	0-2	–
673	10	30720	%	F51 Voltage control/restraint pickup 3-Ph #2	40-1500	–
674	100	7527	A	F51 Pickup 3-Ph #3	2-320	–
675	1	7528	N/A	F51 Curve Selection 3-ph #3	0-71	–
676	1000	7530	Sec	F51 Reset Co-eff 3-Ph #3	1-30000	–
677	100	7532	N/A	F51 Time Multiplier 3-Ph #3	5-100	–
678	100	7533	Sec	F51 Time Adder 3-Ph #3	0-3000	–
679	100	7534	Sec	F51 Min response time adder 3-Ph #3	0-100	–
680	1	30721	N/A	F51 Voltage control/restraint enable 3-Ph #3 Disable – 0, Voltage control – 1, Restraint – 2	0-2	–
681	10	30722	%	F51 Voltage control/restraint pickup 3-Ph #3	40-1500	–
682	100	7541	A	F51 Pickup 3-Ph #4	2-320	–
683	1	7542	N/A	F51 Curve Selection 3-ph #4	0-71	–
684	1000	7544	Sec	F51 Reset Co-eff 3-Ph #4	1-30000	–
685	100	7546	N/A	F51 Time Multiplier 3-Ph #4	5-100	–
686	100	7547	Sec	F51 Time Adder 3-Ph #4	0-3000	–
687	100	7548	Sec	F51 Min response time adder 3-Ph #4	0-100	–
688	1	30723	N/A	F51 Voltage control/restraint enable 3-Ph #4 Disable – 0, Voltage control – 1, Restraint – 2	0-2	–
689	10	30724	%	F51 Voltage control/restraint pickup 3-Ph #4	40-1500	–
690	100	7555	A	F51 Pickup 3-Ph #5	2-320	–
691	1	7556	N/A	F51 Curve Selection 3-ph #5	0-71	–
692	1000	7558	Sec	F51 Reset Co-eff 3-Ph #5	1-30000	–
693	100	7560	N/A	F51 Time Multiplier 3-Ph #5	5-100	–
694	100	7561	Sec	F51 Time Adder 3-Ph #5	0-3000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
695	100	7562	Sec	F51 Min response time adder 3-Ph #5	0-100	–
696	1	30725	N/A	F51 Voltage control/restraint enable 3-Ph #5 Disable – 0, Voltage control – 1, Restraint – 2	0-2	–
697	10	30726	%	F51 Voltage control/restraint pickup 3-Ph #5	40-1500	–
698	100	7569	A	F51 Pickup Ph-A #1	2-320	–
699	1	7570	N/A	F51 Curve Selection Ph-A #1	0-71	–
700	1000	7572	Sec	F51 Reset Co-eff Ph-A #1	1-30000	–
701	100	7574	N/A	F51 Time Multiplier Ph-A #1	5-100	–
702	100	7575	Sec	F51 Time Adder Ph-A #1	0-3000	–
703	100	7576	Sec	F51 Min response time adder Ph-A #1	0-100	–
704	1	30727	N/A	F51 Voltage control/restraint enable Ph-A #1 Disable – 0, Voltage control – 1, Restraint – 2	0-2	–
705	10	30728	%	F51 Voltage control/restraint pickup Ph-A #1	40-1500	–
706	100	7582	A	F51 Pickup Ph-B #1	2-320	–
707	1	7583	N/A	F51 Curve Selection Ph-B #1	0-71	–
708	1000	7585	Sec	F51 Reset Co-eff Ph-B #1	1-30000	–
709	100	7587	N/A	F51 Time Multiplier Ph-B #1	5-100	–
710	100	7588	Sec	F51 Time Adder Ph-B #1	0-3000	–
711	100	7589	Sec	F51 Min response time adder Ph-B #1	0-100	–
712	1	30729	N/A	F51 Voltage control/restraint enable Ph-B #1 Disable – 0, Voltage control – 1, Restraint – 2	0-2	–
713	10	30730	%	F51 Voltage control/restraint pickup Ph-B #1	40-1500	–
714	100	7595	A	F51 Pickup Ph-C #1	2-320	–
715	1	7596	N/A	F51 Curve Selection Ph-C #1	0-71	–
716	1000	7598	Sec	F51 Reset Co-eff Ph-C #1	1-30000	–
717	100	7600	N/A	F51 Time Multiplier Ph-C #1	5-100	–
718	100	7601	Sec	F51 Time Adder Ph-C #1	0-3000	–
719	100	7602	Sec	F51 Min response time adder Ph-C #1	0-100	–
720	1	30731	N/A	F51 Voltage control/restraint enable Ph-C #1 Disable – 0, Voltage control – 1, Restraint – 2	0-2	–
721	10	30732	%	F51 Voltage control/restraint pickup Ph-C #1	40-1500	–
722	100	7608	A	F51 Pickup Ph-A #2	2-320	–
723	1	7609	N/A	F51 Curve Selection Ph-A #2	0-71	–
724	1000	7611	Sec	F51 Reset Co-eff Ph-A #2	1-30000	–
725	100	7613	N/A	F51 Time Multiplier Ph-A #2	5-100	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
726	100	7614	Sec	F51 Time Adder Ph-A #2	0-3000	—
727	100	7615	Sec	F51 Min response time adder Ph-A #2	0-100	—
728	1	30733	N/A	F51 Voltage control/restraint enable Ph-A #2 Disable – 0, Voltage control – 1, Restraint – 2	0-2	—
729	10	30734	%	F51 Voltage control/restraint pickup Ph-A #2	40-1500	—
730	100	7621	A	F51 Pickup Ph-B #2	2-320	—
731	1	7622	N/A	F51 Curve Selection Ph-B #2	0-71	—
732	1000	7624	Sec	F51 Reset Co-eff Ph-B #2	1-30000	—
733	100	7626	N/A	F51 Time Multiplier Ph-B #2	5-100	—
734	100	7627	Sec	F51 Time Adder Ph-B #2	0-3000	—
735	100	7628	Sec	F51 Min response time adder Ph-B #2	0-100	—
736	1	30735	N/A	F51 Voltage control/restraint enable Ph-B #2 Disable – 0, Voltage control – 1, Restraint – 2	0-2	—
737	10	30736	%	F51 Voltage control/restraint pickup Ph-B #2	40-1500	—
738	100	7634	A	F51 Pickup Ph-C #2	2-320	—
739	1	7635	N/A	F51 Curve Selection Ph-C #2	0-71	—
740	1000	7637	Sec	F51 Reset Co-eff Ph-C #2	1-30000	—
741	100	7639	N/A	F51 Time Multiplier Ph-C #2	5-100	—
742	100	7640	Sec	F51 Time Adder Ph-C #2	0-3000	—
743	100	7641	Sec	F51 Min response time adder Ph-C #2	0-100	—
744	1	30737	N/A	F51 Voltage control/restraint enable Ph-C #2 Disable – 0, Voltage control – 1, Restraint – 2	0-2	—
745	10	30738	%	F51 Voltage control/restraint pickup Ph-C #2	40-1500	—
746	100	7647	A	F51 Pickup Ph-A #3	2-320	—
747	1	7648	N/A	F51 Curve Selection Ph-A #3	0-71	—
748	1000	7650	Sec	F51 Reset Co-eff Ph-A #3	1-30000	—
749	100	7652	N/A	F51 Time Multiplier Ph-A #3	5-100	—
750	100	7653	Sec	F51 Time Adder Ph-A #3	0-3000	—
751	100	7654	Sec	F51 Min response time adder Ph-A #3	0-100	—
752	1	30739	N/A	F51 Voltage control/restraint enable Ph-A #3 Disable – 0, Voltage control – 1, Restraint – 2	0-2	—
753	10	30740	%	F51 Voltage control/restraint pickup Ph-A #3	40-1500	—
754	100	7660	A	F51 Pickup Ph-B #3	2-320	—
755	1	7661	N/A	F51 Curve Selection Ph-B #3	0-71	—
756	1000	7663	Sec	F51 Reset Co-eff Ph-B #3	1-30000	—
757	100	7665	N/A	F51 Time Multiplier Ph-B #3	5-100	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
758	100	7666	Sec	F51 Time Adder Ph-B #3	0-3000	—
759	100	7667	Sec	F51 Min response time adder Ph-B #3	0-100	—
760	1	30741	N/A	F51 Voltage control/restraint enable Ph-B #3 Disable – 0, Voltage control – 1, Restraint – 2	0-2	—
761	10	30742	%	F51 Voltage control/restraint pickup Ph-B #3	40-1500	—
762	100	7673	A	F51 Pickup Ph-C #3	2-320	—
763	1	7674	N/A	F51 Curve Selection Ph-C #3	0-71	—
764	1000	7676	Sec	F51 Reset Co-eff Ph-C #3	1-30000	—
765	100	7678	N/A	F51 Time Multiplier Ph-C #3	5-100	—
766	100	7679	Sec	F51 Time Adder Ph-C #3	0-3000	—
767	100	7680	Sec	F51 Min response time adder Ph-C #3	0-100	—
768	1	30743	N/A	F51 Voltage control/restraint enable Ph-C #3 Disable – 0, Voltage control – 1, Restraint – 2	0-2	—
769	10	30744	%	F51 Voltage control/restraint pickup Ph-C #3	40-1500	—
770	100	7686	A	F51 Pickup Ph-A #4	2-320	—
771	1	7687	N/A	F51 Curve Selection Ph-A #4	0-71	—
772	1000	7689	Sec	F51 Reset Co-eff Ph-A #4	1-30000	—
773	100	7691	N/A	F51 Time Multiplier Ph-A #4	5-100	—
774	100	7692	Sec	F51 Time Adder Ph-A #4	0-3000	—
775	100	7693	Sec	F51 Min response time adder Ph-A #4	0-100	—
776	1	30745	N/A	F51 Voltage control/restraint enable Ph-A #4 Disable – 0, Voltage control – 1, Restraint – 2	0-2	—
777	10	30746	%	F51 Voltage control/restraint pickup Ph-A #4	40-1500	—
778	100	7699	A	F51 Pickup Ph-B #4	2-320	—
779	1	7700	N/A	F51 Curve Selection Ph-B #4	0-71	—
780	1000	7702	Sec	F51 Reset Co-eff Ph-B #4	1-30000	—
781	100	7704	N/A	F51 Time Multiplier Ph-B #4	10-200	—
782	100	7705	Sec	F51 Time Adder Ph-B #4	0-3000	—
783	100	7706	Sec	F51 Min response time adder Ph-B #4	0-100	—
784	1	30747	N/A	F51 Voltage control/restraint enable Ph-B #4 Disable – 0, Voltage control – 1, Restraint – 2	0-2	—
785	10	30748	%	F51 Voltage control/restraint pickup Ph-B #4	40-1500	—
786	100	7712	A	F51 Pickup Ph-C #4	2-320	—
787	1	7713	N/A	F51 Curve Selection Ph-C #4	0-71	—
788	1000	7715	Sec	F51 Reset Co-eff Ph-C #4	1-30000	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
789	100	7717	N/A	F51 Time Multiplier Ph-C #4	5-100	—
790	100	7718	Sec	F51 Time Adder Ph-C #4	0-3000	—
791	100	7719	Sec	F51 Min response time adder Ph-C #4	0-100	—
792	1	30749	N/A	F51 Voltage control/restraint enable Ph-C #4 Disable – 0, Voltage control – 1, Restraint – 2	0-2	—
793	10	30750	%	F51 Voltage control/restraint pickup Ph-C #4	40-1500	—
794	100	7725	A	F51 Pickup Ph-A #5	2-320	—
795	1	7726	N/A	F51 Curve Selection Ph-A #5	0-71	—
796	1000	7728	Sec	F51 Reset Co-eff Ph-A #5	1-30000	—
797	100	7730	N/A	F51 Time Multiplier Ph-A #5	5-100	—
798	100	7731	Sec	F51 Time Adder Ph-A #5	0-3000	—
799	100	7732	Sec	F51 Min response time adder Ph-A #5	0-100	—
800	1	30751	N/A	F51 Voltage control/restraint enable Ph-A #5 Disable – 0, Voltage control – 1, Restraint – 2	0-2	—
801	10	30752	%	F51 Voltage control/restraint pickup Ph-A #5	40-1500	—
802	100	7738	A	F51 Pickup Ph-B #5	2-320	—
803	1	7739	N/A	F51 Curve Selection Ph-B #5	0-71	—
804	1000	7741	Sec	F51 Reset Co-eff Ph-B #5	1-30000	—
805	100	7743	N/A	F51 Time Multiplier Ph-B #5	5-100	—
806	100	7744	Sec	F51 Time Adder Ph-B #5	0-3000	—
807	100	7745	Sec	F51 Min response time adder Ph-B #5	0-100	—
808	1	30753	N/A	F51 Voltage control/restraint enable Ph-B #5 Disable – 0, Voltage control – 1, Restraint – 2	0-2	—
809	10	30754	%	F51 Voltage control/restraint pickup Ph-B #5	40-1500	—
810	100	7751	A	F51 Pickup Ph-C #5	2-320	—
811	1	7752	N/A	F51 Curve Selection Ph-C #5	0-71	—
812	1000	7754	Sec	F51 Reset Co-eff Ph-C #5	1-30000	—
813	100	7756	N/A	F51 Time Multiplier Ph-C #5	10-200	—
814	100	7757	Sec	F51 Time Adder Ph-C #5	0-3000	—
815	100	7758	Sec	F51 Min response time adder Ph-C #5	0-100	—
816	1	30755	N/A	F51 Voltage control/restraint enable Ph-C #5 Disable – 0, Voltage control – 1, Restraint – 2	0-2	—
817	10	30756	%	F51 Voltage control/restraint pickup Ph-C #5	40-1500	—
818	100	7775	A	F51N Pickup #1	2-320	—
819	1	7776	N/A	F51N Curve Selection #1	0-71	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
820	1000	7778	Sec	F51N Reset Co-eff #1	1-30000	—
821	100	7780	N/A	F51N Time Multiplier #1	5-100	—
822	100	7781	Sec	F51N Time Adder #1	0-3000	—
823	100	7782	Sec	F51N Min response time adder #1	0-100	—
824	100	7788	A	F51N Pickup #2	2-320	—
825	1	7789	N/A	F51N Curve Selection #2	0-71	—
826	1000	7791	Sec	F51N Reset Co-eff #2	1-30000	—
827	100	7793	N/A	F51N Time Multiplier #2	5-100	—
828	100	7794	Sec	F51N Time Adder #2	0-3000	—
829	100	7795	Sec	F51N Min response time adder #2	0-100	—
830	100	7801	A	F51N Pickup #3	2-320	—
831	1	7802	N/A	F51N Curve Selection #3	0-71	—
832	1000	7804	Sec	F51N Reset Co-eff #3	1-30000	—
833	100	7806	N/A	F51N Time Multiplier #3	5-100	—
834	100	7807	Sec	F51N Time Adder #3	0-3000	—
835	100	7808	Sec	F51N Min response time adder #3	0-100	—
836	100	7814	A	F51N Pickup #4	2-320	—
837	1	7815	N/A	F51N Curve Selection #4	0-71	—
838	1000	7817	Sec	F51N Reset Co-eff #4	1-30000	—
839	100	7819	N/A	F51N Time Multiplier #4	5-100	—
840	100	7820	Sec	F51N Time Adder #4	0-3000	—
841	100	7821	Sec	F51N Min response time adder #4	0-100	—
842	100	7827	A	F51N Pickup #5	2-320	—
843	1	7828	N/A	F51N Curve Selection #5	0-71	—
844	1000	7830	Sec	F51N Reset Co-eff #5	1-30000	—
845	100	7832	N/A	F51N Time Multiplier #5	5-100	—
846	100	7833	Sec	F51N Time Adder #5	0-3000	—
847	100	7834	Sec	F51N Min response time adder #5	0-100	—
848	1	47014	N/A	F51N Blocking Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
849	1	7773	N/A	F51N Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
850	1	7774	N/A	Enable/Disable F51N #1 Enable – 1, Disable – 0	0-1	–
851	1	7777	N/A	Enable Electromechanical Reset for F51N #1 Enable – 1, Disable – 0	0-1	–
852	1	47016	N/A	F51N Blocking Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
853	1	7786	N/A	F51N Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
854	1	7787	N/A	Enable/Disable F51N #2 Enable – 1, Disable – 0	0-1	–
855	1	7790	N/A	Enable Electromechanical Reset for F51N #2 Enable – 1, Disable – 0	0-1	–
856	1	47018	N/A	F51N Blocking Inputs #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
857	1	7799	N/A	F51N Outputs #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
858	1	7800	N/A	Enable/Disable F51N #3 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
859	1	7803	N/A	Enable Electromechanical Reset for F51N #3 Enable – 1, Disable – 0	0-1	–
860	1	47020	N/A	F51N Blocking Inputs #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
861	1	7812	N/A	F51N Outputs #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
862	1	7813	N/A	Enable/Disable F51N #4 Enable – 1, Disable – 0	0-1	–
863	1	7816	N/A	Enable Electromechanical Reset for F51N #4 Enable – 1, Disable – 0	0-1	–
864	1	47022	N/A	F51N Blocking Inputs #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
865	1	7825	N/A	F51N Outputs #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
866	1	7826	N/A	Enable/Disable F51N #5 Enable – 1, Disable – 0	0-1	–
867	1	7829	N/A	Enable Electromechanical Reset for F51N #5 Enable – 1, Disable – 0	0-1	–
868	100	7851	A	F51G Pickup #1	2-320	–
869	1	7852	N/A	F51G Curve Selection #1	0-71	–
870	1000	7854	Sec	F51G Reset Co-eff #1	1-30000	–
871	100	7856	N/A	F51G Time Multiplier #1	5-100	–
872	100	7857	Sec	F51G Time Adder #1	0-3000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
873	100	7858	Sec	F51G Min response time adder #1	0-100	—
874	100	7864	A	F51G Pickup #2	2-320	—
875	1	7865	N/A	F51G Curve Selection #2	0-71	—
876	1000	7867	Sec	F51G Reset Co-eff #2	1-30000	—
877	100	7869	N/A	F51G Time Multiplier #2	5-100	—
878	100	7870	Sec	F51G Time Adder #2	0-3000	—
879	100	7871	Sec	F51G Min response time adder #2	0-100	—
880	100	7877	A	F51G Pickup #3	2-320	—
881	1	7878	N/A	F51G Curve Selection #3	0-71	—
882	1000	7880	Sec	F51G Reset Co-eff #3	1-30000	—
883	100	7882	N/A	F51G Time Multiplier #3	5-100	—
884	100	7883	Sec	F51G Time Adder #3	0-3000	—
885	100	7884	Sec	F51G Min response time adder #3	0-100	—
886	100	7890	A	F51G Pickup #4	2-320	—
887	1	7891	N/A	F51G Curve Selection #4	0-71	—
888	1000	7893	Sec	F51G Reset Co-eff #4	1-30000	—
889	100	7895	N/A	F51G Time Multiplier #4	5-100	—
890	100	7896	Sec	F51G Time Adder #4	0-3000	—
891	100	7897	Sec	F51G Min response time adder #4	0-100	—
892	100	7903	A	F51G Pickup #5	2-320	—
893	1	7904	N/A	F51G Curve Selection #5	0-71	—
894	1000	7906	Sec	F51G Reset Co-eff #5	1-30000	—
895	100	7908	N/A	F51G Time Multiplier #5	5-100	—
896	100	7909	Sec	F51G Time Adder #5	0-3000	—
897	100	7910	Sec	F51G Min response time adder #5	0-100	—
898	1	47024	N/A	F51G Blocking Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
899	1	7849	N/A	F51G Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
900	1	7850	N/A	Enable/Disable F51G #1 Enable – 1, Disable – 0	0-1	–
901	1	7853	N/A	Enable Electromechanical Reset for F51G #1 Enable – 1, Disable – 0	0-1	–
902	1	47026	N/A	F51G Blocking Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
903	1	7862	N/A	F51G Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
904	1	7863	N/A	Enable/Disable F51G #2 Enable – 1, Disable – 0	0-1	–
905	1	7866	N/A	Enable Electromechanical Reset for F51G #2 Enable – 1, Disable – 0	0-1	–
906	1	47028	N/A	F51G Blocking Inputs #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
907	1	7875	N/A	F51G Outputs #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
908	1	7876	N/A	Enable/Disable F51G #3 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
909	1	7879	N/A	Enable Electromechanical Reset for F51G #3 Enable – 1, Disable – 0	0-1	–
910	1	47030	N/A	F51G Blocking Inputs #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
911	1	7888	N/A	F51G Outputs #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
912	1	7889	N/A	Enable/Disable F51G #4 Enable – 1, Disable – 0	0-1	–
913	1	7892	N/A	Enable Electromechanical Reset for F51G #4 Enable – 1, Disable – 0	0-1	–
914	1	47032	N/A	F51G Blocking Inputs #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
915	1	7901	N/A	F51G Outputs #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
916	1	7902	N/A	Enable/Disable F51G #5 Enable – 1, Disable – 0	0-1	–
917	1	7905	N/A	Enable Electromechanical Reset for F51G #5 Enable – 1, Disable – 0	0-1	–
918	100	7927	A	F46IT Pickup #1	2-320	–
919	1	7928	N/A	F46IT Curve Selection #1	0-71	–
920	1000	7930	Sec	F46IT Reset Co-eff #1	1-30000	–
921	100	7932	N/A	F46IT Time Multiplier #1	5-100	–
922	100	7933	Sec	F46IT Time Adder #1	0-3000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
923	100	7934	Sec	F46IT Min response time adder #1	0-100	—
924	100	7940	A	F46IT Pickup #2	2-320	—
925	1	7941	N/A	F46IT Curve Selection #2	0-71	—
926	1000	7943	Sec	F46IT Reset Co-eff #2	1-30000	—
927	100	7945	N/A	F46IT Time Multiplier #2	5-100	—
928	100	7946	Sec	F46IT Time Adder #2	0-3000	—
929	100	7947	Sec	F46IT Min response time adder #2	0-100	—
930	100	7953	A	F46IT Pickup #3	2-320	—
931	1	7954	N/A	F46IT Curve Selection #3	0-71	—
932	1000	7956	Sec	F46IT Reset Co-eff #3	1-30000	—
933	100	7958	N/A	F46IT Time Multiplier #3	5-100	—
934	100	7959	Sec	F46IT Time Adder #3	0-3000	—
935	100	7960	Sec	F46IT Min response time adder #3	0-100	—
936	100	7966	A	F46IT Pickup #4	2-320	—
937	1	7967	N/A	F46IT Curve Selection #4	0-71	—
938	1000	7969	Sec	F46IT Reset Co-eff #4	1-30000	—
939	100	7971	N/A	F46IT Time Multiplier #4	5-100	—
940	100	7972	Sec	F46IT Time Adder #4	0-3000	—
941	100	7973	Sec	F46IT Min response time adder #4	0-100	—
942	100	7979	A	F46IT Pickup #5	2-320	—
943	1	7980	N/A	F46IT Curve Selection #5	0-71	—
944	1000	7982	Sec	F46IT Reset Co-eff #5	1-30000	—
945	100	7984	N/A	F46IT Time Multiplier #5	5-100	—
946	100	7985	Sec	F46IT Time Adder #5	0-3000	—
947	100	7986	Sec	F46IT Min response time adder #5	0-100	—
948	1	47034	N/A	F46IT Blocking Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
949	1	7925	N/A	F46IT Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
950	1	7926	N/A	Enable/Disable F46IT #1 Enable – 1, Disable – 0	0-1	–
951	1	7929	N/A	Enable Electromechanical Reset for F46IT #1	0-1	–
952	1	47036	N/A	F46IT Blocking Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
953	1	7938	N/A	F46IT Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
954	1	7939	N/A	Enable/Disable F46IT #2 Enable – 1, Disable – 0	0-1	–
955	1	7942	N/A	Enable Electromechanical Reset for F46IT #2 Enable – 1, Disable – 0	0-1	–
956	1	47038	N/A	F46IT Blocking Inputs #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
957	1	7951	N/A	F46IT Outputs #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
958	1	7952	N/A	Enable/Disable F46IT #3 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
959	1	7955	N/A	Enable Electromechanical Reset for F46IT #3 Enable – 1, Disable – 0	0-1	–
960	1	47040	N/A	F46IT Blocking Inputs #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
961	1	7964	N/A	F46IT Outputs #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
962	1	7965	N/A	Enable/Disable F46IT #4 Enable – 1, Disable – 0	0-1	–
963	1	7968	N/A	Enable Electromechanical Reset for F46IT #4 Enable – 1, Disable – 0	0-1	–
964	1	47042	N/A	F46IT Blocking Inputs #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
965	1	7977	N/A	F46IT Outputs #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
966	1	7978	N/A	Enable/Disable F46IT #5 Enable – 1, Disable – 0	0-1	–
967	1	7981	N/A	Enable Electromechanical Reset for F46IT #5 Enable – 1, Disable – 0	0-1	–
968	100	8003	V,Sec	F59 Pickup 3-Ph #1	1000-30000	–
969	100	8004	Sec	F59 Definite Time #1	0-60000	–
970	100	8009	V,Sec	F59 Pickup 3-Ph #2	1000-30000	–
971	100	8010	Sec	F59 Definite Time #2	0-60000	–
972	100	8015	V,Sec	F59 Pickup 3-Ph #3	1000-30000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
973	100	8016	Sec	F59 Definite Time #3	0-60000	—
974	100	8021	V,Sec	F59 Pickup 3-Ph #4	1000-30000	—
975	100	8022	Sec	F59 Definite Time #4	0-60000	—
976	100	8027	V,Sec	F59 Pickup Ph-A #1	1000-30000	—
977	100	8028	Sec	F59 Definite Time Ph-A #1	0-60000	—
978	100	8032	V,Sec	F59 Pickup Ph-B #1	1000-30000	—
979	100	8033	Sec	F59 Definite Time Ph-B #1	0-60000	—
980	100	8037	V,Sec	F59 Pickup Ph-C #1	1000-30000	—
981	100	8038	Sec	F59 Definite Time Ph-C #1	0-60000	—
982	100	8042	V,Sec	F59 Pickup Ph-A #2	1000-30000	—
983	100	8043	Sec	F59 Definite Time Ph-A #2	0-60000	—
984	100	8047	V,Sec	F59 Pickup Ph-B #2	1000-30000	—
985	100	8048	Sec	F59 Definite Time Ph-B #2	0-60000	—
986	100	8052	V,Sec	F59 Pickup Ph-C #2	1000-30000	—
987	100	8053	Sec	F59 Definite Time Ph-C #2	0-60000	—
988	100	8057	V,Sec	F59 Pickup Ph-A #3	1000-30000	—
989	100	8058	Sec	F59 Definite Time Ph-A #3	0-60000	—
990	100	8062	V,Sec	F59 Pickup Ph-B #3	1000-30000	—
991	100	8063	Sec	F59 Definite Time Ph-B #3	0-60000	—
992	100	8067	V,Sec	F59 Pickup Ph-C #3	1000-30000	—
993	100	8068	Sec	F59 Definite Time Ph-C #3	0-60000	—
994	100	8072	V,Sec	F59 Pickup Ph-A #4	1000-30000	—
995	100	8073	Sec	F59 Definite Time Ph-A #4	0-60000	—
996	100	8077	V,Sec	F59 Pickup Ph-B #4	1000-30000	—
997	100	8078	Sec	F59 Definite Time Ph-B #4	0-60000	—
998	100	8082	V,Sec	F59 Pickup Ph-C #4	1000-30000	—
999	100	8083	Sec	F59 Definite Time Ph-C #4	0-60000	—
1000	100	46373	V,Sec	F59#5 3 phases Pickup	1000-30000	—
1001	100	46374	s	F59#5 3 phases Definite Time	0-60000	—
1002	100	46378	V,Sec	F59#6 3 phases Pickup	1000-30000	—
1003	100	46379	s	F59#6 3 phases Definite Time	0-60000	—
1004	100	46383	V,Sec	F59#7 3 phases Pickup	1000-30000	—
1005	100	46384	s	F59#7 3 phases Definite Time	0-60000	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1006	100	46388	V,Sec	F59#8 3 phases Pickup	1000-30000	—
1007	100	46389	s	F59#8 3 phases Definite Time	0-60000	—
1008	1	47044	N/A	F59 Blocking Inputs, 3-Ph #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	—
1009	1	8001	N/A	F59 Outputs, 3-Ph #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	—
1010	1	8002	N/A	Enable/Disable F59 3-Ph #1 Enable – 1, Disable – 0	0-1	—
1011	1	8084	N/A	F59 Operating voltage side #1 Vy-0, Vz-1	0-1	—
1012	1	47046	N/A	F59 Blocking Inputs, 3-Ph #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	—
1013	1	8007	N/A	F59 Outputs, 3-Ph #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	—
1014	1	8008	N/A	Enable/Disable F59 3-Ph #2 Enable – 1, Disable – 0	0-1	—
1015	1	8084	N/A	F59 Operating voltage side #2 Vy-0, Vz-1	0-1	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1016	1	47048	N/A	F59 Blocking Inputs, 3-Ph #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1017	1	8013	N/A	F59 Outputs, 3-Ph #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1018	1	8014	N/A	Enable/Disable F59 3-Ph #3 Enable – 1, Disable – 0	0-1	–
1019	1	8084	N/A	F59 Operating voltage side #3 Vy-0, Vz-1	0-1	–
1020	1	47050	N/A	F59 Blocking Inputs, 3-Ph #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1021	1	8019	N/A	F59 Outputs, 3-Ph #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1022	1	8020	N/A	Enable/Disable F59 3-Ph #4 Enable – 1, Disable – 0	0-1	–
1023	1	8084	N/A	F59 Operating voltage side #4 Vy-0, Vz-1	0-1	–
1024	1	47052	N/A	F59 Blocking Inputs, Ph-A #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1025	1	8025	N/A	F59 Outputs, Ph-A #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1026	1	8026	N/A	Enable/Disable F59 Ph-A #1 Enable – 1, Disable – 0	0-1	–
1027	1	47058	N/A	F59 Blocking Inputs, Ph-A #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1028	1	8040	N/A	F59 Outputs, Ph-A #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1029	1	8041	N/A	Enable/Disable F59 Ph-A #2 Enable – 1, Disable – 0	0-1	–
1030	1	47064	N/A	F59 Blocking Input 1, Ph-A #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1031	1	8055	N/A	F59 Output 12, Ph-A #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1032	1	8056	N/A	Enable/Disable F59 Ph-A #3 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1033	1	47070	N/A	F59 Blocking Inputs, Ph-A #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1034	1	8070	N/A	F59 Outputs, Ph-A #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1035	1	8071	N/A	Enable/Disable F59 Ph-A #4 Enable – 1, Disable – 0	0-1	–
1036	1	47054	N/A	F59 Blocking Inputs, Ph-B #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1037	1	8030	N/A	F59 Outputs, Ph-B #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1038	1	8031	N/A	Enable/Disable F59 Ph-B #1 Enable – 1, Disable – 0	0-1	–
1039	1	47060	N/A	F59 Blocking Inputs, Ph-B #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1040	1	8045	N/A	F59 Outputs, Ph-B #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1041	1	8046	N/A	Enable/Disable F59 Ph-B #2 Enable – 1, Disable – 0	0-1	–
1042	1	47066	N/A	F59 Blocking Inputs, Ph-B #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1043	1	8060	N/A	F59 Outputs, Ph-B #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1044	1	8061	N/A	Enable/Disable F59 Ph-B #3 Enable – 1, Disable – 0	0-1	–
1045	1	47072	N/A	F59 Blocking Inputs, Ph-B #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1046	1	8070	N/A	F59 Outputs, Ph-B #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1047	1	8071	N/A	Enable/Disable F59 Ph-B #4 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1048	1	47056	N/A	F59 Blocking Inputs, Ph-C #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1049	1	8035	N/A	F59 Outputs, Ph-C #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1050	1	8036	N/A	Enable/Disable F59 Ph-C #1 Enable – 1, Disable – 0	0-1	–
1051	1	47062	N/A	F59 Blocking Inputs, Ph-C #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1052	1	8050	N/A	F59 Outputs, Ph-C #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1053	1	8051	N/A	Enable/Disable F59 Ph-C #2 Enable – 1, Disable – 0	0-1	–
1054	1	47068	N/A	F59 Blocking Inputs, Ph-C #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1055	1	8065	N/A	F59 Outputs, Ph-C #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1056	1	8066	N/A	Enable/Disable F59 Ph-C #3 Enable – 1, Disable – 0	0-1	–
1057	1	47074	N/A	F59 Blocking Inputs, Ph-C #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1058	1	8080	N/A	F59 Outputs, Ph-C #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1059	1	8081	N/A	Enable/Disable F59 Ph-C #4 Enable – 1, Disable – 0	0-1	–
1060	1	8084	N/A	F59#5 3 phases Operating voltage side Vy-0, Vz-1	0-1	–
1061	1	8084	N/A	F59#6 3 phases Operating voltage side Vy-0, Vz-1	0-1	–
1062	1	8084	N/A	F59#7 3 phases Operating voltage side Vy-0, Vz-1	0-1	–
1063	1	8084	N/A	F59#8 3 phases Operating voltage side Vy-0, Vz-1	0-1	–
1064	1	47076	N/A	F59#5 3 phases Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1065	1	46371	N/A	F59#5 3 phases Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1066	1	46372	N/A	Enable/Disable F59#5 3 phases Enable – 1, Disable – 0	0-1	–
1067	1	47078	N/A	F59#6 3 phases Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
1068	1	46376	N/A	F59#6 3 phases Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1069	1	46377	N/A	Enable/Disable F59#6 3 phases Enable – 1, Disable – 0	0-1	–
1070	1	47080	N/A	F59#7 3 phases Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
1071	1	46381	N/A	F59#7 3 phases Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1072	1	46382	N/A	Enable/Disable F59#7 3 phases Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1073	1	47082	N/A	F59#8 3 phases Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
1074	1	46386	N/A	F59#8 3 phases Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1075	1	46387	N/A	Enable/Disable F59#8 3 phases Enable – 1, Disable – 0	0-1	–
1076	100	8745	V,Sec	F59PP Pickup 3-Ph	1000-30000	–
1077	100	8746	Sec	F59PP Definite time 3-Ph	0-60000	–
1078	100	8751	V,Sec	F59PP Pickup Ph-A	1000-30000	–
1079	100	8752	Sec	F59PP Definite time Ph-A	0-60000	–
1080	100	8756	V ,Sec	F59PP Pickup Ph-B	1000-30000	–
1081	100	8757	Sec	F59PP Definite time Ph-B	0-60000	–
1082	100	8761	V ,Sec	F59PP Pickup Ph-C	1000-30000	–
1083	100	8762	Sec	F59PP Definite time Ph-C	0-60000	–
1084	1	47218	N/A	F59PP Blocking Inputs 3-Ph Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1085	1	8743	N/A	F59PP Outputs 3-Ph Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1086	1	8744	N/A	Enable/Disable F59PP 3-Ph Enable – 1, Disable – 0	0-1	–
1087	1	8763	N/A	F59PP Operating voltage side Vy-0, Vz-1	--	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1088	1	47220	N/A	F59PP Blocking Inputs Ph-AB Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1089	1	8749	N/A	F59PP Outputs Ph-AB Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1090	1	8750	N/A	Enable/Disable F59PP Ph-AB Enable – 1, Disable – 0	0-1	–
1091	1	47222	N/A	F59PP Blocking Inputs Ph-BC Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1092	1	8754	N/A	F59PP Outputs Ph-BC Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1093	1	8755	N/A	Enable/Disable F59PP Ph-BC Enable – 1, Disable – 0	0-1	–
1094	1	47224	N/A	F59PP Blocking Inputs Ph-CA Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1095	1	8759	N/A	F59PP Outputs Ph-CA Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1096	1	8760	N/A	Enable/Disable F59PP Ph-CA Enable – 1, Disable – 0	0-1	–
1097	100	8129	V ,Sec	F59N Pickup #1	1000-30000	–
1098	100	8130	Sec	F59N Definite Time #1	0-60000	–
1099	1	47090	N/A	F59N Blocking Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1100	1	8127	N/A	F59N Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1101	1	8128	N/A	Enable/Disable F59N #1 Enable – 1, Disable – 0	0-1	–
1102	1	8131	N/A	F59N Operating voltage side #1 0 – Y Side 1 – Z Side	0-3	–
1103	100	8135	V ,Sec	F59N Pickup #2	1000-30000	–
1104	100	8136	Sec	F59N Definite Time #2	0-60000	–
1105	1	47092	N/A	F59N Blocking Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1106	1	8133	N/A	F59N Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1107	1	8134	N/A	Enable/Disable F59N #2 Enable – 1, Disable – 0	0-1	–
1108	1	8131	N/A	F59N Operating voltage side #2 0 – Y Side 1 – Z Side	0-3	–
1109	100	8097	V ,Sec	F59Vz1 Pickup	1000-30000	–
1110	100	8098	Sec	F59Vz1 Definite Time	0-60000	–
1111	1	47084	N/A	F59Vz1 Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1112	1	8095	N/A	F59Vz1 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1113	1	8096	N/A	Enable/Disable F59Vz1 Enable – 1, Disable – 0	0-1	–
1114	100	8113	V ,Sec	F47 Pickup	1000-30000	–
1115	100	8114	Sec	F47 Definite Time	0-60000	–
1116	1	47088	N/A	F47 Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1117	1	8111	N/A	F47 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1118	1	8112	N/A	Enable/Disable F47 Enable – 1, Disable – 0	0-1	–
1119	1	8115	N/A	F59Q Operating voltage side Enable – 1, Disable – 0	--	–
1120	1	8145	N/A	F67 Polar Vol, Direction, Action if below 3-Ph #1 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1121	100	30126	A	F67 Inverse Pickup Current 3-Ph #1	2-320	–
1122	100	8147	Sec	F67 Definite Time 3-Ph #1	0-60000	–
1123	1	8148	Degree	F67 Max Sensitivity angle 1 3-Ph #1	0-359	–
1124	100	8146	A	F67 Definite Pickup Current 3-Ph #1	5-2000	–
1125	–	9192	Degree	F67 Max Sensitivity angle 2 3-Ph #1	5-90	–
1126	1	9194	N/A	F67 curve selection 3-Ph #1	0-71	–
1127	1	9195	N/A	F67 Enable Electromechanical Reset 3-Ph #1 Enable – 1, Disable – 0	0-1	–
1128	1000	9196	Sec	F67 Reset Co-eff 3-Ph #1	1-30000	–
1129	100	9198	N/A	F67 Time Multiplier 3-Ph #1	5-100	–
1130	100	9199	Sec	F67 Time Adder 3-Ph #1	0-3000	–
1131	100	9200	Sec	F67 Min response time adder 3-Ph #1	0-100	–
1132	10	8149	%	F67 Min Polarizing Voltage 3-Ph #1	20-100	–
1133	1	8154	N/A	F67 Polar Vol, Direction, Action if below 3-Ph #2 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1134	100	30127	A	F67 Inverse Pickup Current 3-Ph #2	2-320	–
1135	100	8156	Sec	F67 Definite Time 3-Ph #2	0-60000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1136	1	8157	Degree	F67 Max Sensitivity angle 1 3-Ph #2	0-359	–
1137	100	8155	A	F67 Definite Pickup Current 3-Ph #2	5-2000	–
1138	–	9203	Degree	F67 Max Sensitivity angle 2 3-Ph #2	5-90	–
1139	1	9205	N/A	F67 curve selection 3-Ph #2	0-71	–
1140	1	9206	N/A	F67 Enable Electromechanical Reset 3-Ph #2 Enable – 1, Disable – 0	0-1	–
1141	1000	9207	Sec	F67 Reset Co-eff 3-Ph #2	1-30000	–
1142	100	9209	N/A	F67 Time Multiplier 3-Ph #2	5-100	–
1143	100	9210	Sec	F67 Time Adder 3-Ph #2	0-3000	–
1144	100	9211	Sec	F67 Min response time adder 3-Ph #2	0-100	–
1145	10	8158	%	F67 Min Polarizing Voltage 3-Ph #2	20-100	–
1146	1	8163	N/A	F67 Polar Vol, Direction, Action if below 3-Ph #3 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1147	100	30128	A	F67 Inverse Pickup Current 3-Ph #3	2-320	–
1148	100	8165	Sec	F67 Definite Time 3-Ph #3	0-60000	–
1149	1	8166	Degree	F67 Max Sensitivity angle 3-Ph #3	0-359	–
1150	100	8164	A	F67 Definite Pickup Current 3-Ph #3	5-2000	–
1151	–	9214	Degree	F67 Max Sensitivity angle 2 3-Ph #3	5-90	–
1152	1	9216	N/A	F67 curve selection 3-Ph #3	0-71	–
1153	1	9217	N/A	F67 Enable Electromechanical Reset 3-Ph #3 Enable – 1, Disable – 0	0-1	–
1154	1000	9218	Sec	F67 Reset Co-eff 3-Ph #3	1-30000	–
1155	100	9220	N/A	F67 Time Multiplier 3-Ph #3	5-100	–
1156	100	9221	Sec	F67 Time Adder 3-Ph #3	0-3000	–
1157	100	9222	Sec	F67 Min response time adder 3-Ph #3	0-100	–
1158	10	8167	%	F67 Min Polarizing Voltage 3-Ph #3	20-100	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1159	1	8172	N/A	F67 Polar Vol, Direction, Action if below 3-Ph #4 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1160	100	30129	A	F67 Inverse Pickup Current 3-Ph #4	2-320	–
1161	100	8174	Sec	F67 Definite Time 3-Ph #4	0-60000	–
1162	1	8175	Degree	F67 Max Sensitivity angle 3-Ph #4	0-359	–
1163	100	8173	A	F67 Definite Pickup Current 3-Ph #4	5-2000	–
1164	–	9225	Degree	F67 Max Sensitivity angle 2 3-Ph #4	5-90	–
1165	1	9227	N/A	F67 curve selection 3-Ph #4	0-71	–
1166	1	9228	N/A	F67 Enable Electromechanical Reset 3-Ph #4 Enable – 1, Disable – 0	0-1	–
1167	1000	9229	Sec	F67 Reset Co-eff 3-Ph #4	1-30000	–
1168	100	9231	N/A	F67 Time Multiplier 3-Ph #4	5-100	–
1169	100	9232	Sec	F67 Time Adder 3-Ph #4	0-3000	–
1170	100	9233	Sec	F67 Min response time adder 3-Ph #4	0-100	–
1171	10	8176	%	F67 Min Polarizing Voltage 3-Ph #4	20-100	–
1172	1	8181	N/A	F67 Polar Vol, Direction, Action if below 3-Ph #5 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1173	100	30130	A	F67 Inverse Pickup Current 3-Ph #5	2-320	–
1174	100	8183	Sec	F67 Definite Time 3-Ph #5	0-60000	–
1175	1	8184	Degree	F67 Max Sensitivity angle 3-Ph #5	0-359	–
1176	100	8182	A	F67 Definite Pickup Current 3-Ph #5	5-2000	–
1177	–	9236	Degree	F67 Max Sensitivity angle 2 3-Ph #5	5-90	–
1178	1	9238	N/A	F67 curve selection 3-Ph #5	0-71	–
1179	1	9239	N/A	F67 Enable Electromechanical Reset 3-Ph #5 Enable – 1, Disable – 0	0-1	–
1180	1000	9240	Sec	F67 Reset Co-eff 3-Ph #5	1-30000	–
1181	100	9242	N/A	F67 Time Multiplier 3-Ph #5	5-100	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1182	100	9243	Sec	F67 Time Adder 3-Ph #5	0-3000	–
1183	100	9244	Sec	F67 Min response time adder 3-Ph #5	0-100	–
1184	10	8185	%	F67 Min Polarizing Voltage 3-Ph #5	20-100	–
1185	1	8190	N/A	F67 Polar Vol, Direction, Action if below Ph-A #1 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1186	100	8191	A	F67 Definite Pickup Current Ph-A #1	2-320	–
1187	100	8192	Sec	F67 Definite Time Ph-A #1	0-60000	–
1188	1	8193	Degree	F67 Max Sensitivity angle Ph-A #1	0-359	–
1189	100	30131	A	F67 Inverse Pickup Current Ph-A #1	2-320	–
1190	–	9247	Degree	F67 Max Sensitivity angle 2 Ph-A #1	5-90	–
1191	1	9249	N/A	F67 curve selection Ph-A #1	0-71	–
1192	1	9250	N/A	F67 Enable Electromechanical Reset Ph-A #1 Enable – 1, Disable – 0	0-1	–
1193	1000	9251	Sec	F67 Reset Co-eff Ph-A #1	1-30000	–
1194	100	9253	N/A	F67 Time Multiplier Ph-A #1	5-100	–
1195	100	9254	Sec	F67 Time Adder Ph-A #1	0-3000	–
1196	100	9255	Sec	F67 Min response time adder Ph-A #1	0-100	–
1197	10	8194	%	F67 Min Polarizing Voltage Ph-A #1	20-100	–
1198	1	8198	N/A	F67 Polar Vol, Direction, Action if below Ph-A #2 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1199	100	8199	A	F67 Definite Pickup Current Ph-A #2	2-320	–
1200	100	8200	Sec	F67 Definite Time Ph-A #2	0-60000	–
1201	1	8201	Degree	F67 Max Sensitivity angle Ph-A #2	0-359	–
1202	100	30132	A	F67 Inverse Pickup Current Ph-A #2	2-320	–
1203	–	9258	Degree	F67 Max Sensitivity angle 2 Ph-A #2	5-90	–
1204	1	9260	N/A	F67 curve selection Ph-A #2	0-71	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1205	1	9261	N/A	F67 Enable Electromechanical Reset Ph-A #2 Enable – 1, Disable – 0	0-1	–
1206	1000	9262	Sec	F67 Reset Co-eff Ph-A #2	1-30000	–
1207	100	9264	N/A	F67 Time Multiplier Ph-A #2	5-100	–
1208	100	9265	Sec	F67 Time Adder Ph-A #2	0-3000	–
1209	100	9266	Sec	F67 Min response time adder Ph-A #2	0-100	–
1210	10	8202	%	F67 Min Polarizing Voltage Ph-A #2	20-100	–
1211	1	8206	N/A	F67 Polar Vol, Direction, Action if below Ph-A #3 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1212	100	8207	A	F67 Definite Pickup Current Ph-A #3	2-320	–
1213	100	8208	Sec	F67 Definite Time Ph-A #3	0-60000	–
1214	1	8209	Degree	F67 Max Sensitivity angle Ph-A #3	0-359	–
1215	100	30133	A	F67 Inverse Pickup Current Ph-A #3	2-320	–
1216	–	9269	Degree	F67 Max Sensitivity angle 2 Ph-A #3	5-90	–
1217	1	9271	N/A	F67 curve selection Ph-A #3	0-71	–
1218	1	9272	N/A	F67 Enable Electromechanical Reset Ph-A #3 Enable – 1, Disable – 0	0-1	–
1219	1000	9273	Sec	F67 Reset Co-eff Ph-A #3	1-30000	–
1220	100	9275	N/A	F67 Time Multiplier Ph-A #3	5-100	–
1221	100	9276	Sec	F67 Time Adder Ph-A #3	0-3000	–
1222	100	9277	Sec	F67 Min response time adder Ph-A #3	0-100	–
1223	10	8210	%	F67 Min Polarizing Voltage Ph-A #3	20-100	–
1224	1	8214	N/A	F67 Polar Vol, Direction, Action if below Ph-A #4 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1225	100	8215	A	F67 Definite Pickup Current Ph-A #4	2-320	–
1226	100	8216	Sec	F67 Definite Time Ph-A #4	0-60000	–
1227	1	8217	Degree	F67 Max Sensitivity angle Ph-A #4	0-359	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1228	100	30134	A	F67 Inverse Pickup Current Ph-A #4	2-320	–
1229	–	9280	Degree	F67 Max Sensitivity angle 2 Ph-A #4	5-90	–
1230	1	9282	N/A	F67 curve selection Ph-A #4	0-71	–
1231	1	9283	N/A	F67 Enable Electromechanical Reset Ph-A #4 Enable – 1, Disable – 0	0-1	–
1232	1000	9284	Sec	F67 Reset Co-eff Ph-A #4	1-30000	–
1233	100	9286	N/A	F67 Time Multiplier Ph-A #4	5-100	–
1234	100	9287	Sec	F67 Time Adder Ph-A #4	0-3000	–
1235	100	9288	Sec	F67 Min response time adder Ph-A #4	0-100	–
1236	10	8218	%	F67 Min Polarizing Voltage Ph-A #4	20-100	–
1237	1	8222	N/A	F67 Polar Vol, Direction, Action if below Ph-A #5 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1238	100	8223	A	F67 Definite Pickup Current Ph-A #5	2-320	–
1239	100	8224	Sec	F67 Definite Time Ph-A #5	0-60000	–
1240	1	8225	Degree	F67 Max Sensitivity angle Ph-A #5	0-359	–
1241	100	30135	A	F67 Inverse Pickup Current Ph-A #5	2-320	–
1242	–	30000	Degree	F67 Max Sensitivity angle 2 Ph-A #4	5-90	–
1243	1	30002	N/A	F67 curve selection Ph-A #5	0-71	–
1244	1	30003	N/A	F67 Enable Electromechanical Reset Ph-A #5 Enable – 1, Disable – 0	0-1	–
1245	1000	30004	Sec	F67 Reset Co-eff Ph-A #5	1-30000	–
1246	100	30006	N/A	F67 Time Multiplier Ph-A #5	5-100	–
1247	100	30007	Sec	F67 Time Adder Ph-A #5	0-3000	–
1248	100	30008	Sec	F67 Min response time adder Ph-A #5	0-100	–
1249	10	8226	%	F67 Min Polarizing Voltage Ph-A #5	20-100	–
1250	1	8230	N/A	F67 Polar Vol, Direction, Action if below Ph-B #1 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1251	100	8231	A	F67 Definite Pickup Current Ph-B #1	2-320	–
1252	100	8232	Sec	F67 Definite Time Ph-B #1	0-60000	–
1253	1	8233	Degree	F67 Max Sensitivity angle Ph-B #1	0-359	–
1254	100	30136	A	F67 Inverse Pickup Current Ph-B #1	2-320	–
1255	–	30011	Degree	F67 Max Sensitivity angle 2 Ph-B #1	5-90	–
1256	1	30013	N/A	F67 curve selection Ph-B #1	0-71	–
1257	1	30014	N/A	F67 Enable Electromechanical Reset Ph-B #1 Enable – 1, Disable – 0	0-1	–
1258	1000	30015	Sec	F67 Reset Co-eff Ph-B #1	1-30000	–
1259	100	30017	N/A	F67 Time Multiplier Ph-B #1	5-100	–
1260	100	30018	Sec	F67 Time Adder Ph-B #1	0-3000	–
1261	100	30019	Sec	F67 Min response time adder Ph-B #1	0-100	–
1262	10	8234	%	F67 Min Polarizing Voltage Ph-B #1	20-100	–
1263	1	8238	N/A	F67 Polar Vol, Direction, Action if below Ph-B #2 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1264	100	8239	A	F67 Definite Pickup Current Ph-B #2	2-320	–
1265	100	8240	Sec	F67 Definite Time Ph-B #2	0-60000	–
1266	1	8241	Degree	F67 Max Sensitivity angle Ph-B #2	0-359	–
1267	100	30137	A	F67 Inverse Pickup Current Ph-B #2	2-320	–
1268	–	30022	Degree	F67 Max Sensitivity angle 2 Ph-B #2	5-90	–
1269	1	30024	N/A	F67 curve selection Ph-B #2	0-71	–
1270	1	30025	N/A	F67 Enable Electromechanical Reset Ph-B #2 Enable – 1, Disable – 0	0-1	–
1271	1000	30026	Sec	F67 Reset Co-eff Ph-B #2	1-30000	–
1272	100	30028	N/A	F67 Time Multiplier Ph-B #2	5-100	–
1273	100	30029	Sec	F67 Time Adder Ph-B #2	0-3000	–
1274	100	30030	Sec	F67 Min response time adder Ph-B #2	0-100	–
1275	10	8242	%	F67 Min Polarizing Voltage Ph-B #2	20-100	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1276	1	8246	N/A	F67 Polar Vol, Direction, Action if below Ph-B #3 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1277	100	8247	A	F67 Definite Pickup Current Ph-B #3	2-320	–
1278	100	8248	Sec	F67 Definite Time Ph-B #3	0-60000	–
1279	1	8249	Degree	F67 Max Sensitivity angle Ph-B #3	0-359	–
1280	100	30138	A	F67 Inverse Pickup Current Ph-B #3	2-320	–
1281	–	30033	Degree	F67 Max Sensitivity angle 2 Ph-B #3	5-90	–
1282	1	30035	N/A	F67 curve selection Ph-B #3	0-71	–
1283	1	30036	N/A	F67 Enable Electromechanical Reset Ph-B #3 Enable – 1, Disable – 0	0-1	–
1284	1000	30037	Sec	F67 Reset Co-eff Ph-B #3	1-30000	–
1285	100	30039	N/A	F67 Time Multiplier Ph-B #3	5-100	–
1286	100	30040	Sec	F67 Time Adder Ph-B #3	0-3000	–
1287	100	30041	Sec	F67 Min response time adder Ph-B #3	0-100	–
1288	10	8250	%	F67 Min Polarizing Voltage Ph-B #3	20-100	–
1289	1	8254	N/A	F67 Polar Vol, Direction, Action if below Ph-B #4 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1290	100	8255	A	F67 Definite Pickup Current Ph-B #4	2-320	–
1291	100	8256	Sec	F67 Definite Time Ph-B #4	0-60000	–
1292	1	8257	Degree	F67 Max Sensitivity angle Ph-B #4	0-359	–
1293	100	30139	A	F67 Inverse Pickup Current Ph-B #4	2-320	–
1294	–	30044	Degree	F67 Max Sensitivity angle 2 Ph-B #4	5-90	–
1295	1	30046	N/A	F67 curve selection Ph-B #4	0-71	–
1296	1	30047	N/A	F67 Enable Electromechanical Reset Ph-B #4 Enable – 1, Disable – 0	0-1	–
1297	1000	30048	Sec	F67 Reset Co-eff Ph-B #4	1-30000	–
1298	100	30050	N/A	F67 Time Multiplier Ph-B #4	5-100	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1299	100	30051	Sec	F67 Time Adder Ph-B #4	0-3000	—
1300	100	30052	Sec	F67 Min response time adder Ph-B #4	0-100	—
1301	10	8258	%	F67 Min Polarizing Voltage Ph-B #4	20-100	—
1302	1	8262	N/A	F67 Polar Vol, Direction, Action if below Ph-B #5 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	—
1303	100	8263	A	F67 Definite Pickup Current Ph-B #5	2-320	—
1304	100	8264	Sec	F67 Definite Time Ph-B #5	0-60000	—
1305	1	8265	Degree	F67 Max Sensitivity angle Ph-B #5	0-359	—
1306	100	30140	A	F67 Inverse Pickup Current Ph-B #5	2-320	—
1307	—	30055	Degree	F67 Max Sensitivity angle 2 Ph-B #5	5-90	—
1308	1	30057	N/A	F67 curve selection Ph-B #5	0-71	—
1309	1	30058	N/A	F67 Enable Electromechanical Reset Ph-B #5 Enable – 1, Disable – 0	0-1	—
1310	1000	30059	Sec	F67 Reset Co-eff Ph-B #5	1-30000	—
1311	100	30061	N/A	F67 Time Multiplier Ph-B #5	5-100	—
1312	100	30062	Sec	F67 Time Adder Ph-B #5	0-3000	—
1313	100	30063	Sec	F67 Min response time adder Ph-B #5	0-100	—
1314	10	8266	%	F67 Min Polarizing Voltage Ph-B #5	20-100	—
1315	1	8270	N/A	F67 Polar Vol, Direction, Action if below Ph-C #1 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	—
1316	100	8271	A	F67 Definite Pickup Current Ph-C #1	2-320	—
1317	100	8272	Sec	F67 Definite Time Ph-C #1	0-60000	—
1318	1	8273	Degree	F67 Max Sensitivity angle Ph-C #1	0-359	—
1319	100	30141	A	F67 Inverse Pickup Current Ph-C #1	2-320	—
1320	—	30066	Degree	F67 Max Sensitivity angle 2 Ph-C #1	5-90	—
1321	1	30068	N/A	F67 curve selection Ph-C #1	0-71	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1322	1	30069	N/A	F67 Enable Electromechanical Reset Ph-C #1 Enable – 1, Disable – 0	0-1	–
1323	1000	30070	Sec	F67 Reset Co-eff Ph-C #1	1-30000	–
1324	100	30072	N/A	F67 Time Multiplier Ph-C #1	5-100	–
1325	100	30073	Sec	F67 Time Adder Ph-C #1	0-3000	–
1326	100	30074	Sec	F67 Min response time adder Ph-C #1	0-100	–
1327	10	8274	%	F67 Min Polarizing Voltage Ph-C #1	20-100	–
1328	1	8278	N/A	F67 Polar Vol, Direction, Action if below Ph-C #2 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unusedv	0-65535	–
1329	100	8279	A	F67 Definite Pickup Current Ph-C #2	2-320	–
1330	100	8280	Sec	F67 Definite Time Ph-C #2	0-60000	–
1331	1	8281	Degree	F67 Max Sensitivity angle Ph-C #2	0-359	–
1332	100	30142	A	F67 Inverse Pickup Current Ph-C #2	2-320	–
1333	–	30077	Degree	F67 Max Sensitivity angle 2 Ph-C #2	5-90	–
1334	1	30079	N/A	F67 curve selection Ph-C #2	0-71	–
1335	1	30080	N/A	F67 Enable Electromechanical Reset Ph-C #2 Enable – 1, Disable – 0	0-1	–
1336	1000	30081	Sec	F67 Reset Co-eff Ph-C #2	1-30000	–
1337	100	30083	N/A	F67 Time Multiplier Ph-C #2	5-100	–
1338	100	30084	Sec	F67 Time Adder Ph-C #2	0-3000	–
1339	100	30085	Sec	F67 Min response time adder Ph-C #2	0-100	–
1340	10	8282	%	F67 Min Polarizing Voltage Ph-C #2	20-100	–
1341	1	8286	N/A	F67 Polar Vol, Direction, Action if below Ph-C #3 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1342	100	8287	A	F67 Definite Pickup Current Ph-C #3	2-320	–
1343	100	8288	Sec	F67 Definite Time Ph-C #3	0-60000	–
1344	1	8289	Degree	F67 Max Sensitivity angle Ph-C #3	0-359	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1345	100	30143	A	F67 Inverse Pickup Current Ph-C #3	2-320	–
1346	–	30088	Degree	F67 Max Sensitivity angle 2 Ph-C #3	5-90	–
1347	1	30090	N/A	F67 curve selection Ph-C #3	0-71	–
1348	1	30091	N/A	F67 Enable Electromechanical Reset Ph-C #3 Enable – 1, Disable – 0	0-1	–
1349	1000	30092	Sec	F67 Reset Co-eff Ph-C #3	1-30000	–
1350	100	30094	N/A	F67 Time Multiplier Ph-C #3	5-100	–
1351	100	30095	Sec	F67 Time Adder Ph-C #3	0-3000	–
1352	100	30096	Sec	F67 Min response time adder Ph-C #3	0-100	–
1353	10	8290	%	F67 Min Polarizing Voltage Ph-C #3	20-100	–
1354	1	8294	N/A	F67 Polar Vol, Direction, Action if below Ph-C #4 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1355	100	8295	A	F67 Definite Pickup Current Ph-C #4	2-320	–
1356	100	8296	Sec	F67 Definite Time Ph-C #4	0-60000	–
1357	1	8297	Degree	F67 Max Sensitivity angle Ph-C #4	0-359	–
1358	100	30144	A	F67 Inverse Pickup Current Ph-C #4	2-320	–
1359	–	30099	Degree	F67 Max Sensitivity angle 2 Ph-C #4	5-90	–
1360	1	30101	N/A	F67 curve selection Ph-C #4	0-71	–
1361	1	30102	N/A	F67 Enable Electromechanical Reset Ph-C #4 Enable – 1, Disable – 0	0-1	–
1362	1000	30103	Sec	F67 Reset Co-eff Ph-C #4	1-30000	–
1363	100	30105	N/A	F67 Time Multiplier Ph-C #4	5-100	–
1364	100	30106	Sec	F67 Time Adder Ph-C #4	0-3000	–
1365	100	30107	Sec	F67 Min response time adder Ph-C #4	0-100	–
1366	10	8298	%	F67 Min Polarizing Voltage Ph-C #4	20-100	–
1367	1	8302	N/A	F67 Polar Vol, Direction, Action if below Ph-C #5 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Angle Band+ Bit 10 – Polarization Voltage is V1 Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1368	100	8303	A	F67 Definite Pickup Current Ph-C #5	2-320	—
1369	100	8304	Sec	F67 Definite Time Ph-C #5	0-60000	—
1370	1	8305	Degree	F67 Max Sensitivity angle Ph-C #5	0-359	—
1371	100	30145	A	F67 Inverse Pickup Current Ph-C #5	2-320	—
1372	—	30110	Degree	F67 Max Sensitivity angle 2 Ph-C #5	5-90	—
1373	1	30112	N/A	F67 curve selection Ph-C #5	0-71	—
1374	1	30113	N/A	F67 Enable Electromechanical Reset Ph-C #5 Enable – 1, Disable – 0	0-1	—
1375	1000	30114	Sec	F67 Reset Co-eff Ph-C #5	1-30000	—
1376	100	30116	N/A	F67 Time Multiplier Ph-C #5	5-100	—
1377	100	30117	Sec	F67 Time Adder Ph-C #5	0-3000	—
1378	100	30118	Sec	F67 Min response time adder Ph-C #5	0-100	—
1379	10	8306	%	F67 Min Polarizing Voltage Ph-C #5	20-100	—
1380	1	47094	N/A	F67 Blocking Inputs, 3-Ph #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	—
1381	1	8143	N/A	F67 Outputs, 3-Ph #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	—
1382	1	8144	N/A	Enable/Disable F67P 3-Ph #1 Enable – 1, Disable – 0	0-1	—
1383	1	8307	N/A	F67 Operating voltage side #1 Vy-0, Vz-1	0-31	—
1384	1	47096	N/A	F67 Blocking Inputs, 3-Ph #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1385	1	8152	N/A	F67 Outputs, 3-Ph #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1386	1	8153	N/A	Enable/Disable F67P 3-Ph #2 Enable – 1, Disable – 0	0-1	–
1387	1	8307	N/A	F67 Operating voltage side #2 Vy-0, Vz-1	0-31	–
1388	1	47098	N/A	F67 Blocking Inputs, 3-Ph #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1389	1	8161	N/A	F67 Outputs, 3-Ph #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1390	1	8162	N/A	Enable/Disable F67P 3-Ph #3 Enable – 1, Disable – 0	0-1	–
1391	1	8307	N/A	F67 Operating voltage side #3 Vy-0, Vz-1	0-31	–
1392	1	47100	N/A	F67 Blocking Inputs, 3-Ph #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1393	1	8170	N/A	F67 Outputs, 3-Ph #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1394	1	8171	N/A	Enable/Disable F67P 3-Ph #4 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1395	1	8307	N/A	F67 Operating voltage side #4 Vy-0, Vz-1	0-31	—
1396	1	47102	N/A	F67 Blocking Inputs, 3-Ph #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	—
1397	1	8179	N/A	F67 Outputs, 3-Ph #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	—
1398	1	8180	N/A	Enable/Disable F67P 3-Ph #5 Enable – 1, Disable – 0	0-1	—
1399	1	8307	N/A	F67 Operating voltage side #5 Vy-0, Vz-1	-	—
1400	1	47104	N/A	F67 Blocking Inputs, Ph-A #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	—
1401	1	8188	N/A	F67 Outputs, Ph-A #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	—
1402	1	8189	N/A	Enable/Disable F67P Ph-A #1 Enable – 1, Disable – 0	0-1	—
1403	1	47106	N/A	F67 Blocking Inputs, Ph-A #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1404	1	8196	N/A	F67 Outputs, Ph-A #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1405	1	8197	N/A	Enable/Disable F67P Ph-A #2 Enable – 1, Disable – 0	0-1	–
1406	1	47108	N/A	F67 Blocking Inputs, Ph-A #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1407	1	8204	N/A	F67 Outputs, Ph-A #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1408	1	8205	N/A	Enable/Disable F67P Ph-A #3 Enable – 1, Disable – 0	0-1	–
1409	1	47110	N/A	F67 Blocking Inputs, Ph-A #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1410	1	8212	N/A	F67 Outputs, Ph-A #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1411	1	8213	N/A	Enable/Disable F67P Ph-A #4 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1412	1	47112	N/A	F67 Blocking Inputs, Ph-A #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1413	1	8220	N/A	F67 Outputs, Ph-A #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1414	1	8221	N/A	Enable/Disable F67P Ph-A #5 Enable – 1, Disable – 0	0-1	–
1415	1	47114	N/A	F67 Blocking Inputs, Ph-B #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1416	1	8228	N/A	F67 Outputs, Ph-B #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1417	1	8229	N/A	Enable/Disable F67P Ph-B #1 Enable – 1, Disable – 0	0-1	–
1418	1	47116	N/A	F67 Blocking Inputs, Ph-B #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1419	1	8236	N/A	F67 Outputs, Ph-B #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1420	1	8237	N/A	Enable/Disable F67P Ph-B #2 Enable – 1, Disable – 0	0-1	–
1421	1	47118	N/A	F67 Blocking Inputs, Ph-B #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1422	1	8244	N/A	F67 Outputs, Ph-B #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1423	1	8245	N/A	Enable/Disable F67P Ph-B #3 Enable – 1, Disable – 0	0-1	–
1424	1	47120	N/A	F67 Blocking Inputs, Ph-B #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1425	1	8252	N/A	F67 Outputs, Ph-B #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1426	1	8253	N/A	Enable/Disable F67P Ph-B #4 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1427	1	47122	N/A	F67 Blocking Inputs, Ph-B #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1428	1	8260	N/A	F67 Outputs, Ph-B #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1429	1	8261	N/A	Enable/Disable F67P Ph-B #5 Enable – 1, Disable – 0	0-1	–
1430	1	47124	N/A	F67 Blocking Inputs, Ph-C #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1431	1	8268	N/A	F67 Outputs, Ph-C #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1432	1	8269	N/A	Enable/Disable F67P Ph-C #1 Enable – 1, Disable – 0	0-1	–
1433	1	47126	N/A	F67 Blocking Inputs, Ph-C #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1434	1	8276	N/A	F67 Outputs, Ph-C #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1435	1	8277	N/A	Enable/Disable F67P Ph-C #2 Enable – 1, Disable – 0	0-1	–
1436	1	47128	N/A	F67 Blocking Inputs, Ph-C #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1437	1	8284	N/A	F67 Outputs, Ph-C #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1438	1	8285	N/A	Enable/Disable F67P Ph-C #3 Enable – 1, Disable – 0	0-1	–
1439	1	47130	N/A	F67 Blocking Inputs, Ph-C #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1440	1	8292	N/A	F67 Outputs, Ph-C #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1441	1	8293	N/A	Enable/Disable F67P Ph-C #4 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1442	1	47132	N/A	F67 Blocking Inputs, Ph-C #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1443	1	8300	N/A	F67 Outputs, Ph-C #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1444	1	8301	N/A	Enable/Disable F67P Ph-C #5 Enable – 1, Disable – 0	0-1	–
1445	1	47134	N/A	F67N Blocking Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1446	1	8319	N/A	F67N Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1447	1	8320	N/A	Enable/Disable F67N #1 Enable – 1, Disable – 0	0-1	–
1448	1	47136	N/A	F67N Blocking Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1449	1	8327	N/A	F67N Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1450	1	8328	N/A	Enable/Disable F67N #2 Enable – 1, Disable – 0	0-1	–
1451	1	47138	N/A	F67N Blocking Inputs #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1452	1	8335	N/A	F67N Outputs #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1453	1	8336	N/A	Enable/Disable F67N #3 Enable – 1, Disable – 0	0-1	–
1454	1	47140	N/A	F67N Blocking Inputs #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1455	1	8343	N/A	F67N Outputs #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1456	1	8344	N/A	Enable/Disable F67N #4 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1457	1	47142	N/A	F67N Blocking Inputs #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1458	1	8351	N/A	F67N Outputs #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1459	1	8352	N/A	Enable/Disable F67N #5 Enable – 1, Disable – 0	0-1	–
1460	1	8321	N/A	F67N Polarizing Voltage #1 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 9 – Polarization Voltage is Vz1 (set both bits to 1) Bit 10 – Polarization Voltage is V1 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 9, 10 – Polarization Voltage is V0 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1461	100	8322	A	F67N Definite Pickup Current #1	5-2000	–
1462	100	8323	Sec	F67N Definite Time #1	0-60000	–
1463	100	8324	Degree	F67N Max Sensitivity angle #1	0-359	–
1464	100	30245	A	F67N Inverse Pickup Current #1	2-320	–
1465	1	30246	Degree	F67N Max Sensitivity angle 2 #1	5-90	–
1466	1	30248	N/A	F67N curve selection #1	0-71	–
1467	1	30249	N/A	F67N Enable Electromechanical Reset #1 Enable – 1, Disable – 0	0-1	–
1468	1000	30250	Sec	F67N Reset Co-eff #1	1-30000	–
1469	100	30252	N/A	F67N Time Multiplier #1	5-100	–
1470	100	30253	Sec	F67N Time Adder #1	0-3000	–
1471	100	30254	Sec	F67N Min response time adder #1	0-100	–
1472	10	8325	%	F67N Min Polarizing Voltage #1	20-100	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1473	1	8358	N/A	F67N Operating voltage side #1 Vy-0, Vz-1	0-31	—
1474	1	8329	N/A	F67N Polarizing Voltage #2 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 9 – Polarization Voltage is Vz1 (set both bits to 1) Bit 10 – Polarization Voltage is V1 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 9, 10 – Polarization Voltage is V0 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	—
1475	100	8330	A	F67N Definite Pickup Current #2	5-2000	—
1476	100	8331	Sec	F67N Definite Time #2	0-60000	—
1477	100	8332	Degree	F67N Max Sensitivity angle #2	0-359	—
1478	100	30257	A	F67N Inverse Pickup Current #2	2-320	—
1479	1	30258	Degree	F67N Max Sensitivity angle 2 #2	5-90	—
1480	1	30260	N/A	F67N curve selection #2	0-71	—
1481	1	30261	N/A	F67N Enable Electromechanical Reset #2 Enable – 1, Disable – 0	0-1	—
1482	1000	30262	Sec	F67N Reset Co-eff #2	1-30000	—
1483	100	30264	N/A	F67N Time Multiplier #2	5-100	—
1484	100	30265	Sec	F67N Time Adder #2	0-3000	—
1485	100	30266	Sec	F67N Min response time adder #2	0-100	—
1486	10	8333	%	F67N Min Polarizing Voltage #2	20-100	—
1487	1	8358	N/A	F67N Operating voltage side #2 Vy-0, Vz-1	0-31	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1488	1	8337	N/A	F67N Polarizing Voltage #3 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 9 – Polarization Voltage is Vz1 (set both bits to 1) Bit 10 – Polarization Voltage is V1 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 9, 10 – Polarization Voltage is V0 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1489	100	8338	A	F67N Definite Pickup Current #3	5-2000	–
1490	100	8339	Sec	F67N Definite Time #3	0-60000	–
1491	100	8340	Degree	F67N Max Sensitivity angle #3	0-359	–
1492	100	30269	A	F67N Inverse Pickup Current #3	2-320	–
1493	1	30270	Degree	F67N Max Sensitivity angle 2 #3	5-90	–
1494	1	30272	N/A	F67N curve selection #3	0-71	–
1495	1	30273	N/A	F67N Enable Electromechanical Reset #3 Enable – 1, Disable – 0	0-1	–
1496	1000	30274	Sec	F67N Reset Co-eff #3	1-30000	–
1497	100	30276	N/A	F67N Time Multiplier #3	5-100	–
1498	100	30277	Sec	F67N Time Adder #3	0-3000	–
1499	100	30278	Sec	F67N Min response time adder #3	0-100	–
1500	10	8341	%	F67N Min Polarizing Voltage #3	20-100	–
1501	1	8358	N/A	F67N Operating voltage side #3 Vy-0, Vz-1	0-31	–
1502	1	8345	N/A	F67N Polarizing Voltage #4 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 9 – Polarization Voltage is Vz1 (set both bits to 1) Bit 10 – Polarization Voltage is V1 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 9, 10 – Polarization Voltage is V0 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1503	100	8346	A	F67N Definite Pickup Current #4	5-2000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1504	100	8347	Sec	F67N Definite Time #4	0-60000	—
1505	100	8348	Degree	F67N Max Sensitivity angle #4	0-359	—
1506	100	30281	A	F67N Inverse Pickup Current #4	2-320	—
1507	1	30282	Degree	F67N Max Sensitivity angle 2 #4	5-90	—
1508	1	30284	N/A	F67N curve selection #4	0-71	—
1509	1	30285	N/A	F67N Enable Electromechanical Reset #4 Enable – 1, Disable – 0	0-1	—
1510	1000	30286	Sec	F67N Reset Co-eff #4	1-30000	—
1511	100	30288	N/A	F67N Time Multiplier #4	5-100	—
1512	100	30289	Sec	F67N Time Adder #4	0-3000	—
1513	100	30290	Sec	F67N Min response time adder #4	0-100	—
1514	10	8349	%	F67N Min Polarizing Voltage #4	20-100	—
1515	1	8358	N/A	F67N Operating voltage side #4 Vy-0, Vz-1	0-31	—
1516	1	8353	N/A	F67N Polarizing Voltage #5 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 9 – Polarization Voltage is Vz1 (set both bits to 1) Bit 10 – Polarization Voltage is V1 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 9, 10 – Polarization Voltage is V0 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	—
1517	100	8354	A	F67N Definite Pickup Current #5	5-2000	—
1518	100	8355	Sec	F67N Definite Time #5	0-60000	—
1519	100	8356	Degree	F67N Max Sensitivity angle #5	0-359	—
1520	100	30293	A	F67N Inverse Pickup Current #5	2-320	—
1521	1	30294	Degree	F67N Max Sensitivity angle 2 #5	5-90	—
1522	1	30296	N/A	F67N curve selection #5	0-71	—
1523	1	30297	N/A	F67N Enable Electromechanical Reset #5 Enable – 1, Disable – 0	0-1	—
1524	1000	30298	Sec	F67N Reset Co-eff #5	1-30000	—
1525	100	30300	N/A	F67N Time Multiplier #5	5-100	—
1526	100	30301	Sec	F67N Time Adder #5	0-3000	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1527	100	30302	Sec	F67N Min response time adder #5	0-100	—
1528	10	8357	%	F67N Min Polarizing Voltage #5	20-100	—
1529	1	8358	N/A	F67N Operating voltage side #5 Vy-0, Vz-1	0-31	—
1530	1	8371	N/A	F67G Polarizing Voltage #1 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 9 – Polarization Voltage is Vz1 (set both bits to 1) Bit 10 – Polarization Voltage is V1 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 9, 10 – Polarization Voltage is V0 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	—
1531	100	8372	A	F67G Definite Pickup Current #1	5-2000	—
1532	100	8373	Sec	F67G Definite Time #1	0-60000	—
1533	100	8374	Degree	F67G Max Sensitivity angle #1	0-359	—
1534	100	30404	A	F67G Inverse Pickup Current #1	2-320	—
1535	1	30405	Degree	F67G Max Sensitivity angle 2 #1	5-90	—
1536	1	30407	N/A	F67G curve selection #1	0-71	—
1537	1	30408	N/A	F67G Enable Electromechanical Reset #1 Enable – 1, Disable – 0	0-1	—
1538	1000	30409	Sec	F67G Reset Co-eff #1	1-30000	—
1539	100	30411	N/A	F67G Time Multiplier #1	5-100	—
1540	100	30412	Sec	F67G Time Adder #1	0-3000	—
1541	100	30413	Sec	F67G Min response time adder #1	0-100	—
1542	10	8375	%	F67G Min Polarizing Voltage #1	20-100	—
1543	1	8408	N/A	F67G Operating voltage side #1 Vy-0, Vz-1	0-31	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1544	1	8379	N/A	F67G Polarizing Voltage #2 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 9 – Polarization Voltage is Vz1 (set both bits to 1) Bit 10 – Polarization Voltage is V1 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 9, 10 – Polarization Voltage is V0 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1545	100	8380	A	F67G Definite Pickup Current #2	5-2000	–
1546	100	8381	Sec	F67G Definite Time #2	0-60000	–
1547	100	8382	Degree	F67G Max Sensitivity angle #2	0-359	–
1548	100	30416	A	F67G Inverse Pickup Current #2	2-320	–
1549	1	30417	Degree	F67G Max Sensitivity angle 2 #2	5-90	–
1550	1	30419	N/A	F67G curve selection #2	0-71	–
1551	1	30420	N/A	F67G Enable Electromechanical Reset #2 Enable – 1, Disable – 0	0-1	–
1552	1000	30421	Sec	F67G Reset Co-eff #2	1-30000	–
1553	100	30423	N/A	F67G Time Multiplier #2	5-100	–
1554	100	30424	Sec	F67G Time Adder #2	0-3000	–
1555	100	30425	Sec	F67G Min response time adder #2	0-100	–
1556	10	8283	%	F67G Min Polarizing Voltage #2	20-100	–
1557	1	8408	N/A	F67G Operating voltage side #2 Vy-0, Vz-1	0-31	–
1558	1	8387	N/A	F67G Polarizing Voltage #3 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 9 – Polarization Voltage is Vz1 (set both bits to 1) Bit 10 – Polarization Voltage is V1 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 9, 10 – Polarization Voltage is V0 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1559	100	8388	A	F67G Definite Pickup Current #3	5-2000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1560	100	8389	Sec	F67G Definite Time #3	0-60000	–
1561	100	8390	Degree	F67G Max Sensitivity angle #3	0-359	–
1562	100	30428	A	F67G Inverse Pickup Current #3	2-320	–
1563	1	30429	Degree	F67G Max Sensitivity angle 2 #3	5-90	–
1564	1	30431	N/A	F67G curve selection #3	0-71	–
1565	1	30432	N/A	F67G Enable Electromechanical Reset #3 Enable – 1, Disable – 0	0-1	–
1566	1000	30433	Sec	F67G Reset Co-eff #3	1-30000	–
1567	100	30435	N/A	F67G Time Multiplier #3	5-100	–
1568	100	30436	Sec	F67G Time Adder #3	0-3000	–
1569	100	30437	Sec	F67G Min response time adder #3	0-100	–
1570	10	8391	%	F67G Min Polarizing Voltage #3	20-100	–
1571	1	8408	N/A	F67G Operating voltage side #3 Vy-0, Vz-1	0-31	–
1572	1	8395	N/A	F67G Polarizing Voltage #4 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 9 – Polarization Voltage is Vz1 (set both bits to 1) Bit 10 – Polarization Voltage is V1 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 9, 10 – Polarization Voltage is V0 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1573	100	8396	A	F67G Definite Pickup Current #4	5-2000	–
1574	100	8397	Sec	F67G Definite Time #4	0-60000	–
1575	100	8398	Degree	F67G Max Sensitivity angle #4	0-359	–
1576	100	30440	A	F67G Inverse Pickup Current #4	2-320	–
1577	1	30441	Degree	F67G Max Sensitivity angle 2 #4	5-90	–
1578	1	30443	N/A	F67G curve selection #4	0-71	–
1579	1	30444	N/A	F67G Enable Electromechanical Reset #4 Enable – 1, Disable – 0	0-1	–
1580	1000	30445	Sec	F67G Reset Co-eff #4	1-30000	–
1581	100	30447	N/A	F67G Time Multiplier #4	5-100	–
1582	100	30448	Sec	F67G Time Adder #4	0-3000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1583	100	30449	Sec	F67G Min response time adder #4	0-100	—
1584	10	8399	%	F67G Min Polarizing Voltage #4	20-100	—
1585	1	8408	N/A	F67G Operating voltage side #4 Vy-0, Vz-1	0-31	—
1586	1	8403	N/A	F67G Polarizing Voltage #5 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 9 – Polarization Voltage is Vz1 (set both bits to 1) Bit 10 – Polarization Voltage is V1 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 9, 10 – Polarization Voltage is V0 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	—
1587	100	8404	A	F67G Definite Pickup Current #5	5-2000	—
1588	100	8405	Sec	F67G Definite Time #5	0-60000	—
1589	100	8406	Degree	F67G Max Sensitivity angle #5	0-359	—
1590	100	30452	A	F67G Inverse Pickup Current #5	2-320	—
1591	1	30453	Degree	F67G Max Sensitivity angle 2 #5	5-90	—
1592	1	30455	N/A	F67G curve selection #5	0-71	—
1593	1	30456	N/A	F67G Enable Electromechanical Reset #5 Enable – 1, Disable – 0	0-1	—
1594	1000	30457	Sec	F67G Reset Co-eff #5	1-30000	—
1595	100	30459	N/A	F67G Time Multiplier #5	5-100	—
1596	100	30460	Sec	F67G Time Adder #5	0-3000	—
1597	100	30461	Sec	F67G Min response time adder #5	0-100	—
1598	10	8407	%	F67G Min Polarizing Voltage #5	20-100	—
1599	1	8408	N/A	F67G Operating voltage side #5 Vy-0, Vz-1	0-31	—
1600	1	47144	N/A	F67G Blocking Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1601	1	8369	N/A	F67G Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1602	1	8370	N/A	Enable/Disable F67G #1 Enable – 1, Disable – 0	0-1	–
1603	1	47146	N/A	F67G Blocking Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1604	1	8377	N/A	F67G Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1605	1	8378	N/A	Enable/Disable F67G #2 Enable – 1, Disable – 0	0-1	–
1606	1	47148	N/A	F67G Blocking Inputs #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1607	1	8385	N/A	F67G Outputs #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1608	1	8386	N/A	Enable/Disable F67G #3 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1609	1	47150	N/A	F67G Blocking Inputs #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1610	1	8393	N/A	F67G Outputs #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1611	1	8394	N/A	Enable/Disable F67G #4 Enable – 1, Disable – 0	0-1	–
1612	1	47152	N/A	F67G Blocking Inputs #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1613	1	8401	N/A	F67G Outputs #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1614	1	8402	N/A	Enable/Disable F67G #5 Enable – 1, Disable – 0	0-1	–
1615	1	8421	N/A	F67Q Polarizing Voltage #1 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1616	100	8422	A	F67Q Definite Pickup Current #1	5-2000	–
1617	100	8423	Sec	F67Q Definite Time #1	0-60000	–
1618	100	8424	Degree	F67Q Max Sensitivity angle #1	0-359	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1619	100	30563	A	F67Q Inverse Pickup Current #1	2-320	–
1620	1	30564	Degree	F67Q Max Sensitivity angle 2 #1	5-90	–
1621	1	30565	N/A	F67Q curve selection #1	0-71	–
1622	1	30566	N/A	F67Q Enable Electromechanical Reset #1 Enable – 1, Disable – 0	0-1	–
1623	1000	30567	Sec	F67Q Reset Co-eff #1	1-30000	–
1624	100	30569	N/A	F67Q Time Multiplier #1	5-100	–
1625	100	30570	Sec	F67Q Time Adder #1	0-3000	–
1626	100	30571	Sec	F67Q Min response time adder #1	0-100	–
1627	10	8425	%	F67Q Min Polarizing Voltage #1	20-100	–
1628	1	8458	N/A	F67Q Operating voltage side #1 Vy-0, Vz-1	0-31	–
1629	1	8429	N/A	F67Q Polarizing Voltage #2 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1630	100	8430	A	F67Q Definite Pickup Current #2	5-2000	–
1631	100	8431	Sec	F67Q Definite Time #2	0-60000	–
1632	100	8432	Degree	F67Q Max Sensitivity angle #2	0-359	–
1633	100	30574	A	F67Q Inverse Pickup Current #2	2-320	–
1634	1	30575	Degree	F67Q Max Sensitivity angle 2 #2	5-90	–
1635	1	30576	N/A	F67Q curve selection #2	0-71	–
1636	1	30577	N/A	F67Q Enable Electromechanical Reset #2 Enable – 1, Disable – 0	0-1	–
1637	1000	30578	Sec	F67Q Reset Co-eff #2	1-30000	–
1638	100	30580	N/A	F67Q Time Multiplier #2	5-100	–
1639	100	30581	Sec	F67Q Time Adder #2	0-3000	–
1640	100	30582	Sec	F67Q Min response time adder #2	0-100	–
1641	10	8433	%	F67Q Min Polarizing Voltage #2	20-100	–
1642	1	8458	N/A	F67Q Operating voltage side #2 Vy-0, Vz-1	0-31	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1643	1	8437	N/A	F67Q Polarizing Voltage #3 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1644	100	8438	A	F67Q Definite Pickup Current #3	5-2000	–
1645	100	8439	Sec	F67Q Definite Time #3	0-60000	–
1646	100	8440	Degree	F67Q Max Sensitivity angle #3	0-359	–
1647	100	30585	A	F67Q Inverse Pickup Current #3	2-320	–
1648	1	30586	Degree	F67Q Max Sensitivity angle 2 #3	5-90	–
1649	1	30587	N/A	F67Q curve selection #3	0-71	–
1650	1	30588	N/A	F67Q Enable Electromechanical Reset #3 Enable – 1, Disable – 0	0-1	–
1651	1000	30589	Sec	F67Q Reset Co-eff #3	1-30000	–
1652	100	30591	N/A	F67Q Time Multiplier #3	5-100	–
1653	100	30592	Sec	F67Q Time Adder #3	0-3000	–
1654	100	30593	Sec	F67Q Min response time adder #3	0-100	–
1655	10	8441	%	F67Q Min Polarizing Voltage #3	20-100	–
1656	1	8458	N/A	F67Q Operating voltage side #3 Vy-0, Vz-1	0-31	–
1657	1	8445	N/A	F67Q Polarizing Voltage #4 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1658	100	8446	A	F67Q Definite Pickup Current #4	5-2000	–
1659	100	8447	Sec	F67Q Definite Time #4	0-60000	–
1660	100	8448	Degree	F67Q Max Sensitivity angle #4	0-359	–
1661	100	30596	A	F67Q Inverse Pickup Current #4	2-320	–
1662	1	30597	Degree	F67Q Max Sensitivity angle 2 #4	5-90	–
1663	1	30598	N/A	F67Q curve selection #4	0-71	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1664	1	30599	N/A	F67Q Enable Electromechanical Reset #4 Enable – 1, Disable – 0	0-1	–
1665	1000	30600	Sec	F67Q Reset Co-eff #4	1-30000	–
1666	100	30602	N/A	F67Q Time Multiplier #4	5-100	–
1667	100	30603	Sec	F67Q Time Adder #4	0-3000	–
1668	100	30604	Sec	F67Q Min response time adder #4	0-100	–
1669	10	8449	%	F67Q Min Polarizing Voltage #4	20-100	–
1670	1	8458	N/A	F67Q Operating voltage side #4 Vy-0, Vz-1	0-31	–
1671	1	8453	N/A	F67Q Polarizing Voltage #5 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip Rest of the bits are unused	0-65535	–
1672	100	8454	A	F67Q Definite Pickup Current #5	5-2000	–
1673	100	8455	Sec	F67Q Definite Time #5	0-60000	–
1674	100	8456	Degree	F67Q Max Sensitivity angle #5	0-359	–
1675	100	30607	A	F67Q Inverse Pickup Current #5	2-320	–
1676	1	30608	Degree	F67Q Max Sensitivity angle 2 #5	5-90	–
1677	1	30609	N/A	F67Q curve selection #5	0-71	–
1678	1	30610	N/A	F67Q Enable Electromechanical Reset #5 Enable – 1, Disable – 0	0-1	–
1679	1000	30611	Sec	F67Q Reset Co-eff #5	1-30000	–
1680	100	30613	N/A	F67Q Time Multiplier #5	5-100	–
1681	100	30614	Sec	F67Q Time Adder #5	0-3000	–
1682	100	30615	Sec	F67Q Min response time adder #5	0-100	–
1683	10	8457	%	F67Q Min Polarizing Voltage #5	20-100	–
1684	1	8458	N/A	F67Q Operating voltage side #5 Vy-0, Vz-1	0-31	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1685	1	47154	N/A	F67Q Blocking Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1686	1	8419	N/A	F67Q Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1687	1	8420	N/A	Enable/Disable F67Q #1 Enable – 1, Disable – 0	0-1	–
1688	1	47156	N/A	F67Q Blocking Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1689	1	8427	N/A	F67Q Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1690	1	8428	N/A	Enable/Disable F67Q #2 Enable – 1, Disable – 0	0-1	–
1691	1	47158	N/A	F67Q Blocking Inputs #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1692	1	8435	N/A	F67Q Outputs #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1693	1	8436	N/A	Enable/Disable F67Q #3 Enable – 1, Disable – 0	0-1	–
1694	1	47160	N/A	F67Q Blocking Inputs #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1695	1	8443	N/A	F67Q Outputs #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1696	1	8444	N/A	Enable/Disable F67Q #4 Enable – 1, Disable – 0	0-1	–
1697	1	47162	N/A	F67Q Blocking Inputs #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
1698	1	8451	N/A	F67Q Outputs #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
1699	1	8452	N/A	Enable/Disable F67Q #5 Enable – 1, Disable – 0	0-1	–
1700	1	30922	N/A	Triple Single Mode Selection	0-31	–
1701	1	8470	N/A	F79 Sequence Co-ordination	0-7	–
1702	1	8471	Sec	F79 Reset Time after auto-reclose	1-1800	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1703	1	8472	Sec	F79 Reset Time from Lockout	0-1800	—
1704	1	8473	N/A	F79 Recloser count,Ph and Gnd	0-65535	—
1705	1	30925	N/A	F79 Ph A Sequence Co-ordination	0-7	—
1706	1	30926	Sec	F79 Ph A Reset Time after auto-reclose	1-1800	—
1707	1	30927	Sec	F79 Ph A Reset Time from Lockout	0-1800	—
1708	1	30928	N/A	F79 Ph A Recloser count,Ph and Gnd	0-65535	—
1709	1	30932	N/A	F79 Ph B Sequence Co-ordination	0-7	—
1710	1	30933	Sec	F79 Ph B Reset Time after auto-reclose	1-1800	—
1711	1	30934	Sec	F79 Ph B Reset Time from Lockout	0-1800	—
1712	1	30935	N/A	F79 Ph B Recloser count,Ph and Gnd	0-65535	—
1713	1	30939	N/A	F79 Ph C Sequence Co-ordination	0-7	—
1714	1	30940	Sec	F79 Ph C Reset Time after auto-reclose	1-1800	—
1715	1	30941	Sec	F79 Ph C Reset Time from Lockout	0-1800	—
1716	1	30942	N/A	F79 Ph C Recloser count,Ph and Gnd	0-65535	—
1717	100	8475	A	F79 HCL Phase Pickup #1	10-10000	—
1718	100	8476	A	F79 HCL Ground Pickup #1	3-10000	—
1719	100	9016	A	F79 HCL Neutral Pickup #1	3-10000	—
1720	1000	9015	A	F79 HCL Sensitive Ground Pickup #1	1-160	—
1721	100	8477	Sec	F79 HCL Phase Time delay #1	0-60000	—
1722	100	8478	Sec	F79 HCL Ground Time delay #1	0-60000	—
1723	100	8480	A	F79 HCL Phase Pickup #2	10-10000	—
1724	100	8481	A	F79 HCL Ground Pickup #2	3-10000	—
1725	100	9018	A	F79 HCL Neutral Pickup #2	3-10000	—
1726	1000	9017	A	F79 HCL Sensitive Ground Pickup #2	1-160	—
1727	100	8482	Sec	F79 HCL Phase Time delay #2	0-60000	—
1728	100	8483	Sec	F79 HCL Ground Time delay #2	0-60000	—
1729	100	8485	A	F79 HCL Phase Pickup #3	10-10000	—
1730	100	8486	A	F79 HCL Ground Pickup #3	3-10000	—
1731	100	9020	A	F79 HCL Neutral Pickup #3	3-10000	—
1732	1000	9019	A	F79 HCL Sensitive Ground Pickup #3	1-160	—
1733	100	8487	Sec	F79 HCL Phase Time delay #3	0-60000	—
1734	100	8488	Sec	F79 HCL Ground Time delay #3	0-60000	—
1735	100	8490	A	F79 HCL Phase Pickup #4	10-10000	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1736	100	8491	A	F79 HCL Ground Pickup #4	3-10000	—
1737	100	9022	A	F79 HCL Neutral Pickup #4	3-10000	—
1738	1000	9021	A	F79 HCL Sensitive Ground Pickup #4	1-160	—
1739	100	8492	Sec	F79 HCL Phase Time delay #4	0-60000	—
1740	100	8493	Sec	F79 HCL Ground Time delay #4	0-60000	—
1741	100	8495	A	F79 HCL Phase Pickup #5	10-10000	—
1742	100	8496	A	F79 HCL Ground Pickup #5	3-10000	—
1743	100	9024	A	F79 HCL Neutral Pickup #5	3-10000	—
1744	1000	9023	A	F79 HCL Sensitive Ground Pickup #5	1-160	—
1745	100	8497	Sec	F79 HCL Phase Time delay #5	0-60000	—
1746	100	8498	Sec	F79 HCL Ground Time delay #5	0-60000	—
1747	100	30944	A	F79 HCL Phase A Pickup #1	10-10000	—
1748	100	30945	Sec	F79 HCL Phase A Time delay #1	0-60000	—
1749	100	30953	A	F79 HCL Phase A Pickup #2	10-10000	—
1750	100	30954	Sec	F79 HCL Phase A Time delay #2	0-60000	—
1751	100	30962	A	F79 HCL Phase A Pickup #3	10-10000	—
1752	100	30963	Sec	F79 HCL Phase A Time delay #3	0-60000	—
1753	100	30971	A	F79 HCL Phase A Pickup #4	10-10000	—
1754	100	30972	Sec	F79 HCL Phase A Time delay #4	0-60000	—
1755	100	30980	A	F79 HCL Phase A Pickup #5	10-10000	—
1756	100	30981	Sec	F79 HCL Phase A Time delay #5	0-60000	—
1757	100	30947	A	F79 HCL Phase B Pickup #1	10-10000	—
1758	100	30948	Sec	F79 HCL Phase B Time delay #1	0-60000	—
1759	100	30956	A	F79 HCL Phase B Pickup #2	10-10000	—
1760	100	30957	Sec	F79 HCL Phase B Time delay #2	0-60000	—
1761	100	30965	A	F79 HCL Phase B Pickup #3	10-10000	—
1762	100	30966	Sec	F79 HCL Phase B Time delay #3	0-60000	—
1763	100	30974	A	F79 HCL Phase B Pickup #4	10-10000	—
1764	100	30975	Sec	F79 HCL Phase B Time delay #4	0-60000	—
1765	100	30983	A	F79 HCL Phase B Pickup #5	10-10000	—
1766	100	30984	Sec	F79 HCL Phase B Time delay #5	0-60000	—
1767	100	30950	A	F79 HCL Phase C Pickup #1	10-10000	—
1768	100	30951	Sec	F79 HCL Phase C Time delay #1	0-60000	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1769	100	30959	A	F79 HCL Phase C Pickup #2	10-10000	—
1770	100	30960	Sec	F79 HCL Phase C Time delay #2	0-60000	—
1771	100	30968	A	F79 HCL Phase C Pickup #3	10-10000	—
1772	100	30969	Sec	F79 HCL Phase C Time delay #3	0-60000	—
1773	100	30977	A	F79 HCL Phase C Pickup #4	10-10000	—
1774	100	30978	Sec	F79 HCL Phase C Time delay #4	0-60000	—
1775	100	30986	A	F79 HCL Phase C Pickup #5	10-10000	—
1776	100	30987	Sec	F79 HCL Phase C Time delay #5	0-60000	—
1777	100	31096	A	F79 HCL Ph A Ground Pickup #1	3-10000	—
1778	100	31098	A	F79 HCL Ph A Neutral Pickup #1	3-10000	—
1779	1000	31097	A	F79 HCL Ph A Sensitive Ground Pickup #1	1-160	—
1780	100	31108	A	F79 HCL Ph A Ground Pickup #2	3-10000	—
1781	100	31110	A	F79 HCL Ph A Neutral Pickup #2	3-10000	—
1782	1000	31109	A	F79 HCL Ph A Sensitive Ground Pickup #2	1-160	—
1783	100	31120	A	F79 HCL Ph A Ground Pickup #3	3-10000	—
1784	100	31122	A	F79 HCL Ph A Neutral Pickup #3	3-10000	—
1785	1000	31121	A	F79 HCL Ph A Sensitive Ground Pickup #3	1-160	—
1786	100	31132	A	F79 HCL Ph A Ground Pickup #4	3-10000	—
1787	100	31134	A	F79 HCL Ph A Neutral Pickup #4	3-10000	—
1788	1000	31133	A	F79 HCL Ph A Sensitive Ground Pickup #4	1-160	—
1789	100	31144	A	F79 HCL Ph A Ground Pickup #5	3-10000	—
1790	100	31146	A	F79 HCL Ph A Neutral Pickup #5	3-10000	—
1791	1000	31145	A	F79 HCL Ph A Sensitive Ground Pickup #5	1-160	—
1792	100	31100	A	F79 HCL Ph B Ground Pickup #1	3-10000	—
1793	100	31102	A	F79 HCL Ph B Neutral Pickup #1	3-10000	—
1794	1000	31101	A	F79 HCL Ph B Sensitive Ground Pickup #1	1-160	—
1795	100	31112	A	F79 HCL Ph B Ground Pickup #2	3-10000	—
1796	100	31114	A	F79 HCL Ph B Neutral Pickup #2	3-10000	—
1797	1000	31113	A	F79 HCL Ph B Sensitive Ground Pickup #2	1-160	—
1798	100	31124	A	F79 HCL Ph B Ground Pickup #3	3-10000	—
1799	100	31126	A	F79 HCL Ph B Neutral Pickup #3	3-10000	—
1800	1000	31125	A	F79 HCL Ph B Sensitive Ground Pickup #3	1-160	—
1801	100	31136	A	F79 HCL Ph B Ground Pickup #4	3-10000	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1802	100	31138	A	F79 HCL Ph B Neutral Pickup #4	3-10000	—
1803	1000	31137	A	F79 HCL Ph B Sensitive Ground Pickup #4	1-160	—
1804	100	31148	A	F79 HCL Ph B Ground Pickup #5	3-10000	—
1805	100	31150	A	F79 HCL Ph B Neutral Pickup #5	3-10000	—
1806	1000	31149	A	F79 HCL Ph B Sensitive Ground Pickup #5	1-160	—
1807	100	31104	A	F79 HCL Ph C Ground Pickup #1	3-10000	—
1808	100	31107	A	F79 HCL Ph C Neutral Pickup #1	3-10000	—
1809	1000	31106	A	F79 HCL Ph C Sensitive Ground Pickup #1	1-160	—
1810	100	31116	A	F79 HCL Ph C Ground Pickup #2	3-10000	—
1811	100	31118	A	F79 HCL Ph C Neutral Pickup #2	3-10000	—
1812	1000	31117	A	F79 HCL Ph C Sensitive Ground Pickup #2	1-160	—
1813	100	31128	A	F79 HCL Ph C Ground Pickup #3	3-10000	—
1814	100	31130	A	F79 HCL Ph C Neutral Pickup #3	3-10000	—
1815	1000	31129	A	F79 HCL Ph C Sensitive Ground Pickup #3	1-160	—
1816	100	31140	A	F79 HCL Ph C Ground Pickup #4	3-10000	—
1817	100	31142	A	F79 HCL Ph C Neutral Pickup #4	3-10000	—
1818	1000	31141	A	F79 HCL Ph C Sensitive Ground Pickup #4	1-160	—
1819	100	31152	A	F79 HCL Ph C Ground Pickup #5	3-10000	—
1820	100	31154	A	F79 HCL Ph C Neutral Pickup #5	3-10000	—
1821	1000	31153	A	F79 HCL Ph C Sensitive Ground Pickup #5	1-160	—
1822	100	8501	V	27BSVS Bus minimum Closing Voltage	0-30000	—
1823	100	8502	Sec	27BSVS Supervision time	0-60000	—
1824	100	31014	V	27BSVS Ph A Bus minimum Closing Voltage	0-30000	—
1825	100	31015	Sec	27BSVS Ph A Supervision time	0-60000	—
1826	100	31017	V	27BSVS Ph B Bus minimum Closing Voltage	0-30000	—
1827	100	31018	Sec	27BSVS Ph B Supervision time	0-60000	—
1828	100	31020	V	27BSVS Ph C Bus minimum Closing Voltage	0-30000	—
1829	100	31021	Sec	27BSVS Ph C Supervision time	0-60000	—
1830	100	8503	Sec	F79 3-Ph Time delay for Reclose #1	1-60000	—
1831	100	8504	Sec	F79 Gnd Time delay for Reclose #1	1-60000	—
1832	100	8505	Sec	F79 3-Ph Time delay for Reclose #2	1-60000	—
1833	100	8506	Sec	F79 Gnd Time delay for Reclose #2	1-60000	—
1834	100	8507	Sec	F79 3-Ph Time delay for Reclose #3	1-60000	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1835	100	8508	Sec	F79 Gnd Time delay for Reclose #3	1-60000	—
1836	100	8509	Sec	F79 3-Ph Time delay for Reclose #4	1-60000	—
1837	100	8510	Sec	F79 Gnd Time delay for Reclose #4	1-60000	—
1838	100	30989	Sec	F79 Ph-A Time delay for Reclose #1	1-60000	—
1839	100	30990	Sec	F79 Ph-A Gnd Time delay for Reclose #1	1-60000	—
1840	100	30991	Sec	F79 Ph-A Time delay for Reclose #2	1-60000	—
1841	100	30992	Sec	F79 Ph-A Gnd Time delay for Reclose #2	1-60000	—
1842	100	30993	Sec	F79 Ph-A Time delay for Reclose #3	1-60000	—
1843	100	30994	Sec	F79 Ph-A Gnd Time delay for Reclose #3	1-60000	—
1844	100	30995	Sec	F79 Ph-A Time delay for Reclose #4	1-60000	—
1845	100	30996	Sec	F79 Ph-A Gnd Time delay for Reclose #4	1-60000	—
1846	100	30997	Sec	F79 Ph-B Time delay for Reclose #1	1-60000	—
1847	100	30998	Sec	F79 Ph-B Gnd Time delay for Reclose #1	1-60000	—
1848	100	30999	Sec	F79 Ph-B Time delay for Reclose #2	1-60000	—
1849	100	31000	Sec	F79 Ph-B Gnd Time delay for Reclose #2	1-60000	—
1850	100	31001	Sec	F79 Ph-B Time delay for Reclose #3	1-60000	—
1851	100	31002	Sec	F79 Ph-B Gnd Time delay for Reclose #3	1-60000	—
1852	100	31003	Sec	F79 Ph-B Time delay for Reclose #4	1-60000	—
1853	100	31004	Sec	F79 Ph-B Gnd Time delay for Reclose #4	1-60000	—
1854	100	31005	Sec	F79 Ph-C Time delay for Reclose #1	1-60000	—
1855	100	31006	Sec	F79 Ph-C Gnd Time delay for Reclose #1	1-60000	—
1856	100	31007	Sec	F79 Ph-C Time delay for Reclose #2	1-60000	—
1857	100	31008	Sec	F79 Ph-C Gnd Time delay for Reclose #2	1-60000	—
1858	100	31009	Sec	F79 Ph-C Time delay for Reclose #3	1-60000	—
1859	100	31010	Sec	F79 Ph-C Gnd Time delay for Reclose #3	1-60000	—
1860	100	31011	Sec	F79 Ph-C Time delay for Reclose #4	1-60000	—
1861	100	31012	Sec	F79 Ph-C Gnd Time delay for Reclose #4	1-60000	—
1862	1	47164	N/A	F79 3-Ph Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1863	1	47166	N/A	F79 Ph-A Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
1864	1	47168	N/A	F79 Ph-B Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
1865	1	47170	N/A	F79 Ph-C Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
1866	1	8469	N/A	Enable/Disable F79 for 3-Ph Enable – 1, Disable – 0	0-1	–
1867	1	30924	N/A	Enable/Disable F79 for Ph-A Enable – 1, Disable – 0	0-1	–
1868	1	30931	N/A	Enable/Disable F79 for Ph-B Enable – 1, Disable – 0	0-1	–
1869	1	30938	N/A	Enable/Disable F79 for Ph-C Enable – 1, Disable – 0	0-1	–
1870	1	8479	N/A	Enable HCL Phase and Ground Setting #1 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1871	1	8484	N/A	Enable HCL Phase and Ground Setting #2 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1872	1	8489	N/A	Enable HCL Phase and Ground Setting #3 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1873	1	8494	N/A	Enable HCL Phase and Ground Setting #4 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1874	1	8499	N/A	Enable HCL Phase and Ground Setting #5 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1875	1	30946	N/A	Enable HCL PhA and Ground Setting #1 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1876	1	30955	N/A	Enable HCL PhA and Ground Setting #2 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1877	1	30964	N/A	Enable HCL PhA and Ground Setting #3 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1878	1	30973	N/A	Enable HCL PhA and Ground Setting #4 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1879	1	30988	N/A	Enable HCL PhA and Ground Setting #5 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1880	1	30949	N/A	Enable HCL PhB and Ground Setting #1 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1881	1	30958	N/A	Enable HCL PhB and Ground Setting #2 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1882	1	30967	N/A	Enable HCL PhB and Ground Setting #3 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1883	1	30976	N/A	Enable HCL PhB and Ground Setting #4 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1884	1	30985	N/A	Enable HCL PhB and Ground Setting #5 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1885	1	30952	N/A	Enable HCL PhC and Ground Setting #1 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1886	1	30961	N/A	Enable HCL PhC and Ground Setting #2 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1887	1	30970	N/A	Enable HCL PhC and Ground Setting #3 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1888	1	30979	N/A	Enable HCL PhC and Ground Setting #4 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1889	1	30988	N/A	Enable HCL PhC and Ground Setting #5 Bit 0 – Phase HCL Enable Bit 1 – Ground HCL Enable	0-3	–
1890	1	8500	N/A	Enable 27BSVS Enable – 1, Disable – 0	0-1	–
1891	1	31013	N/A	Enable 27BSVS for Ph-A Enable – 1, Disable – 0	0-1	–
1892	1	31016	N/A	Enable 27BSVS for Ph-B Enable – 1, Disable – 0	0-1	–
1893	1	31019	N/A	Enable 27BSVS for Ph-C Enable – 1, Disable – 0	0-1	–
1894	1	8511	N/A	F79 Drive to lockout	0-65535	–
1895	1	8512	N/A	F79 Blocking(Supervision) functions Bit 0 – Enable 27Vx Bit 1 – Enable 25 Sync Bit 2 – Enable Remote Permission	0-7	–
1896	100	8513	Sec	F79 Supervision timer	0-60000	–
1897	1	31162	N/A	F79 Ph-A Drive to lockout	0-65535	–
1898	1	31156	N/A	F79 Ph-A Blocking(Supervision) functions Bit 0 – Enable 27Vx for Phase A Bit 1 – Enable 25 Sync for Phase A Bit 2 – Enable Remote Permission for Phase A	0-7	–
1899	100	31157	Sec	F79 Ph-A Supervision timer	0-60000	–
1900	1	31163	N/A	F79 Ph-B Drive to lockout	0-65535	–
1901	1	31158	N/A	F79 Ph-B Blocking(Supervision) functions Bit 0 – Enable 27Vx for Phase B Bit 1 – Enable 25 Sync for Phase B Bit 2 – Enable Remote Permission for Phase B	0-7	–
1902	100	31159	Sec	F79 Ph-B Supervision timer	0-60000	–
1903	1	31164	N/A	F79 Ph-C Drive to lockout	0-65535	–
1904	1	31160	N/A	F79 Ph-C Blocking(Supervision) functions Bit 0 – Enable 27Vx for Phase C Bit 1 – Enable 25 Sync for Phase C Bit 2 – Enable Remote Permission for Phase C	0-7	–
1905	100	31161	Sec	F79 Ph-C Supervision timer	0-60000	–
1906	1	46267	N/A	BFCP Curve Time Modifier Enable Enable – 1, Disable – 0	0-1	–
1907	1	46268	N/A	BFCP Curve Selection	22-62	–
1908	100	46269	Sec	BFCP Curve Time Multiplier	1-200	–
1909	100	46270	Sec	BFCP Curve Time Adder	0-3000	–
1910	100	46271	Sec	BFCP Curve Min. Resp Time Adder	0-100	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1911	1	46272	N/A	BFCP Curve Elec Mech Reset Enable Enable – 1, Disable – 0	0-1	–
1912	1000	46273	Sec	BFCP Curve Reset Coefficient	1-30000	–
1913	1	46275	N/A	BSCP Curve Time Modifier Enable Enable – 1, Disable – 0	0-1	–
1914	1	46276	N/A	BSCP Curve Selection	22-62	–
1915	100	46277	Sec	BSCP Curve Time Multiplier	1-200	–
1916	100	46278	Sec	BSCP Curve Time Adder	0-3000	–
1917	100	46279	Sec	BSCP Curve Min. Resp Time Adder	0-100	–
1918	1	46280	N/A	BSCP Curve ElecMech Reset Enable Enable – 1, Disable – 0	0-1	–
1919	1000	46281	Sec	BSCP Curve Reset Coefficient	1-30000	–
1920	1	46283	N/A	BFCG Curve Time Modifier Enable Enable – 1, Disable – 0	0-1	–
1921	1	46284	N/A	BFCG Curve Selection	22-62	–
1922	100	46285	Sec	BFCG Curve Time Multiplier	1-200	–
1923	100	46286	Sec	BFCG Curve Time Adder	0-3000	–
1924	100	46287	Sec	BFCG Curve Min. Resp Time Adder	0-100	–
1925	1	46288	N/A	BFCG Curve ElecMech Reset Enable Enable – 1, Disable – 0	0-1	–
1926	1000	46289	Sec	BFCG Curve Reset Coefficient	1-30000	–
1927	1	46291	N/A	BSCG Curve Time Modifier Enable Enable – 1, Disable – 0	0-1	–
1928	1	46292	N/A	BSCG Curve Selection	22-62	–
1929	100	46293	Sec	BSCG Curve Time Multiplier	1-200	–
1930	100	46294	Sec	BSCG Curve Time Adder	0-3000	–
1931	100	46295	Sec	BSCG Curve Min. Resp Time Adder	0-100	–
1932	1	46296	N/A	BSCG Curve ElecMech Reset Enable Enable – 1, Disable – 0	0-1	–
1933	1000	46297	Sec	BSCG Curve Reset Coefficient	1-30000	–
1934	1	8522	N/A	Trip/Close Seq F50P #(1-5), Stage 1	0-65535	–
1935	1	8523	N/A	Trip/Close Seq for 50N,50G,46DT Stage 1	0-65535	–
1936	1	8524	N/A	Trip/Close Seq F51P #(1-5), Stage 1	0-65535	–
1937	1	8525	N/A	Trip/Close Seq for 51N,51G,46IT Stage 1	0-65535	–
1938	1	8526	N/A	Trip/Close Seq F67P #(1-5), Stage 1	0-65535	–
1939	1	8527	N/A	Trip/Close Seq for 67N,67G,67Q Stage 1	0-65535	–
1940	1	8528	N/A	Trip/Close Seq F50P #(1-5), Stage 2	0-65535	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1941	1	8529	N/A	Trip/Close Seq for 50N,50G,46DT Stage 2	0-65535	—
1942	1	8530	N/A	Trip/Close Seq F51P #(1-5), Stage 2	0-65535	—
1943	1	8531	N/A	Trip/Close Seq for 51N,51G,46IT Stage 2	0-65535	—
1944	1	8532	N/A	Trip/Close Seq F67P #(1-5), Stage 2	0-65535	—
1945	1	8533	N/A	Trip/Close Seq for 67N,67G,67Q Stage 2	0-65535	—
1946	1	8534	N/A	Trip/Close Seq F50P #(1-5), Stage 3	0-65535	—
1947	1	8535	N/A	Trip/Close Seq for 50N,50G,46DT Stage 3	0-65535	—
1948	1	8536	N/A	Trip/Close Seq F51P #(1-5), Stage 3	0-65535	—
1949	1	8537	N/A	Trip/Close Seq for 51N,51G,46IT Stage 3	0-65535	—
1950	1	8538	N/A	Trip/Close Seq F67P #(1-5), Stage 3	0-65535	—
1951	1	8539	N/A	Trip/Close Seq for 67N,67G,67Q Stage 3	0-65535	—
1952	1	8540	N/A	Trip/Close Seq F50P #(1-5), Stage 4	0-65535	—
1953	1	8541	N/A	Trip/Close Seq for 50N,50G,46DT Stage 4	0-65535	—
1954	1	8542	N/A	Trip/Close Seq F51P #(1-5), Stage 4	0-65535	—
1955	1	8543	N/A	Trip/Close Seq for 51N,51G,46IT Stage 4	0-65535	—
1956	1	8544	N/A	Trip/Close Seq F67P #(1-5), Stage 4	0-65535	—
1957	1	8545	N/A	Trip/Close Seq for 67N,67G,67Q Stage 4	0-65535	—
1958	1	8546	N/A	Trip/Close Seq F50P #(1-5), Stage 5	0-65535	—
1959	1	8547	N/A	Trip/Close Seq for 50N,50G,46DT Stage 5	0-65535	—
1960	1	8548	N/A	Trip/Close Seq F51P #(1-5), Stage 5	0-65535	—
1961	1	8549	N/A	Trip/Close Seq for 51N,51G,46IT Stage 5	0-65535	—
1962	1	8550	N/A	Trip/Close Seq F67P #(1-5), Stage 5	0-65535	—
1963	1	8551	N/A	Trip/Close Seq for 67N,67G,67Q Stage 5	0-65535	—
1964	1	31041	N/A	Trip/Close Seq Ph-A F50P #(1-5), Stage 1	0-65535	—
1965	1	31223	N/A	Trip/Close Seq Ph-A for 50N,50G,46DT Stage 1	0-65535	—
1966	1	31044	N/A	Trip/Close Seq Ph-A F51P #(1-5), Stage 1	0-65535	—
1967	1	31226	N/A	Trip/Close Seq Ph-A for 51N,51G,46IT Stage 1	0-65535	—
1968	1	31047	N/A	Trip/Close Seq Ph-A F67P #(1-5), Stage 1	0-65535	—
1969	1	31229	N/A	Trip/Close Seq Ph-A for 67N,67G,67Q Stage 1	0-65535	—
1970	1	31050	N/A	Trip/Close Seq Ph-A F50P #(1-5), Stage 2	0-65535	—
1971	1	31232	N/A	Trip/Close Seq Ph-A for 50N,50G,46DT Stage 2	0-65535	—
1972	1	31053	N/A	Trip/Close Seq Ph-A F51P #(1-5), Stage 2	0-65535	—
1973	1	31235	N/A	Trip/Close Seq Ph-A for 51N,51G,46IT Stage 2	0-65535	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1974	1	31056	N/A	Trip/Close Seq Ph-A F67P #(1-5), Stage 2	0-65535	—
1975	1	31238	N/A	Trip/Close Seq Ph-A for 67N,67G,67Q Stage 2	0-65535	—
1976	1	31059	N/A	Trip/Close Seq Ph-A F50P #(1-5), Stage 3	0-65535	—
1977	1	31241	N/A	Trip/Close Seq Ph-A for 50N,50G,46DT Stage 3	0-65535	—
1978	1	31062	N/A	Trip/Close Seq Ph-A F51P #(1-5), Stage 3	0-65535	—
1979	1	31244	N/A	Trip/Close Seq Ph-A for 51N,51G,46IT Stage 3	0-65535	—
1980	1	31065	N/A	Trip/Close Seq Ph-A F67P #(1-5), Stage 3	0-65535	—
1981	1	31247	N/A	Trip/Close Seq Ph-A for 67N,67G,67Q Stage 3	0-65535	—
1982	1	31068	N/A	Trip/Close Seq Ph-A F50P #(1-5), Stage 4	0-65535	—
1983	1	31250	N/A	Trip/Close Seq Ph-A for 50N,50G,46DT Stage 4	0-65535	—
1984	1	31071	N/A	Trip/Close Seq Ph-A F51P #(1-5), Stage 4	0-65535	—
1985	1	31253	N/A	Trip/Close Seq Ph-A for 51N,51G,46IT Stage 4	0-65535	—
1986	1	31074	N/A	Trip/Close Seq Ph-A F67P #(1-5), Stage 4	0-65535	—
1987	1	31256	N/A	Trip/Close Seq Ph-A for 67N,67G,67Q Stage 4	0-65535	—
1988	1	31077	N/A	Trip/Close Seq Ph-A F50P #(1-5), Stage 5	0-65535	—
1989	1	31259	N/A	Trip/Close Seq Ph-A for 50N,50G,46DT Stage 5	0-65535	—
1990	1	31080	N/A	Trip/Close Seq Ph-A F51P #(1-5), Stage 5	0-65535	—
1991	1	31262	N/A	Trip/Close Seq Ph-A for 51N,51G,46IT Stage 5	0-65535	—
1992	1	31083	N/A	Trip/Close Seq Ph-A F67P #(1-5), Stage 5	0-65535	—
1993	1	31265	N/A	Trip/Close Seq Ph-A for 67N,67G,67Q Stage 5	0-65535	—
1994	1	31042	N/A	Trip/Close Seq Ph-B F50P #(1-5), Stage 1	0-65535	—
1995	1	31224	N/A	Trip/Close Seq Ph-B for 50N,50G,46DT Stage 1	0-65535	—
1996	1	31045	N/A	Trip/Close Seq Ph-B F51P #(1-5), Stage 1	0-65535	—
1997	1	31227	N/A	Trip/Close Seq Ph-B for 51N,51G,46IT Stage 1	0-65535	—
1998	1	31048	N/A	Trip/Close Seq Ph-B F67P #(1-5), Stage 1	0-65535	—
1999	1	31230	N/A	Trip/Close Seq Ph-B for 67N,67G,67Q Stage 1	0-65535	—
2000	1	31051	N/A	Trip/Close Seq Ph-B F50P #(1-5), Stage 2	0-65535	—
2001	1	31233	N/A	Trip/Close Seq Ph-B for 50N,50G,46DT Stage 2	0-65535	—
2002	1	31054	N/A	Trip/Close Seq Ph-B F51P #(1-5), Stage 2	0-65535	—
2003	1	31236	N/A	Trip/Close Seq Ph-B for 51N,51G,46IT Stage 2	0-65535	—
2004	1	31057	N/A	Trip/Close Seq Ph-B F67P #(1-5), Stage 2	0-65535	—
2005	1	31239	N/A	Trip/Close Seq Ph-B for 67N,67G,67Q Stage 2	0-65535	—
2006	1	31060	N/A	Trip/Close Seq Ph-B F50P #(1-5), Stage 3	0-65535	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2007	1	31242	N/A	Trip/Close Seq Ph-B for 50N,50G,46DT Stage 3	0-65535	—
2008	1	31063	N/A	Trip/Close Seq Ph-B F51P #(1-5), Stage 3	0-65535	—
2009	1	31245	N/A	Trip/Close Seq Ph-B for 51N,51G,46IT Stage 3	0-65535	—
2010	1	31066	N/A	Trip/Close Seq Ph-B F67P #(1-5), Stage 3	0-65535	—
2011	1	31248	N/A	Trip/Close Seq Ph-B for 67N,67G,67Q Stage 3	0-65535	—
2012	1	31069	N/A	Trip/Close Seq Ph-B F50P #(1-5), Stage 4	0-65535	—
2013	1	31251	N/A	Trip/Close Seq Ph-B for 50N,50G,46DT Stage 4	0-65535	—
2014	1	31072	N/A	Trip/Close Seq Ph-B F51P #(1-5), Stage 4	0-65535	—
2015	1	31254	N/A	Trip/Close Seq Ph-B for 51N,51G,46IT Stage 4	0-65535	—
2016	1	31075	N/A	Trip/Close Seq Ph-B F67P #(1-5), Stage 4	0-65535	—
2017	1	31257	N/A	Trip/Close Seq Ph-B for 67N,67G,67Q Stage 4	0-65535	—
2018	1	31078	N/A	Trip/Close Seq Ph-B F50P #(1-5), Stage 5	0-65535	—
2019	1	31260	N/A	Trip/Close Seq Ph-B for 50N,50G,46DT Stage 5	0-65535	—
2020	1	31081	N/A	Trip/Close Seq Ph-B F51P #(1-5), Stage 5	0-65535	—
2021	1	31263	N/A	Trip/Close Seq Ph-B for 51N,51G,46IT Stage 5	0-65535	—
2022	1	31084	N/A	Trip/Close Seq Ph-B F67P #(1-5), Stage 5	0-65535	—
2023	1	31266	N/A	Trip/Close Seq Ph-B for 67N,67G,67Q Stage 5	0-65535	—
2024	1	31043	N/A	Trip/Close Seq Ph-C F50P #(1-5), Stage 1	0-65535	—
2025	1	31225	N/A	Trip/Close Seq Ph-C for 50N,50G,46DT Stage 1	0-65535	—
2026	1	31046	N/A	Trip/Close Seq Ph-C F51P #(1-5), Stage 1	0-65535	—
2027	1	31228	N/A	Trip/Close Seq Ph-C for 51N,51G,46IT Stage 1	0-65535	—
2028	1	31049	N/A	Trip/Close Seq Ph-C F67P #(1-5), Stage 1	0-65535	—
2029	1	31231	N/A	Trip/Close Seq Ph-C for 67N,67G,67Q Stage 1	0-65535	—
2030	1	31052	N/A	Trip/Close Seq Ph-C F50P #(1-5), Stage 2	0-65535	—
2031	1	31234	N/A	Trip/Close Seq Ph-C for 50N,50G,46DT Stage 2	0-65535	—
2032	1	31055	N/A	Trip/Close Seq Ph-C F51P #(1-5), Stage 2	0-65535	—
2033	1	31237	N/A	Trip/Close Seq Ph-C for 51N,51G,46IT Stage 2	0-65535	—
2034	1	31058	N/A	Trip/Close Seq Ph-C F67P #(1-5), Stage 2	0-65535	—
2035	1	31240	N/A	Trip/Close Seq Ph-C for 67N,67G,67Q Stage 2	0-65535	—
2036	1	31061	N/A	Trip/Close Seq Ph-C F50P #(1-5), Stage 3	0-65535	—
2037	1	31243	N/A	Trip/Close Seq Ph-C for 50N,50G,46DT Stage 3	0-65535	—
2038	1	31064	N/A	Trip/Close Seq Ph-C F51P #(1-5), Stage 3	0-65535	—
2039	1	31246	N/A	Trip/Close Seq Ph-C for 51N,51G,46IT Stage 3	0-65535	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2040	1	31067	N/A	Trip/Close Seq Ph-C F67P #(1-5), Stage 3	0-65535	—
2041	1	31249	N/A	Trip/Close Seq Ph-C for 67N,67G,67Q Stage 3	0-65535	—
2042	1	31070	N/A	Trip/Close Seq Ph-C F50P #(1-5), Stage 4	0-65535	—
2043	1	31252	N/A	Trip/Close Seq Ph-C for 50N,50G,46DT Stage 4	0-65535	—
2044	1	31073	N/A	Trip/Close Seq Ph-C F51P #(1-5), Stage 4	0-65535	—
2045	1	31255	N/A	Trip/Close Seq Ph-C for 51N,51G,46IT Stage 4	0-65535	—
2046	1	31076	N/A	Trip/Close Seq Ph-C F67P #(1-5), Stage 4	0-65535	—
2047	1	31258	N/A	Trip/Close Seq Ph-C for 67N,67G,67Q Stage 4	0-65535	—
2048	1	31079	N/A	Trip/Close Seq Ph-C F50P #(1-5), Stage 5	0-65535	—
2049	1	31261	N/A	Trip/Close Seq Ph-C for 50N,50G,46DT Stage 5	0-65535	—
2050	1	31082	N/A	Trip/Close Seq Ph-C F51P #(1-5), Stage 5	0-65535	—
2051	1	31264	N/A	Trip/Close Seq Ph-C for 51N,51G,46IT Stage 5	0-65535	—
2052	1	31085	N/A	Trip/Close Seq Ph-C F67P #(1-5), Stage 5	0-65535	—
2053	1	31267	N/A	Trip/Close Seq Ph-C for 67N,67G,67Q Stage 5	0-65535	—
2054	100	8565	Hz	F81 Pickup #1	4000-6500	—
2055	100	8566	Sec	F81 Definite time #1	0-60000	—
2056	10	8567	Hz	F81 Hysteresis #1	0-10	—
2057	100	8568	V	F81 Minimum Voltage #1	100-18000	—
2058	100	8569	A	F81 Minimum Load #1	0-4000	—
2059	1	8570	N/A	F81 Operating voltage side #1 Bit 0 – Enable Undervoltage Block Bit 1 – Enable Minimum Load Bit 2 – 0 = Y-Side, 1 = Z-Side	0-7	—
2060	100	8574	Hz	F81 Pickup #2	4000-6500	—
2061	100	8575	Sec	F81 Definite Time #2	0-60000	—
2062	10	8576	Hz	F81 Hysteresis #2	0-10	—
2063	100	8577	V	F81 Minimum Voltage #2	100-18000	—
2064	100	8578	A	F81 Minimum Load #2	0-4000	—
2065	1	8579	N/A	F81 Operating voltage side #2 Bit 0 – Enable Undervoltage Block Bit 1 – Enable Minimum Load Bit 2 – 0 = Y-Side, 1 = Z-Side	0-7	—
2066	100	8583	Hz	F81 Pickup #3	4000-6500	—
2067	100	8584	Sec	F81 Definite Time #3	0-60000	—
2068	10	8585	Hz	F81 Hysteresis #3	0-10	—
2069	100	8586	V	F81 Minimum Voltage #3	100-18000	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2070	100	8587	A	F81 Minimum Load #3	0-4000	–
2071	1	8588	N/A	F81 Operating voltage side #3 Bit 0 – Enable Undervoltage Block Bit 1 – Enable Minimum Load Bit 2 – 0 = Y-Side, 1 = Z-Side	0-7	–
2072	100	8592	Hz	F81 Pickup #4	4000-6500	–
2073	100	8593	Sec	F81 Definite Time #4	0-60000	–
2074	10	8594	Hz	F81 Hysteresis #4	0-10	–
2075	100	8595	V	F81 Minimum Voltage #4	100-18000	–
2076	100	8596	A	F81 Minimum Load #4	0-4000	–
2077	1	8597	N/A	F81 Operating voltage side #4 Bit 0 – Enable Undervoltage Block Bit 1 – Enable Minimum Load Bit 2 – 0 = Y-Side, 1 = Z-Side	0-7	–
2078	1	47172	N/A	F81 Blocking Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2079	1	8563	N/A	F81 Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2080	1	8564	N/A	Enable/Disable F81 #1 Enable – 1, Disable – 0	0-1	–
2081	1	8570	N/A	Enable F81 UndVol Block #1	0-7	–
2082	1	8569	N/A	Enable F81 Min Load #1	0-4000	–
2083	1	47174	N/A	F81 Blocking Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2084	1	8572	N/A	F81 Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2085	1	8573	N/A	Enable/Disable F81 #2 Enable – 1, Disable – 0	0-1	–
2086	1	8579	N/A	Enable F81 UndVol Block #2	0-7	–
2087	1	8578	N/A	Enable F81 Min Load #2	0-4000	–
2088	1	47176	N/A	F81 Blocking Inputs #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2089	1	8581	N/A	F81 Outputs #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2090	1	8582	N/A	Enable/Disable F81 #3 Enable – 1, Disable – 0	0-1	–
2091	1	8588	N/A	Enable F81 UndVol Block #3	0-7	–
2092	1	8587	N/A	Enable F81 Min Load #3	0-4000	–
2093	1	47178	N/A	F81 Blocking Inputs #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2094	1	8590	N/A	F81 Outputs #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2095	1	8591	N/A	Enable/Disable F81 #4 Enable – 1, Disable – 0	0-1	–
2096	1	8597	N/A	Enable F81 UndVol Block #4	0-7	–
2097	1	8596	N/A	Enable F81 Min Load #4	0-4000	–
2098	100	8675	Hz/Sec	F81R Pickup #1	20-500	–
2099	100	8676	Sec	F81R Definite time #1	0-60000	–
2100	100	8677	Hz	F81R Max Frequency #1	4000-7000	–
2101	100	8678	A	F81R Min Current #1	0-10000	–
2102	100	8679	V	F81R Min Voltage #1	0-30000	–
2103	1	8680	N/A	F81R Pickup cycle number #1	3-15	–
2104	1	8690	N/A	F81R Operating voltage side #1 Vy-0, Vz-1	0-1	–
2105	100	8684	Hz/Sec	F81R Pickup #2	20-500	–
2106	100	8685	Sec	F81R Definite time #2	0-60000	–
2107	100	8686	Hz	F81R Max Frequency #2	4000-7000	–
2108	100	8687	A	F81R Min Current #2	0-10000	–
2109	100	8688	V	F81R Min Voltage #2	0-30000	–
2110	1	8689	N/A	F81R Pickup cycle number #2	3-15	–
2111	1	8691	N/A	F81R Operating voltage side #2 Vy-0, Vz-1	0-1	–
2112	1	47190	N/A	F81R Blocking Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2113	1	8673	N/A	F81R Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2114	1	8674	N/A	Enable/Disable F81R #1 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2115	1	47192	N/A	F81R Blocking Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2116	1	8682	N/A	F81R Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2117	1	8683	N/A	Enable/Disable F81R #2 Enable – 1, Disable – 0	0-1	–
2118	1	8774	N/A	Enable F50P in HLT Enable – 1, Disable – 0	0-1	–
2119	100	8775	A	F50P Pickup in HLT	2-2000	–
2120	100	8776	Sec	F50P Definite time in HLT	0-60000	–
2121	1	8777	N/A	Enable F50G in HLT Enable – 1, Disable – 0	0-1	–
2122	100	8778	A	F50G Pickup in HLT	2-2000	–
2123	100	8779	Sec	F50G Definite time in HLT	0-60000	–
2124	1	8780	N/A	Enable F51P in HLT Enable – 1, Disable – 0	0-1	–
2125	100	8781	A	F51P Pickup in HLT	2-320	–
2126	1	8782	N/A	F51P Curve selection in HLT	0-71	–
2127	1000	8784	Sec	F51P Reset Coeff in HLT	1-30000	–
2128	100	8786	N/A	F51P Time Multiplier in HLT	10-200	–
2129	100	8787	Sec	F51P Time Adder in HLT	0-3000	–
2130	100	8788	Sec	F51P Min response time adder in HLT	0-100	–
2131	1	8783	N/A	Enable Electromechanical Reset for F51P in HLT Enable – 1, Disable – 0	0-1	–
2132	1	8791	N/A	Enable F51G in HLT Enable – 1, Disable – 0	0-1	–
2133	100	8792	A	F51G Pickup in HLT	2-320	–
2134	1	8793	N/A	F51G Curve selection in HLT	0-71	–
2135	1000	8795	Sec	F51G Reset Coeff in HLT	1-30000	–
2136	100	8797	N/A	F51G Time Multiplier in HLT	10-200	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2137	100	8798	Sec	F51G Time Adder in HLT	0-3000	–
2138	100	8799	Sec	F51G Min response time adder in HLT	0-100	–
2139	1	8794	N/A	Enable Electromechanical Reset for F51G in HLT Enable – 1, Disable – 0	0-1	–
2140	1000	8802	A	F50GS Pickup in HLT	1-160	–
2141	1000	8803	A	F51GS Pickup in HLT	1-160	–
2142	–	8804	--	Enable F50N in HLT	0-1	–
2143	–	8805	A	F50N Pickup in HLT	2-2000	–
2144	–	8776	s	F50N Definite time in HLT	0-60000	–
2145	–	8807	--	Enable F51N in HLT	0-1	–
2146	–	8808	A	F51N Pickup in HLT	2-320	–
2147	–	8809	--	F51N Curve selection in HLT	0-67	–
2148	–	8810	s	F51N Reset Coeff in HLT	1-30000	–
2149	–	8823	--	F51N Time Multiplier in HLT	10-200	–
2150	–	8824	s	F51N Time Adder in HLT	0-3000	–
2151	–	8825	s	F51N Min response time adder in HLT	0-100	–
2152	–	8822	--	Enable Electromechanical Reset for F51N in HLT	0-1	–
2153	100	8817	A	F50BF Phase Current Pickup	2-200	–
2154	100	8819	A	F50BF Neutral Current Pickup	2-200	–
2155	100	9028	A	F50BF Ground Current Pickup	2-200	–
2156	1000	9027	A	F50BF Sensitive Ground Current Pickup	1-160	–
2157	100	8820	Sec	F50BF Definite time	1-13600	–
2158	100	8821	Sec	F50BF Retrip time	1-13600	–
2159	1	47226	N/A	F50BF Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2160	1	8813	N/A	F50BF Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2161	1	8827	N/A	F50BF Input Initiate Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2162	1	8816	N/A	F50BF Retrip Output Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2163	1	8814	N/A	Enable/Disable F50BF Enable – 1, Disable – 0	0-1	–
2164	1	8818	N/A	Enable Phase and Neutral Current pickup in F50BF Bit 0 – Enable Phase Current Bit 1 – Enable Ground/Neutral Current	0-3	–
2165	1	46045	N/A	Enable/Disable FBM Enable – 1, Disable – 0	0-1	–
2166	1	47282	N/A	Breaker Monitor Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2167	1	46044	N/A	Breaker Monitor Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2168	1	46052	N/A	Breaker Monitor Pickup	1-60000	–
2169	100	46053	Sec	Breaker Monitor Time Delay	0-600	–
2170	1	46054	mSec	FBM Breaker Open Arc Current Delay	0-2000	–
2171	1	46055	cycle	FBM Arc Current Cycle	0-20	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2172	1	47476	N/A	Breaker Monitor Input Initiate PhA Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2173	1	47478	N/A	Breaker Monitor Input Initiate PhB Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2174	1	47480	N/A	Breaker Monitor Input Initiate PhC Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2175	1	46049	N/A	Breaker Monitor Output Initiate PhA Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2176	1	46050	N/A	Breaker Monitor Output Initiate PhB Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2177	1	46051	N/A	Breaker Monitor Output Initiate PhC Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2178	1	8833	V,Sec	Auto restore High Voltage limit	1000-30000	–
2179	100	8834	V,Sec	Auto restore Low Voltage limit	1000-30000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2180	100	8835	Hz	Auto restore Low Frequency limit	5000-6000	—
2181	100	8836	Hz	Auto restore High Frequency limit	6000-7000	—
2182	10	8837	Sec	Auto restore Schedule time	0-36000	—
2183	10	8838	Sec	Auto restore Abort time	1-36000	—
2184	10	8839	Sec	Auto restore Transient time	1-36000	—
2185	1	8832	N/A	Enable Frequency loadshed restore limits Bit 0 – Enable the option to use both Voltage AND Frequency Restoration Limits Bit 1 – Enable Under Frequency Loadshed Restoration Bit 2 – Enable Under voltage/Overvoltage Loadshed Restoration	0-7	—
2186	1	8832	N/A	Enable Voltage loadshed restore limits Bit 0 – Enable the option to use both Voltage AND Frequency Restoration Limits Bit 1 – Enable Under Frequency Loadshed Restoration Bit 2 – Enable Undervoltage/Overvoltage Loadshed Restoration	0-7	—
2187	1	8840	N/A	Enable use both Vol and Freq restore limits Bit 0 – Enable option to use both Frequency and Voltage Restoration Limits Bit 1 – Change Mode of Operation (Three Phase/Single Phase) 0 =Three Phase 1 =Single Phase	0-3	—
2188	1	8840	N/A	Enable 3-Ph/1-Ph mode in Vol restore Bit 0 – Enable option to use both Frequency and Voltage Restoration Limits Bit 1 – Change Mode of Operation (Three Phase/Single Phase) 0 =Three Phase 1 =Single Phase	0-3	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2189	1	8841	N/A	F27 autorestore participation Bit 0 – F27 Element #1 Phase A Bit 1 – F27 Element #1 Phase B Bit 2 – F27 Element #1 Phase C Bit 3 – F27 Element #2 Phase A Bit 4 – F27 Element #2 Phase B Bit 5 – F27 Element #2 Phase C Bit 6 – F27 Element #3 Phase A Bit 7 – F27 Element #3 Phase B Bit 8 – F27 Element #3 Phase C Bit 9 – F27 Element #4 Phase A Bit 10 – F27 Element #4 Phase B Bit 11 – F27 Element #4 Phase C Bit 12 – F27 Element #5 3Ph Phase Bit 13 – F27 Element #6 3Ph Phase Bit 14 – F27 Element #7 3Ph Phase Bit 15 – F27 Element #8 3Ph Phase	0-65535	–
2190	1	8842	N/A	F59 auto restore participation Bit 0 – F59 Element #1 Phase A Bit 1 – F59 Element #1 Phase B Bit 2 – F59 Element #1 Phase C Bit 3 – F59 Element #2 Phase A Bit 4 – F59 Element #2 Phase B Bit 5 – F59 Element #2 Phase C Bit 6 – F59 Element #3 Phase A Bit 7 – F59 Element #3 Phase B Bit 8 – F59 Element #3 Phase C Bit 9 – F59 Element #4 Phase A Bit 10 – F59 Element #4 Phase B Bit 11 – F59 Element #4 Phase C Bit 12 – F59 Element #5 3Ph Phase Bit 13 – F59 Element #6 3Ph Phase Bit 14 – F59 Element #7 3Ph Phase Bit 15 – F59 Element #8 3Ph Phase	0-65535	–
2191	1	8843	N/A	F81 auto restore participation Bit 0 – F81 Element #1 Phase A Bit 1 – F81 Element #1 Phase B Bit 2 – F81 Element #1 Phase C Bit 3 – F81 Element #2 Phase A Bit 4 – F81 Element #2 Phase B Bit 5 – F81 Element #2 Phase C Bit 6 – F81 Element #3 Phase A Bit 7 – F81 Element #3 Phase B Bit 8 – F81 Element #3 Phase C Bit 9 – F81 Element #4 Phase A Bit 10 – F81 Element #4 Phase B Bit 11 – F81 Element #4 Phase C	0-4095	–
2192	1000	8857	A	F50GS Pickup #1	20-3200	–
2193	100	8858	Sec	F50GS Definite time #1	0-60000	–
2194	1000	8862	A	F50GS Pickup #2	20-3200	–
2195	100	8863	Sec	F50GS Definite time #2	0-60000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2196	1000	8867	A	F50GS Pickup #3	20-3200	—
2197	100	8868	Sec	F50GS Definite time #3	0-60000	—
2198	1000	8872	A	F50GS Pickup #4	20-3200	—
2199	100	8873	Sec	F50GS Definite time #4	0-60000	—
2200	1000	8877	A	F50GS Pickup #5	20-3200	—
2201	100	8878	Sec	F50GS Definite time #5	0-60000	—
2202	1	47228	N/A	F50GS Blocking Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	—
2203	1	8855	N/A	F50GS Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	—
2204	1	8856	N/A	Enable/Disable F50GS #1 Enable – 1, Disable – 0	0-1	—
2205	1	47230	N/A	F50GS Blocking Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	—
2206	1	8860	N/A	F50GS Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	—
2207	1	8861	N/A	Enable/Disable F50GS #2 Enable – 1, Disable – 0	0-1	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2208	1	47232	N/A	F50GS Blocking Inputs #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2209	1	8865	N/A	F50GS Outputs #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2210	1	8866	N/A	Enable/Disable F50GS #3 Enable – 1, Disable – 0	0-1	–
2211	1	47234	N/A	F50GS Blocking Inputs #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2212	1	8870	N/A	F50GS Outputs #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2213	1	8871	N/A	Enable/Disable F50GS #4 Enable – 1, Disable – 0	0-1	–
2214	1	47236	N/A	F50GS Blocking Inputs #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2215	1	8875	N/A	F50GS Outputs #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2216	1	8876	N/A	Enable/Disable F50GS #5 Enable – 1, Disable – 0	0-1	–
2217	1000	8893	A	F51GS Pickup #1	20-3200	–
2218	1	8894	N/A	F51GS Curve selection #1	0-71	–
2219	1000	8896	Sec	F51GS Reset Coeff #1	1-30000	–
2220	100	8898	N/A	F51GS Time Multiplier #1	10-200	–
2221	100	8899	Sec	F51GS Time Adder #1	0-3000	–
2222	100	8900	Sec	F51GS Min response time adder #1	1-100	–
2223	1000	8906	A	F51GS Pickup #2	20-3200	–
2224	1	8907	N/A	F51GS Curve selection #2	0-71	–
2225	1000	8909	Sec	F51GS Reset Coeff #2	1-30000	–
2226	100	8911	N/A	F51GS Time Multiplier #2	10-200	–
2227	100	8912	Sec	F51GS Time Adder #2	0-3000	–
2228	100	8913	Sec	F51GS Min response time adder #2	0-100	–
2229	1000	8919	A	F51GS Pickup #3	20-3200	–
2230	1	8920	N/A	F51GS Curve selection #3	0-71	–
2231	1000	8922	Sec	F51GS Reset Coeff #3	1-30000	–
2232	100	8924	N/A	F51GS Time Multiplier #3	10-200	–
2233	100	8925	Sec	F51GS Time Adder #3	0-3000	–
2234	100	8926	Sec	F51GS Min response time adder #3	0-100	–
2235	1000	8932	A	F51GS Pickup #4	20-3200	–
2236	1	8933	N/A	F51GS Curve selection #4	0-71	–
2237	1000	8935	Sec	F51GS Reset Coeff #4	1-30000	–
2238	100	8937	N/A	F51GS Time Multiplier #4	10-200	–
2239	100	8938	Sec	F51GS Time Adder #4	0-3000	–
2240	100	8939	Sec	F51GS Min response time adder #4	1-100	–
2241	1000	8945	A	F51GS Pickup #5	20-3200	–
2242	1	8946	N/A	F51GS Curve selection #5	0-71	–
2243	1000	8948	Sec	F51GS Reset Coeff #5	1-30000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2244	100	8950	N/A	F51GS Time Multiplier #5	10-200	—
2245	100	8951	Sec	F51GS Time Adder #5	0-3000	—
2246	100	8952	Sec	F51GS Min response time adder #5	1-100	—
2247	1	47238	N/A	F51GS Blocking Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	—
2248	1	8891	N/A	F51GS Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	—
2249	1	8892	N/A	Enable/Disable F51GS #1 Enable – 1, Disable – 0	0-1	—
2250	1	47240	N/A	F51GS Blocking Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	—
2251	1	8904	N/A	F51GS Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	—
2252	1	8905	N/A	Enable/Disable F51GS #2 Enable – 1, Disable – 0	0-1	—
2253	1	47242	N/A	F51GS Blocking Inputs #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2254	1	8917	N/A	F51GS Outputs #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2255	1	8918	N/A	Enable/Disable F51GS #3 Enable – 1, Disable – 0	0-1	–
2256	1	47244	N/A	F51GS Blocking Inputs #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2257	1	8930	N/A	F51GS Outputs #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2258	1	8931	N/A	Enable/Disable F51GS #4 Enable – 1, Disable – 0	0-1	–
2259	1	47246	N/A	F51GS Blocking Inputs #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2260	1	8943	N/A	F51GS Outputs #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2261	1	8944	N/A	Enable/Disable F51GS #5 Enable – 1, Disable – 0	0-1	–
2262	1	8895	N/A	Enable Electromechanical Reset for F51GS #1 Enable – 1, Disable – 0	0-1	–
2263	1	8908	N/A	Enable Electromechanical Reset for F51GS #2 Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2264	1	8921	N/A	Enable Electromechanical Reset for F51GS #3 Enable – 1, Disable – 0	0-1	–
2265	1	8934	N/A	Enable Electromechanical Reset for F51GS #4 Enable – 1, Disable – 0	0-1	–
2266	1	8947	N/A	Enable Electromechanical Reset for F51GS #5 Enable – 1, Disable – 0	0-1	–
2267	1	8969	N/A	F67GS Polarizing Voltage #1 Bit 0-Enable Direction Bit 4-0 = Use Definite Time Function 1 = Use Inverse Time Function Bit 5-Enable Max. Sensitivity Angle 2 Bit 8,9-Polarization Voltage is Vz1 (set both bits to 1) Bit 10-Polarization Voltage is V1 Bit 8,10-Polarization Voltage is V2 (set both bits to 1) Bit 9,10-Polarization Voltage is V0 (set both bits to 1) Bit 15-0 = Action if Below is Block Trip 1 = Action if Below is Trip	0-65535	–
2268	1000	8970	A	F67GS Definite Pickup Current #1	1-160	–
2269	100	8971	Sec	F67GS Definite Time #1	0-60000	–
2270	1	8972	Degree	F67GS Max Sensitivity angle #1	0-359	–
2271	1000	30856	A	F67GS Inverse Pickup Current #1	1-160	–
2272	1	30857	Degree	F67GS Max Sensitivity angle 2 #1	5-90	–
2273	1	30858	N/A	F67GS curve selection #1	0-71	–
2274	1	30859	N/A	F67GS Enable Electromechanical Reset #1 Enable – 1, Disable – 0	0-1	–
2275	1000	30560	Sec	F67GS Reset Co-eff #1	1-30000	–
2276	100	30862	N/A	F67GS Time Multiplier #1	5-100	–
2277	100	30863	Sec	F67GS Time Adder #1	0-3000	–
2278	100	30864	Sec	F67GS Min response time adder #1	0-100	–
2279	10	8973	%	F67GS Min Polarizing Voltage #1	20-100	–
2280	1	30911	N/A	F67GS Operating voltage side #1	0-31	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2281	1	8977	N/A	F67GS Polarizing Voltage #2 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 9 – Polarization Voltage is Vz1 (set both bits to 1) Bit 10 – Polarization Voltage is V1 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 9, 10 – Polarization Voltage is V0 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip	0-65535	–
2282	1000	8978	A	F67GS Definite Pickup Current #2	1-160	–
2283	100	8979	Sec	F67GS Definite Time #2	0-60000	–
2284	1	8980	Degree	F67GS Max Sensitivity angle #2	0-359	–
2285	1000	30867	A	F67GS Inverse Pickup Current #2	1-160	–
2286	1	30868	Degree	F67GS Max Sensitivity angle 2 #2	5-90	–
2287	1	30869	N/A	F67GS curve selection #2	0-71	–
2288	1	30870	N/A	F67GS Enable Electromechanical Reset #2 Enable – 1, Disable – 0	0-1	–
2289	1000	30871	Sec	F67GS Reset Co-eff #2	1-30000	–
2290	100	30873	N/A	F67GS Time Multiplier #2	5-100	–
2291	100	30874	Sec	F67GS Time Adder #2	0-3000	–
2292	100	30875	Sec	F67GS Min response time adder #2	0-100	–
2293	10	8981	%	F67GS Min Polarizing Voltage #2	20-100	–
2294	1	30911	N/A	F67GS Operating voltage side #2	0-31	–
2295	1	8985	N/A	F67GS Polarizing Voltage #3 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 9 – Polarization Voltage is Vz1 (set both bits to 1) Bit 10 – Polarization Voltage is V1 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 9, 10 – Polarization Voltage is V0 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip	0-65535	–
2296	1000	8986	A	F67GS Definite Pickup Current #3	1-160	–
2297	100	8987	Sec	F67GS Definite Time #3	0-60000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2298	1	8988	Degree	F67GS Max Sensitivity angle #3	0-359	–
2299	1000	30878	A	F67GS Inverse Pickup Current #3	1-160	–
2300	1	30879	Degree	F67GS Max Sensitivity angle 2 #3	5-90	–
2301	1	30880	N/A	F67GS curve selection #3	0-71	–
2302	1	30881	N/A	F67GS Enable Electromechanical Reset #3 Enable – 1, Disable – 0	0-1	–
2303	1000	30882	Sec	F67GS Reset Co-eff #3	1-30000	–
2304	100	30884	N/A	F67GS Time Multiplier #3	5-100	–
2305	100	30885	Sec	F67GS Time Adder #3	0-3000	–
2306	100	30886	Sec	F67GS Min response time adder #3	0-100	–
2307	10	8989	%	F67GS Min Polarizing Voltage #3	20-100	–
2308	1	30911	N/A	F67GS Operating voltage side #3	0-31	–
2309	1	8993	N/A	F67GS Polarizing Voltage #4 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 9 – Polarization Voltage is Vz1 (set both bits to 1) Bit 10 – Polarization Voltage is V1 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 9, 10 – Polarization Voltage is V0 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip	0-65535	–
2310	1000	8994	A	F67GS Definite Pickup Current #4	1-160	–
2311	100	8995	Sec	F67GS Definite Time #4	0-60000	–
2312	1	8996	Degree	F67GS Max Sensitivity angle #4	0-359	–
2313	1000	30889	A	F67GS Inverse Pickup Current #4	1-160	–
2314	1	30890	Degree	F67GS Max Sensitivity angle 2 #4	5-90	–
2315	1	30891	N/A	F67GS curve selection #4	0-71	–
2316	1	30892	N/A	F67GS Enable Electromechanical Reset #4 Enable – 1, Disable – 0	0-1	–
2317	1000	30893	Sec	F67GS Reset Co-eff #4	1-30000	–
2318	100	30895	N/A	F67GS Time Multiplier #4	5-100	–
2319	100	30896	Sec	F67GS Time Adder #4	0-3000	–
2320	100	30897	Sec	F67GS Min response time adder #4	0-100	–
2321	10	8997	%	F67GS Min Polarizing Voltage #4	20-100	–
2322	1	30911	N/A	F67GS Operating voltage side #4	0-31	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2323	1	9001	N/A	F67GS Polarizing Voltage #5 Bit 0 – Enable Direction Bit 4-0 – Use Definite Time Function 1 – Use Inverse Time Function Bit 5 – Enable Max. Sensitivity Angle 2 Bit 8, 9 – Polarization Voltage is Vz1 (set both bits to 1) Bit 10 – Polarization Voltage is V1 Bit 8, 10 – Polarization Voltage is V2 (set both bits to 1) Bit 9, 10 – Polarization Voltage is V0 (set both bits to 1) Bit 15-0 – Action if Below is Block Trip 1 – Action if Below is Trip	0-65535	–
2324	1000	9002	A	F67GS Definite Pickup Current #5	1-160	–
2325	100	9003	Sec	F67GS Definite Time #5	0-60000	–
2326	1	9004	Degree	F67GS Max Sensitivity angle #5	0-359	–
2327	1000	30900	A	F67GS Inverse Pickup Current #5	1-160	–
2328	1	30901	Degree	F67GS Max Sensitivity angle 2 #5	5-90	–
2329	1	30902	N/A	F67GS curve selection #5	0-71	–
2330	1	30903	N/A	F67GS Enable Electromechanical Reset #5 Enable – 1, Disable – 0	0-1	–
2331	1000	30904	Sec	F67GS Reset Co-eff #5	1-30000	–
2332	100	30906	N/A	F67GS Time Multiplier #5	5-100	–
2333	100	30907	Sec	F67GS Time Adder #5	0-3000	–
2334	100	30908	Sec	F67GS Min response time adder #5	0-100	–
2335	10	9005	%	F67GS Min Polarizing Voltage #5	20-100	–
2336	1	30911	N/A	F67GS Operating voltage side #5	0-31	–
2337	1	47248	N/A	F67GS Blocking Inputs #1 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2338	1	8967	N/A	F67GS Outputs #1 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2339	1	8968	N/A	Enable/Disable F67GS #1 Enable – 1, Disable – 0	0-1	–
2340	1	47250	N/A	F67GS Blocking Inputs #2 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2341	1	8975	N/A	F67GS Outputs #2 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2342	1	8976	N/A	Enable/Disable F67GS #2 Enable – 1, Disable – 0	0-1	–
2343	1	47252	N/A	F67GS Blocking Inputs #3 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2344	1	8983	N/A	F67GS Outputs #3 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2345	1	8984	N/A	Enable/Disable F67GS #3 Enable – 1, Disable – 0	0-1	–
2346	1	47254	N/A	F67GS Blocking Inputs #4 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2347	1	8991	N/A	F67GS Outputs #4 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2348	1	8992	N/A	Enable/Disable F67GS #4 Enable – 1, Disable – 0	0-1	–
2349	1	47256	N/A	F67GS Blocking Inputs #5 Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2350	1	8999	N/A	F67GS Outputs #5 Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2351	1	9000	N/A	Enable/Disable F67GS #5 Enable – 1, Disable – 0	0-1	–
2352	1	9145	%	F59I Pickup 3-Ph	100-150	–
2353	100	9146	Sec	F59I Definite time 3-Ph	1-14000	–
2354	1	9151	%	F59I Pickup Ph-A	100-150	–
2355	100	9152	Sec	F59I Definite time Ph-A	1-14000	–
2356	1	9156	%	F59I Pickup Ph-B	100-150	–
2357	100	9157	Sec	F59I Definite time Ph-B	1-14000	–
2358	1	9161	%	F59I Pickup Ph-C	100-150	–
2359	100	9162	Sec	F59I Definite time Ph-C	1-14000	–
2360	1	47274	N/A	F59I Blocking Inputs 3-Ph Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2361	1	9143	N/A	F59I Outputs 3-Ph Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2362	1	9144	N/A	Enable/Disable F59I 3-Ph Enable – 1, Disable – 0	0-1	–
2363	1	47276	N/A	F59I Blocking Inputs Ph-A Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2364	1	9149	N/A	F59I Outputs Ph-A Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2365	1	9150	N/A	Enable/Disable F59I Ph-A Enable – 1, Disable – 0	0-1	–
2366	1	47278	N/A	F59I Blocking Inputs Ph-B Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2367	1	9154	N/A	F59I Outputs Ph-B Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2368	1	9155	N/A	Enable/Disable F59I Ph-B Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2369	1	47280	N/A	F59I Blocking Inputs Ph-C Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2370	1	9159	N/A	F59I Outputs Ph-C Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2371	1	9160	N/A	Enable/Disable F59I Ph-C Enable – 1, Disable – 0	0-1	–
2372	1	63414	N/A	TCM Blocking Inputs Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2373	1	25431	N/A	TCM Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2374	1	63416	N/A	Trip Coil Inputs Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2375	1	25432	N/A	Enable/Disable TCM Enable – 1, Disable – 0	0-1	–
2376	100	25434	Sec	TCM Definite time	0-60000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2377	1	63466	N/A	CCM Blocking Inputs Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2378	1	25436	N/A	CCM Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2379	1	63418	N/A	Close Coil Inputs Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2380	1	25437	N/A	Enable/Disable CCM Enable – 1, Disable – 0	0-1	–
2381	100	25439	Sec	CCM Definite time	0-60000	–
2382	1	25458	N/A	Power Supply type 0 – Low Voltage DC 1 – High Voltage AC 2 – High Voltage DC	0-2	–
2383	100	25459	Sec	Time delay of Power supply test	0-65535	–
2384	1	25460	V	Reference High voltage in PSM	180-3150	–
2385	1	25461	V	Reference Low voltage in PSM	180-2500	–
2386	1	63418	N/A	PSM Blocking Inputs Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2387	1	25456	N/A	PSM Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2388	1	25457	N/A	Enable/Disable PSM Enable – 1, Disable – 0	0-1	–
2389	10	25500	atm	SF6 Gas Pressure Alarm Limit	9-20	–
2390	10	25501	atm	SF6 Gas Pressure DSP Block Limit	0-2	–
2391	1	25502	min	Power-Off Delay After AC Loss	1-10000	–
2392	1	25503	min	Power-Off Delay After Wakeup	1-1440	–
2393	1	25504	Sec	Boost Power On Delay After Trip, Close/Reclose	1-2000	–
2394	1	6507	N/A	Enable/Disable Battery charger Enable – 1, Disable – 0	0-1	–
2395	100	6506	V	Min. Voltage to turn off main 24V	10-30	–
2396	1	6505	hr	Time to perform load test on battery Bit 0 – 12 AM Bit 12 – 12 PM Bit 1 – 1 AM Bit 13 – 1 PM Bit 2 – 2 AM Bit 14 – 2 PM Bit 3 – 3 AM Bit 15 – 3 PM Bit 4 – 4 AM Bit 16 – 4 PM Bit 5 – 5 AM Bit 17 – 5 PM Bit 6 – 6 AM Bit 18 – 6 PM Bit 7 – 7 AM Bit 19 – 7 PM Bit 8 – 8 AM Bit 20 – 8 PM Bit 9 – 9 AM Bit 21 – 9 PM Bit 10 – 10 AM Bit 22 – 10 PM Bit 11 – 11 AM Bit 23 – 11 PM	0-23	–
2397	1	6515	N/A	Enable/Disable battery charger auxillary voltage Enable – 1, Disable – 0	0-1	–
2398	1	6511	N/A	Battery Charger model selection 0 – M-2032A 1 – G and W VRC 2 – BECO Drive (M-2034) 3 – M-2032B 4 – M-2035 5 – M-2035-Ext 6 – M-2035 Voltage Only	0-6	–
2399	1	63420	N/A	60FL Blocking Inputs Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2400	1	25486	N/A	60FL Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2401	1	25487	N/A	Enable/Disable 60FL Enable – 1, Disable – 0	0-1	–
2402	1	25489	N/A	Enable 3-Ph FL detection Enable – 1, Disable – 0	0-1	–
2403	1	63468	N/A	60FL Initiating Inputs Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-8191	–
2404	100	25488	Sec	60FL Definite time	3-60000	–
2405	1	63422	N/A	THD/TDD ABC #1 Inputs Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2406	1	28001	N/A	THD/TDD ABC #1 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2407	1	28002	N/A	THD/TDD ABC #1 Enable Enable – 1, Disable – 0	0-1	–
2408	10	28003	%	THD/TDD ABC #1 THD limit	3-10	–
2409	100	28004	Sec	THD/TDD ABC #1 Definite time	0-600	–
2410	1	28005	N/A	THD/TDD ABC #1 Operating quantity Bit 0-0 = THD Current; 1 = THD Voltage Bit 1 – 0 = THD; 1 = TDD	0-2	–
2411	1	28005	N/A	THD/TDD #1 Selection Bit 0-0 = THD Current; 1 = THD Voltage Bit 1 – 0 = THD; 1 = TDD	0-2	–
2412	1	28006	N/A	THD/TDD ABC #1 Individual phase enable Enable – 1, Disable – 0	0-1	–
2413	1	63424	N/A	THD/TDD ABC #2 Inputs Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2414	1	28008	N/A	THD/TDD ABC #2 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2415	1	28009	N/A	THD/TDD ABC #2 Enable Enable – 1, Disable – 0	0-1	–
2416	10	28010	%	THD/TDD ABC #2 THD limit	30-100	–
2417	100	28011	Sec	THD/TDD ABC #2 Definite time	0-60000	–
2418	1	28012	N/A	THD/TDD ABC #2 Operating quantity Bit 0-0 = THD Current; 1 = THD Voltage Bit 1 – 0 = THD; 1 = TDD	0-2	–
2419	1	28012	N/A	THD/TDD #2 Selection Bit 0-0 = THD Current; 1 = THD Voltage Bit 1 – 0 = THD; 1 = TDD	0-2	–
2420	1	28013	N/A	THD/TDD ABC #2 Individual phase enable Enable – 1, Disable – 0	0-1	–
2421	1	63426	N/A	THD/TDD A #1 Inputs Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2422	1	28015	N/A	THD/TDD A #1 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2423	1	28016	N/A	THD/TDD A #1 Enable Enable – 1, Disable – 0	0-1	–
2424	10	28017	%	THD/TDD A #1 THD limit	30-100	–
2425	100	28018	Sec	THD/TDD A #1 Definite time	0-60000	–
2426	1	28019	N/A	THD/TDD A #1 Operating quantity Bit 0-0 = THD Current; 1 = THD Voltage Bit 1 – 0 = THD; 1 = TDD	0-2	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2427	1	63428	N/A	THD/TDD B #1 Inputs Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2428	1	28021	N/A	THD/TDD B #1 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2429	1	28022	N/A	THD/TDD B #1 Enable Enable – 1, Disable – 0	0-1	–
2430	10	28023	%	THD/TDD B #1 THD limit	30-100	–
2431	100	28025	Sec	THD/TDD B #1 Definite time	0-60000	–
2432	1	28024	N/A	THD/TDD B #1 Operating quantity Bit 0-0 = THD Current; 1 = THD Voltage Bit 1 – 0 = THD; 1 = TDD	0-2	–
2433	1	63430	N/A	THD/TDD C #1 Inputs Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2434	1	28027	N/A	THD/TDD C #1 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2435	1	28028	N/A	THD/TDD C #1 Enable Enable – 1, Disable – 0	0-1	–
2436	10	28029	%	THD/TDD C #1 THD limit	30-100	–
2437	100	28030	Sec	THD/TDD C #1 Definite time	0-60000	–
2438	1	28031	N/A	THD/TDD C #1 Operating quantity Bit 0-0 = THD Current; 1 = THD Voltage Bit 1 – 0 = THD; 1 = TDD	0-2	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2439	1	63432	N/A	THD/TDD A #2 Inputs Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2440	1	28033	N/A	THD/TDD A #2 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2441	1	28034	N/A	THD/TDD A #2 Enable Enable – 1, Disable – 0	0-1	–
2442	10	28035	%	THD/TDD A #2 THD limit	30-100	–
2443	100	28036	Sec	THD/TDD A #2 Definite time	0-60000	–
2444	1	28037	N/A	THD/TDD A #2 Operating quantity Bit 0-0 = THD Current; 1 = THD Voltage Bit 1 – 0 = THD; 1 = TDD	0-2	–
2445	1	63434	N/A	THD/TDD B #2 Inputs Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2446	1	28039	N/A	THD/TDD B #2 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2447	1	28040	N/A	THD/TDD B #2 Enable Enable – 1, Disable – 0	0-1	–
2448	10	28041	%	THD/TDD B #2 THD limit	30-100	–
2449	100	28042	Sec	THD/TDD B #2 Definite time	0-60000	–
2450	1	28043	N/A	THD/TDD B #2 Operating quantity Bit 0-0 = THD Current; 1 = THD Voltage Bit 1 – 0 = THD; 1 = TDD	0-2	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2451	1	63436	N/A	THD/TDD C #2 Inputs Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2452	1	28045	N/A	THD/TDD C #2 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2453	1	28046	N/A	THD/TDD C #2 Enable Enable – 1, Disable – 0	0-1	–
2454	10	28047	%	THD/TDD C #2 THD limit	30-100	–
2455	100	28048	Sec	THD/TDD C #2 Definite time	0-60000	–
2456	1	28049	N/A	THD/TDD C #2 Operating quantity Bit 0-0 = THD Current; 1 = THD Voltage Bit 1 – 0 = THD; 1 = TDD	0-2	–
2457	1	47258	N/A	IPSLOGIC-1 Inputs Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2458	1	9101	N/A	IPSLOGIC-1 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2459	1	9102	N/A	IPSLOGIC-1 Enable Enable – 1, Disable – 0	0-1	–
2460	100	9103	Sec	IPSLOGIC-1 Delay	0-60000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2461	1	9132	N/A	IPSLOGIC-1 Profile Switching Bit 0 – None Bit 1 – Profile 1 Bit 2 – Profile 2 Bit 3 – Profile 3 Bit 4 – Profile 4 Bit 5 – Profile 5 Bit 6 – Profile 6 Bit 7 – Profile 7 Bit 8 – Profile 8	0-8	–
2462	1	46065	N/A	IPSLOGIC-1 Custom Message Option Bit 0 – None Bit 1 – Custom Message 1 Bit 2 – Custom Message 2 Bit 3 – Custom Message 3 Bit 4 – Custom Message 4 Bit 5 – Custom Message 5 Bit 6 – Custom Message 6 Bit 7 – Custom Message 7 Bit 8 – Custom Message 8	0-8	–
2463	1	47260	N/A	IPSLOGIC-2 Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2464	1	9105	N/A	IPSLOGIC-2 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2465	1	9106	N/A	IPSLOGIC-2 Enable Enable – 1, Disable – 0	0-1	–
2466	100	9107	Sec	IPSLOGIC-2 Delay	0-60000	–
2467	1	9133	N/A	IPSLOGIC-2 Profile Switching Bit 0 – None Bit 1 – Profile 1 Bit 2 – Profile 2 Bit 3 – Profile 3 Bit 4 – Profile 4 Bit 5 – Profile 5 Bit 6 – Profile 6 Bit 7 – Profile 7 Bit 8 – Profile 8	0-8	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2468	1	46066	N/A	IPSLOGIC-2 Custom Message Option Bit 0 – None Bit 1 – Custom Message 1 Bit 2 – Custom Message 2 Bit 3 – Custom Message 3 Bit 4 – Custom Message 4 Bit 5 – Custom Message 5 Bit 6 – Custom Message 6 Bit 7 – Custom Message 7 Bit 8 – Custom Message 8	0-8	–
2469	1	47262	N/A	IPSLOGIC-3 Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2470	1	9109	N/A	IPSLOGIC-3 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2471	1	9110	N/A	IPSLOGIC-3 Enable Enable – 1, Disable – 0	0-1	–
2472	100	9111	Sec	IPSLOGIC-3 Delay	0-60000	–
2473	1	9134	N/A	IPSLOGIC-3 Profile Switching Bit 0 – None Bit 1 – Profile 1 Bit 2 – Profile 2 Bit 3 – Profile 3 Bit 4 – Profile 4 Bit 5 – Profile 5 Bit 6 – Profile 6 Bit 7 – Profile 7 Bit 8 – Profile 8	0-8	–
2474	1	46067	N/A	IPSLOGIC-3 Custom Message Option Bit 0 – None Bit 1 – Custom Message 1 Bit 2 – Custom Message 2 Bit 3 – Custom Message 3 Bit 4 – Custom Message 4 Bit 5 – Custom Message 5 Bit 6 – Custom Message 6 Bit 7 – Custom Message 7 Bit 8 – Custom Message 8	0-8	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2475	1	47264	N/A	IPSLOGIC-4 Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2476	1	9113	N/A	IPSLOGIC-4 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2477	1	9114	N/A	IPSLOGIC-4 Enable Enable – 1, Disable – 0	0-1	–
2478	100	9115	Sec	IPSLOGIC-4 Delay	0-60000	–
2479	1	9135	N/A	IPSLOGIC-4 Profile Switching Bit 0 – None Bit 1 – Profile 1 Bit 2 – Profile 2 Bit 3 – Profile 3 Bit 4 – Profile 4 Bit 5 – Profile 5 Bit 6 – Profile 6 Bit 7 – Profile 7 Bit 8 – Profile 8	0-8	–
2480	1	46068	N/A	IPSLOGIC-4 Custom Message Option Bit 0 – None Bit 1 – Custom Message 1 Bit 2 – Custom Message 2 Bit 3 – Custom Message 3 Bit 4 – Custom Message 4 Bit 5 – Custom Message 5 Bit 6 – Custom Message 6 Bit 7 – Custom Message 7 Bit 8 – Custom Message 8	0-8	–
2481	1	47266	N/A	IPSLOGIC-5 Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2482	1	9117	N/A	IPSLOGIC-5 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2483	1	9118	N/A	IPSLOGIC-5 Enable Enable – 1, Disable – 0	0-1	–
2484	100	9119	Sec	IPSLOGIC-5 Delay	0-60000	–
2485	1	9136	N/A	IPSLOGIC-5 Profile Switching Bit 0 – None Bit 1 – Profile 1 Bit 2 – Profile 2 Bit 3 – Profile 3 Bit 4 – Profile 4 Bit 5 – Profile 5 Bit 6 – Profile 6 Bit 7 – Profile 7 Bit 8 – Profile 8	0-8	–
2486	1	46069	N/A	IPSLOGIC-5 Custom Message Option Bit 0 – None Bit 1 – Custom Message 1 Bit 2 – Custom Message 2 Bit 3 – Custom Message 3 Bit 4 – Custom Message 4 Bit 5 – Custom Message 5 Bit 6 – Custom Message 6 Bit 7 – Custom Message 7 Bit 8 – Custom Message 8	0-8	–
2487	1	47268	N/A	IPSLOGIC-6 Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2488	1	9121	N/A	IPSLOGIC-6 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2489	1	9122	N/A	IPSLOGIC-6 Enable Enable – 1, Disable – 0	0-1	–
2490	100	9123	Sec	IPSLOGIC-6 Delay	0-60000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2491	1	9137	N/A	IPSLOGIC-6 Profile Switching Bit 0 – None Bit 1 – Profile 1 Bit 2 – Profile 2 Bit 3 – Profile 3 Bit 4 – Profile 4 Bit 5 – Profile 5 Bit 6 – Profile 6 Bit 7 – Profile 7 Bit 8 – Profile 8	0-8	—
2492	1	46070	N/A	IPSLOGIC-6 Custom Message Option Bit 0 – None Bit 1 – Custom Message 1 Bit 2 – Custom Message 2 Bit 3 – Custom Message 3 Bit 4 – Custom Message 4 Bit 5 – Custom Message 5 Bit 6 – Custom Message 6 Bit 7 – Custom Message 7 Bit 8 – Custom Message 8	0-8	—
2493	1	47270	N/A	IPSLOGIC-7 Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	—
2494	1	9125	N/A	IPSLOGIC-7 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	—
2495	1	9126	N/A	IPSLOGIC-7 Enable Enable – 1, Disable – 0	0-1	—
2496	100	9127	Sec	IPSLOGIC-7 Delay	0-60000	—
2497	1	9138	N/A	IPSLOGIC-7 Profile Switching Bit 0 – None Bit 1 – Profile 1 Bit 2 – Profile 2 Bit 3 – Profile 3 Bit 4 – Profile 4 Bit 5 – Profile 5 Bit 6 – Profile 6 Bit 7 – Profile 7 Bit 8 – Profile 8	0-8	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2498	1	46071	N/A	IPSLOGIC-7 Custom Message Option Bit 0 – None Bit 1 – Custom Message 1 Bit 2 – Custom Message 2 Bit 3 – Custom Message 3 Bit 4 – Custom Message 4 Bit 5 – Custom Message 5 Bit 6 – Custom Message 6 Bit 7 – Custom Message 7 Bit 8 – Custom Message 8	0-8	–
2499	1	47272	N/A	IPSLOGIC-8 Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2500	1	9129	N/A	IPSLOGIC-8 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2501	1	9130	N/A	IPSLOGIC-8 Enable Enable – 1, Disable – 0	0-1	–
2502	100	9131	Sec	IPSLOGIC-8 Delay	0-60000	–
2503	1	9139	N/A	IPSLOGIC-8 Profile Switching Bit 0 – None Bit 1 – Profile 1 Bit 2 – Profile 2 Bit 3 – Profile 3 Bit 4 – Profile 4 Bit 5 – Profile 5 Bit 6 – Profile 6 Bit 7 – Profile 7 Bit 8 – Profile 8	0-8	–
2504	1	46072	N/A	IPSLOGIC-8 Custom Message Option Bit 0 – None Bit 1 – Custom Message 1 Bit 2 – Custom Message 2 Bit 3 – Custom Message 3 Bit 4 – Custom Message 4 Bit 5 – Custom Message 5 Bit 6 – Custom Message 6 Bit 7 – Custom Message 7 Bit 8 – Custom Message 8	0-8	–
2505	1	9174	N/A	Load Encroachment Enable Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2506	1	9175	N/A	Load Encroachment 67P direction Bit 0 – is asserted Bit 1 – F67P #1 Bit 2 – F67P #2 Bit 3 – F67P #3 Bit 4 – F67P #4 Bit 5 – F67P #5	0-5	–
2507	1	9176	ohm	Load Encroachment Fwd Load Imp	1-200	–
2508	1	9177	ohm	Load Encroachment Rev Load Imp	1-200	–
2509	1	9178	deg	Load Encroachment Pos Fwd load angle	-90-90	–
2510	1	9179	deg	Load Encroachment Pos Rev load angle	90-270	–
2511	1	9180	deg	Load Encroachment Neg Fwd load angle	-90-90	–
2512	1	9181	deg	Load Encroachment Neg Rev load angle	90-270	–
2513	1	47284	N/A	Sectionalizer Blocking Inputs 3-Phase Ganged Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2514	1	46136	N/A	Sectionalizer Enable 3-Phase Ganged Enable – 1, Disable – 0	0-1	–
2515	1	46137	N/A	Sectionalizer Max Trips 3-Phase Ganged Bit 1 – 1 Bit 2 – 2 Bit 3 – 3 Bit 4 – 4	1-4	–
2516	1	46138	N/A	Sectionalizer Trigger Options 3-Phase Ganged	0-15	–
2517	100	46139	Sec	Sectionalizer Phase Duration 3-Phase Ganged	1-30000	–
2518	1	46143	N/A	Sectionalizer Phase Multiplier 3-Phase Ganged	1-60000	–
2519	100	46145	Sec	Sectionalizer Reset Timer 3-Phase Ganged	1-60000	–
2520	100	46146	Sec	Sectionalizer Voltage Loss Delay 3-Phase Ganged	1-60000	–
2521	1	46147	N/A	Sectionalizer 50P,50N,50G/GS,46DT Selection 3-Phase Ganged	0-2097151	–
2522	1	46149	N/A	Sectionalizer 27,27PP Selection 3-Phase Ganged	0-65535	–
2523	1	46150	N/A	Sectionalizer Switch Enable 3-Phase Ganged Enable – 1, Disable – 0	0-1	–
2524	100	46141	Sec	Sectionalizer Ground Duration	1-30000	–
2525	1	46151	N/A	Sectionalizer Blocking Inputs Phase-A	0-65535	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2526	1	46152	N/A	Sectionalizer Enable Phase-A Enable – 1, Disable – 0	0-1	–
2527	1	46153	N/A	Sectionalizer Max Trips Phase-A Bit 1 – 1 Bit 2 – 2 Bit 3 – 3 Bit 4 – 4	1-4	–
2528	1	46154	N/A	Sectionalizer Trigger Options Phase-A	0-15	–
2529	100	46155	Sec	Sectionalizer Phase Duration Phase-A	1-30000	–
2530	1	46159	N/A	Sectionalizer Phase Multiplier Phase-A	1-60000	–
2531	100	46161	Sec	Sectionalizer Reset Timer Phase-A	1-60000	–
2532	100	46162	Sec	Sectionalizer Voltage Loss Delay Phase-A	1-60000	–
2533	1	46163	N/A	Sectionalizer 50P,50N,50G/GS,46DT Selection Phase-A	0-2097151	–
2534	1	46165	N/A	Sectionalizer 27,27PP Selection Phase-A	0-65535	–
2535	1	46166	N/A	Sectionalizer Switch Enable Phase-A Enable – 1, Disable – 0	0-1	–
2536	1	46167	N/A	Sectionalizer Blocking Inputs Phase-B	0-65535	–
2537	1	46168	N/A	Sectionalizer Enable Phase-B Enable – 1, Disable – 0	0-1	–
2538	1	46169	N/A	Sectionalizer Max Trips Phase-B Bit 1 – 1 Bit 2 – 2 Bit 3 – 3 Bit 4 – 4	1-4	–
2539	1	46170	N/A	Sectionalizer Trigger Options Phase-B	0-15	–
2540	100	46171	Sec	Sectionalizer Phase Duration Phase-B	1-30000	–
2541	1	46175	N/A	Sectionalizer Phase Multiplier Phase-B	1-60000	–
2542	100	46177	Sec	Sectionalizer Reset Timer Phase-B	1-60000	–
2543	100	46178	Sec	Sectionalizer Voltage Loss Delay Phase-B	1-60000	–
2544	1	46179	N/A	Sectionalizer 50P,50N,50G/GS,46DT Selection Phase-B	0-2097151	–
2545	1	46181	N/A	Sectionalizer 27,27PP Selection Phase-B	0-65535	–
2546	1	46182	N/A	Sectionalizer Switch Enable Phase-B Enable – 1, Disable – 0	0-1	–
2547	1	46183	N/A	Sectionalizer Blocking Inputs Phase-C	0-65535	–
2548	1	46184	N/A	Sectionalizer Enable Phase-C Enable – 1, Disable – 0	0-1	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2549	1	46185	N/A	Sectionalizer Max Trips Phase-C Bit 1 – 1 Bit 2 – 2 Bit 3 – 3 Bit 4 – 4	1-4	–
2550	1	46186	N/A	Sectionalizer Trigger Options Phase-C	0-15	–
2551	100	46187	Sec	Sectionalizer Phase Duration Phase-C	1-30000	–
2552	1	46191	N/A	Sectionalizer Phase Multiplier Phase-C	1-60000	–
2553	100	46193	Sec	Sectionalizer Reset Timer Phase-C	1-60000	–
2554	100	46194	Sec	Sectionalizer Voltage Loss Delay Phase-C	1-60000	–
2555	1	46195	N/A	Sectionalizer 50P,50N,50G/GS,46DT Selection Phase-C	0-2097151	–
2556	1	46197	N/A	Sectionalizer 27,27PP Selection Phase-C	0-65535	–
2557	1	46198	N/A	Sectionalizer Switch Enable Phase-C Enable – 1, Disable – 0	0-1	–
2558	1	46199	N/A	Sectionalizer Operation Mode	0-2	–
2559	1	46200	N/A	Sectionalizer Enable Target Reset Timer Enable: 1, Disable: 0	0-1	–
2560	–	46201	Sec	Sectionalizer Target Reset Timer	0-65000	–
2561	–	N/A	N/A	Sectionalizer Restraint Initiate Current Level	0-50	–
2562	–	N/A	N/A	Sectionalizer Restraint Initiate Voltage Level	0-99	–
2563	–	N/A	N/A	Sectionalizer Restraint Initiate Timer	0.01-600	–
2564	1	28060	N/A	Close Block Enable 3-Phase Enable: 1, Disable: 0	0-1	–
2565	1	63438	N/A	Close Block Inputs 3-Phase Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2566	1	28061	N/A	Close Block Voltage Upper Limit Enable 3-Phase	0-65535	–
2567	1	28061	N/A	Close Block Voltage Lower Limit Enable 3-Phase	0-65535	–
2568	1	28061	N/A	Close Block Frequency Limit Enable 3-Phase	0-65535	–
2569	100	28062	V l-g	Close Block Upper Volt Limit 3-Phase	1000-30000	–
2570	100	28063	V l-g	Close Block Lower Volt Limit 3-Phase	1000-30000	–
2571	100	28064	Hz	Close Block Upper Freq Limit 3-Phase	4000-6500	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2572	100	28065	Hz	Close Block Lower Freq Limit 3-Phase	4000-6500	—
2573	100	28066	Sec	Close Block Definite Time 3-Phase	0-60000	—
2574	100	28067	Sec	Close Block Override Time 3-Phase	0-60000	—
2575	1	28069	N/A	Close Block Enable Phase A Enable: 1, Disable: 0	0-1	—
2576	1	63440	N/A	Close Block Inputs Phase A Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	—
2577	1	28070	N/A	Close Block Voltage Upper Limit Enable Phase A	0-65535	—
2578	1	28070	N/A	Close Block Voltage Lower Limit Enable Phase A	0-65535	—
2579	1	28070	N/A	Close Block Frequency Limit Enable Phase A	0-65535	—
2580	100	28071	V I-g	Close Block Upper Volt Limit Phase A	1000-30000	—
2581	100	28072	V I-g	Close Block Lower Volt Limit Phase A	1000-30000	—
2582	100	28073	Hz	Close Block Upper Freq Limit Phase A	4000-6500	—
2583	100	28074	Hz	Close Block Lower Freq Limit Phase A	4000-6500	—
2584	100	28075	Sec	Close Block Definite Time Phase A	0-60000	—
2585	100	28076	Sec	Close Block Override Time Phase A	0-60000	—
2586	1	28078	N/A	Close Block Enable Phase B Enable: 1, Disable: 0	0-1	—
2587	1	63442	N/A	Close Block Inputs Phase B Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	—
2588	1	28079	N/A	Close Block Voltage Upper Limit Enable Phase B	0-65535	—
2589	1	28079	N/A	Close Block Voltage Lower Limit Enable Phase B	0-65535	—
2590	1	28079	N/A	Close Block Frequency Limit Enable Phase B	0-65535	—
2591	100	28080	V I-g	Close Block Upper Volt Limit Phase B	1000-30000	—
2592	100	28081	V I-g	Close Block Lower Volt Limit Phase B	1000-30000	—
2593	100	28082	Hz	Close Block Upper Freq Limit Phase B	4000-6500	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2594	100	28083	Hz	Close Block Lower Freq Limit Phase B	4000-6500	—
2595	100	28084	Sec	Close Block Definite Time Phase B	0-60000	—
2596	100	28085	Sec	Close Block Override Time Phase B	0-60000	—
2597	1	28087	N/A	Close Block Enable Phase C Enable: 1, Disable: 0	0-1	—
2598	1	63444	N/A	Close Block Inputs Phase C Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	—
2599	1	28088	N/A	Close Block Voltage Upper Limit Enable Phase C	0-65535	—
2600	1	28088	N/A	Close Block Voltage Lower Limit Enable Phase C	0-65535	—
2601	1	28088	N/A	Close Block Frequency Limit Enable Phase C	0-65535	—
2602	100	28089	V I-g	Close Block Upper Volt Limit Phase C	1000-30000	—
2603	100	28090	V I-g	Close Block Lower Volt Limit Phase C	1000-30000	—
2604	100	28091	Hz	Close Block Upper Freq Limit Phase C	4000-6500	—
2605	100	28092	Hz	Close Block Lower Freq Limit Phase C	4000-6500	—
2606	100	28093	Sec	Close Block Definite Time Phase C	0-60000	—
2607	100	28094	Sec	Close Block Override Time Phase C	0-60000	—
2608	1	28095	N/A	Close Block Operating Side Vy-0, Vz-1	0-1	—
2609	1000	6788	MileSec	Fault Location-Line Length	1-200000	—
2610	1000	6790	ohm	Fault Location-Positive Seq Resistance	1-1000000	—
2611	1000	6792	ohm	Fault Location-Positive Seq Reactance	1-1000000	—
2612	1000	6794	ohm	Fault Location-Zero Seq Resistance	1-1000000	—
2613	1000	6796	ohm	Fault Location-Zero Seq Reactance	1-1000000	—
2614	1000	N/A	N/A	Fault Location-Negative Seq Resistance	1-1000000	—
2615	1000	N/A	N/A	Fault Location-Negative Seq Reactance	1-1000000	—
2616	1	6798	N/A	Fault Loc-Participating Functions-F50P #(1-5)	0-65535	—
2617	1	6799	N/A	Fault Loc-Participating Functions-50N,50G,46DT	0-65535	—
2618	1	6800	N/A	Fault Loc-Participating Functions-F51P #(1-5)	0-65535	—
2619	1	6801	N/A	Fault Loc-Participating Functions-51N,51G,46IT	0-65535	—
2620	1	6802	N/A	Fault Loc-Participating Functions-F67P #(1-5)	0-65535	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2621	1	6803	N/A	Fault Loc-Participating Functions-67N,67G,67Q	0-65535	—
2622	1	20	N/A	Change Active profile 0 – Profile 1 1 – Profile 2 2 – Profile 3 3 – Profile 4 4 – Profile 5 5 – Profile 6 6 – Profile 7 7 – Profile 8	0-7	—
2623	1	6360	N/A	Maximum Profiles to be used 1 – Profile 1 is available 2 – Profiles 1 & 2 are available 3 – Profiles 1 to 3 are available 4 – Profiles 1 to 4 are available 5 – Profiles 1 to 5 are available 6 – Profiles 1 to 6 are available 7 – Profiles 1 to 7 are available 8 – All 8 Profiles are available	1-8	—
2624	—	0	N/A	Profile to be edited	0-7	—
2625	1	3804	N/A	Energize/De-energize the outputs	0-65535	—
2626	1	3803	N/A	Force Reset the selected outputs	0-65535	—
2627	1	8473	N/A	F79 Maximum number of phase trips 3-Phase Ganged	0-256	—
2628	1	8473	N/A	F79 Maximum number of ground/residual trips	0-256	—
2629	1	30928	N/A	F79 Maximum number of phase trips Phase A	0-256	—
2630	1	30935	N/A	F79 Maximum number of phase trips Phase B	0-256	—
2631	1	30942	N/A	F79 Maximum number of phase trips Phase C	0-256	—
2632	1	30928	N/A	F79 Maximum number of ground/residual trips Independent Phase Capable	0-256	—
2633	1	1793	N/A	REFU Port	1-65535	—
2634	—	113	N/A	Phase Angle Reference	0-18	—
2635	1	47292	N/A	Loop Scheme Blocking Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	—
2636	1	46320	N/A	Loop Scheme Application Type 0 – Sectionalizer, 1 – Tie	0-2	—
2637	1	46321	N/A	Loop Scheme Action on Voltage Option 0 - Auto Trip/Lockout, 1 - Auto Close, 2 - None	0-511	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2638	1	46322	N/A	Loop Scheme Reset Options Reset on: Bit 0 – Voltage Restored and no Fault Bit 1 – Ipslogic Enable Bit 2 – Manual Close Bit 3 – Manual Trip Bit 4 – After reset Auto Close Bit 5 – After reset return to Profile with Fwd power	0-63	–
2639	1	46323	N/A	Loop Scheme After reset LS Options Bit 0 – Ground Trip Block Bit 1 – Reclosing Block Bit 2 – Profile Switch Enable	0-7	–
2640	1	46324	N/A	Loop Scheme Supervision Bit 0 – Enable Bit 1 – F59NEnable Bit 2 – F59QEnable Bit 3 – IPSLogic Enable Bit 4 – Profile Switch Enable	0-31	–
2641	1	46325	N/A	Loop Scheme IPSLogic Selection Bits 0-2 – Reset Option Bits 3-7 – Reserved Bits8-10 – Supervision	0-4095	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2642	1	46331,46332	N/A	LS_DeadLine_Action: Source (Vy): (0-3) Bit 0-Profile1 Bit 4-Profile5 Bit 1-Profile2 Bit 5-Profile6 Bit 2-Profile3 Bit 6-Profile7 Bit 3-Profile4 Bit 7-Profile8 Bit 4-Profile5 LS_DeadLine_Action: Source (Vz): (4-7) Bit 0-Profile1 Bit 4-Profile5 Bit 1-Profile2 Bit 5-Profile6 Bit 2-Profile3 Bit 6-Profile7 Bit 3-Profile4 Bit 7-Profile8 Bit 4-Profile5 LS Restore on LiveLine_Source(Vy): (8-11) Bit 0-Profile1 Bit 4-Profile5 Bit 1-Profile2 Bit 5-Profile6 Bit 2-Profile3 Bit 6-Profile7 Bit 3-Profile4 Bit 7-Profile8 Bit 4-Profile5 LS Restore on LiveLine_Load(Vz): (12-15) Bit 0-Profile1 Bit 4-Profile5 Bit 1-Profile2 Bit 5-Profile6 Bit 2-Profile3 Bit 6-Profile7 Bit 3-Profile4 Bit 7-Profile8 Bit 4-Profile5 LS Reset_After Reset: (16-20) Bit 0-Profile1 Bit 4-Profile5 Bit 1-Profile2 Bit 5-Profile6 Bit 2-Profile3 Bit 6-Profile7 Bit 3-Profile4 Bit 7-Profile8 Bit 4-Profile5 LS TIE SUPV_Profile Switching: (21-24) Bit 0-Profile1 Bit 4-Profile5 Bit 1-Profile2 Bit 5-Profile6 Bit 2-Profile3 Bit 6-Profile7 Bit 3-Profile4 Bit 7-Profile8 Bit 4-Profile5 After LS_Profile Switching: (25-28) Bit 0-Profile1 Bit 4-Profile5 Bit 1-Profile2 Bit 5-Profile6 Bit 2-Profile3 Bit 6-Profile7 Bit 3-Profile4 Bit 7-Profile8 Bit 4-Profile5 N.A- (26-31)	—	—
2643	1	46328	N/A	Loop Scheme Dead Line Delay Time Y	0-9000	—
2644	1	46333	N/A	Loop Scheme Dead Line Delay Time Z	0-9000	—
2645	1	46329	Sec	Loop Scheme Gnd Trip Block Duration	0-300	—
2646	1	46330	Sec	Loop Scheme Reclose Block Duration	0-300	—
2647	100	46310	V	Loop Scheme Dead Line Vy Pickup	0-300	—
2648	100	46311	S	Loop Scheme Dead Line Vy Definite Time	0-600	—
2649	100	46312	V	Loop Scheme Dead Line Vz Pickup	0-300	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2650	100	46313	S	Loop Scheme Dead Line Vz Definite Time	0-600	—
2651	100	46314	V	Loop Scheme Live Line Sensing Vy Pickup	0-300	—
2652	100	46315	S	Loop Scheme Live Line Sensing Vy Definite Time	0-600	—
2653	100	46316	V	Loop Scheme Live Line Sensing Vz Pickup	0-300	—
2654	100	46317	S	Loop Scheme Live Line Sensing Vz Definite Time	0-600	—
2655	1	46326	-	Loop Scheme Live Line Window Delay Time	0-9000	—
2656	—	46334	S	Loop Scheme Restore Vy Delay Time	0-900	—
2657	—	46343	S	Loop Scheme Restore Vz Delay Time	0-900	—
2658	10	46300	%	IHR Pickup	1-1000	—
2659	10	46301	%	IHR Dropout	1-1000	—
2660	100	46302	Sec	IHR Active Time	1-60000	—
2661	1	46303	N/A	IHR Function 50P	0-65535	—
2662	1	46304	N/A	IHR Function 50N,50G,46DT	0-65535	—
2663	1	46305	N/A	IHR Function 51P	0-65535	—
2664	1	46306	N/A	IHR Function 51N,51G,46IT	0-65535	—
2665	1	46307	N/A	IHR Function 67P	0-65535	—
2666	1	46308	N/A	IHR Function 67N,67G,67Q	0-65535	—
2667	100	29053	Sec	SGI Reset Timer	0-3000	—
2668	100	29054	Sec	SGI Time Delay	0-1000	—
2669	100	29065	Sec	SGI Reset Latch Timer	0-1000	—
2670	100	29055	Sec	SGI Window	0-100	—
2671	1	29120	Cycles	SGI PreTrigger Delay	0-30	—
2672	10	29126	%	SGI Delta Ig 3rd Harmonic Pickup	0-6000	--
2673	1000	29122	A	SGI Ig Fund OverCurrent Threshold Pickup	1-160	--
2674	100	29123	s	SGI Ig Fund OverCurrent Threshold Definite Time	0-60000	--
2675	1	29057	N/A	SGI Init Logic	0-80	—
2676	1	29058	N/A	SGI Reset Logic	0-80	—
2677	10000	29059	A	SGI Delta Ig Pickup	10-8000	—
2678	100	29060	Sec	SGI Delta Ig Dropout Time Delay	0-600	—
2679	1	29061	Deg	SGI Direction Angle	0-359	—
2680	1	29062	Deg	SGI Direction Angle Band	5-90	—
2681	100	29063	Sec	SGI Direction Time Delay	0-60000	—
2682	100	29063		SGI Directional OverCurrent Definite Time	0-60000	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2683	1	29066	Cycles	SIG Pre-Trigger Averaging Window	1-60	—
2684	10	29124	%	SIG Ig 3rd Harmonic Supervision Pickup	0-6000	—
2685	100	29125	s	SIG Ig 3rd Harmonic Supervision Definite Time	0-60000	—
2686	1	29001	N/A	Phase Current Harmonic selection #1	2-63	—
2687	1	29002	N/A	Phase Current Harmonic selection #2	2-63	—
2688	1	29003	N/A	Phase Current Harmonic selection #3	2-63	—
2689	1	29004	N/A	IG Harmonic Selection #1	2-63	—
2690	1	29005	N/A	IG Harmonic Selection #2	2-63	—
2691	1	29006	N/A	Phase Voltage Harmonic Selection #1	2-63	—
2692	1	29007	N/A	Phase Voltage Harmonic Selection #2	2-63	—
2693	1	29008	N/A	Phase Voltage Harmonic Selection #3	2-63	—
2694	1	29009	N/A	VG Harmonic Selection #1	2-63	—
2695	1	29010	N/A	VG Harmonic Selection #2	2-63	—
2696	10	29011	%	Phase Current #1 Threshold	0-6000	—
2697	10	29012	%	Phase Current #2 Threshold	0-6000	—
2698	10	29013	%	Phase Current #3 Threshold	0-6000	—
2699	10	29014	%	IG #1 Current Threshold	0-6000	—
2700	10	29015	%	IG #2 Current Threshold	0-6000	—
2701	10	29016	%	Phase Voltage #1 Threshold	0-6000	—
2702	10	29017	%	Phase Voltage #2 Threshold	0-6000	—
2703	10	29018	%	Phase Voltage #3 Threshold	0-6000	—
2704	10	29019	%	VG #1 Voltage Threshold	0-6000	—
2705	10	29020	%	VG #2 Voltage Threshold	0-6000	—
2706	100	29021	%	HIF Duration	0-60000	—
2707	10	29022	%	HIF Current Min Level	1-100	—
2708	10	29023	%	HIF Voltage Min Level	1-100	—
2709	1	63448	N/A	GlobalLOGIC-#1 Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	—

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2710	1	65001	N/A	GlobalLOGIC-#1 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2711	100	65003	Sec	GlobalLOGIC-#1 Delay	0-60000	–
2712	1	65004	N/A	Global LOGIC #1 Profile Switching Bit 0 – None Bit 1 – Profile 1 Bit 2 – Profile 2 Bit 3 – Profile 3 Bit 4 – Profile 4 Bit 5 – Profile 5 Bit 6 – Profile 6 Bit 7 – Profile 7 Bit 8 – Profile 8	0-8	–
2713	1	63450	N/A	Global LOGIC #2 Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2714	1	65010	N/A	Global LOGIC #2 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2715	100	65012	Sec	Global LOGIC #2 Delay	0-60000	–
2716	1	65013	N/A	Global LOGIC #2 Profile Switching Bit 0 – None Bit 1 – Profile 1 Bit 2 – Profile 2 Bit 3 – Profile 3 Bit 4 – Profile 4 Bit 5 – Profile 5 Bit 6 – Profile 6 Bit 7 – Profile 7 Bit 8 – Profile 8	0-8	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2717	1	63452	N/A	Global LOGIC #3 Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2718	1	65019	N/A	Global LOGIC #3 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2719	100	65021	Sec	Global LOGIC #3 Delay	0-60000	–
2720	1	65022	N/A	Global LOGIC #3 Profile Switching Bit 0 – None Bit 1 – Profile 1 Bit 2 – Profile 2 Bit 3 – Profile 3 Bit 4 – Profile 4 Bit 5 – Profile 5 Bit 6 – Profile 6 Bit 7 – Profile 7 Bit 8 – Profile 8	0-8	–
2721	1	63454	N/A	Global LOGIC #4 Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2722	1	65028	N/A	Global LOGIC #4 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2723	100	65030	Sec	Global LOGIC #4 Delay	0-60000	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2724	1	65031	N/A	Global LOGIC #4 Profile Switching Bit 0 – None Bit 1 – Profile 1 Bit 2 – Profile 2 Bit 3 – Profile 3 Bit 4 – Profile 4 Bit 5 – Profile 5 Bit 6 – Profile 6 Bit 7 – Profile 7 Bit 8 – Profile 8	0-8	–
2725	1	63456	N/A	Global LOGIC #5 Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2726	1	65037	N/A	Global LOGIC #5 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2727	100	65030	Sec	Global LOGIC #5 Delay	0-60000	–
2728	1	65031	N/A	Global LOGIC #5 Profile Switching Bit 0 – None Bit 1 – Profile 1 Bit 2 – Profile 2 Bit 3 – Profile 3 Bit 4 – Profile 4 Bit 5 – Profile 5 Bit 6 – Profile 6 Bit 7 – Profile 7 Bit 8 – Profile 8	0-8	–
2729	1	63458	N/A	Global LOGIC #6 Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2730	1	65046	N/A	Global LOGIC #6 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2731	100	65048	Sec	Global LOGIC #6 Delay	0-60000	–
2732	1	65049	N/A	Global LOGIC #6 Profile Switching Bit 0 – None Bit 1 – Profile 1 Bit 2 – Profile 2 Bit 3 – Profile 3 Bit 4 – Profile 4 Bit 5 – Profile 5 Bit 6 – Profile 6 Bit 7 – Profile 7 Bit 8 – Profile 8	0-8	–
2733	1	63460	N/A	Global LOGIC #7 Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2734	1	65055	N/A	Global LOGIC #7 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2735	100	65057	Sec	Global LOGIC #7 Delay	0-60000	–
2736	1	65058	N/A	Global LOGIC #7 Profile Switching Bit 0 – None Bit 1 – Profile 1 Bit 2 – Profile 2 Bit 3 – Profile 3 Bit 4 – Profile 4 Bit 5 – Profile 5 Bit 6 – Profile 6 Bit 7 – Profile 7 Bit 8 – Profile 8	0-8	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2737	1	63462	N/A	GlobalLOGIC #8 Inputs Bit 0 – In 1 Bit 8 – In 9 Bit 1 – In 2 Bit 9 – In 10 Bit 2 – In 3 Bit 10 – In 11 Bit 3 – In 4 Bit 11 – In 12 Bit 4 – In 5 Bit 12 – In FL Bit 5 – In 6 Bit 13 – In V1 Bit 6 – In 7 Bit 14 – In V2 Bit 7 – In 8 Bit 15 – In V3	0-65535	–
2738	1	65064	N/A	GlobalLOGIC #8 Outputs Bit 0 – Out 1 Bit 6 – Out 7 Bit 1 – Out 2 Bit 7 – Out 8 Bit 2 – Out 3 Bit 8 – Out 9 Bit 3 – Out 4 Bit 9 – Out 10 Bit 4 – Out 5 Bit 10 – Out 11 Bit 5 – Out 6 Bit 11 – Out 12	0-4095	–
2739	100	65066	Sec	GlobalLOGIC #8 Delay	0-60000	–
2740	1	65067	N/A	Global LOGIC #8 Profile Switching Bit 0 – None Bit 1 – Profile 1 Bit 2 – Profile 2 Bit 3 – Profile 3 Bit 4 – Profile 4 Bit 5 – Profile 5 Bit 6 – Profile 6 Bit 7 – Profile 7 Bit 8 – Profile 8	0-8	–
2741	1	46202	N/A	Sectionalizer SCM Current Selection 3-Phase Ganged	0-65535	–
2742	1	46203	N/A	Sectionalizer SCM Current Selection Phase-A	0-65535	–
2743	1	46204	N/A	Sectionalizer SCM Current Selection Phase-B	0-65535	–
2744	1	46205	N/A	Sectionalizer SCM Current Selection Phase-C	0-65535	–
2745	1	28100	N/A	Anlg Out Enable Port 1	0-3	–
2746	1	28102	N/A	Anlg Out Port 1 Ch 1 Scaling range Min	–	–
2747	1	28104	N/A	Anlg Out Port 1 Ch 1 Scaling range Max	–	–
2748	1	28107	N/A	Anlg Out Port 1 Ch 2 Scaling range Min	–	–
2749	1	28109	N/A	Anlg Out Port 1 Ch 2 Scaling range Max	–	–
2750	1	28101	N/A	Anlg Out Port 1 Ch 1 Mapping Reg	0-65535	–
2751	1	28106	N/A	Anlg Out Port 1 Ch 2 Mapping Reg	0-65535	–
2752	1	28111	N/A	Anlg Out Port 1 Current range Select	0-3	–
2753	1	28112	N/A	Anlg Out Enable Port 4	0-3	–
2754	1	28114	N/A	Anlg Out Port 4 Ch 1 Scaling range Min	–	–
2755	1	28116	N/A	Anlg Out Port 4 Ch 1 Scaling range Max	–	–

Table 4 Analog Output Points List (Status)

Analog Output Points List (Status)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
2756	1	28119	N/A	Anlg Out Port 4 Ch 2 Scaling range Min	—	—
2757	1	28121	N/A	Anlg Out Port 4 Ch 2 Scaling range Max	—	—
2758	1	28113	N/A	Anlg Out Port 4 Ch 1 Mapping Reg	0-65535	—
2759	1	28118	N/A	Anlg Out Port 4 Ch 2 Mapping Reg	0-65535	—
2760	1	28123	N/A	Anlg Out Port 4 Current range Select	0-3	—
2761	1	720	N/A	Open Lockout Initiate Input	0-65535	—
2762	1	721	N/A	Close Initiate Input	0-65535	—
2763	1	1066	N/A	Open Status Output	0-65535	—
2764	1	1065	N/A	Close Status Output	0-65535	—
2765	1	7377	N/A	View Independent phase F50P	0-31	—
2766	1	30757	N/A	View Independent phase F51P	0-31	—
2767	1	30121	N/A	View Independent phase F67P	0-31	—
2768	1	147	N/A	Fault Current 1stT Reset	0-1440	—

Table 4 Analog Output Points List (Status)

Binary Counter Lists						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
0	–	1166	N/A	Recloser/Breakers Operation Counters Phase A	0-99999999	–
1	–	1168	N/A	Recloser/Breakers Operation Counters Phase B	0-99999999	–
2	–	1170	N/A	Recloser/Breakers Operation Counters Phase C	0-99999999	–
3	–	1150	N/A	OC Pickup Counter Phase A	0-99999999	–
4	–	1154	N/A	OC Pickup Counter Phase B	0-99999999	–
5	–	1158	N/A	OC Pickup Counter Phase C	0-99999999	–
6	–	1188	N/A	OC Pickup Counter Ground	0-99999999	–
7	–	1186	N/A	OC Pickup Counter Residual	0-99999999	–
8	–	1152	N/A	OC Trip Counter Phase A	0-99999999	–
9	–	1156	N/A	OC Trip Counter Phase B	0-99999999	–
10	–	1160	N/A	OC Trip Counter Phase C	0-99999999	–
11	–	1184	N/A	OC Trip Counter Phase Ground	0-99999999	–
12	–	1182	N/A	OC Trip Counter Phase Residual	0-99999999	–
13	–	1174	N/A	IPSlogic Counter #1	0-65000	–
14	–	1175	N/A	IPSlogic Counter #2	0-65000	–
15	–	1176	N/A	IPSlogic Counter #3	0-65000	–
16	–	1177	N/A	IPSlogic Counter #4	0-65000	–
17	–	1178	N/A	IPSlogic Counter #5	0-65000	–
18	–	1179	N/A	IPSlogic Counter #6	0-65000	–
19	–	1180	N/A	IPSlogic Counter #7	0-65000	–
20	–	1181	N/A	IPSlogic Counter #8	0-65000	–
21	–	1195	N/A	CBEMA Event Counter Phase A	0-99999999	–
22	–	1196	N/A	CBEMA Event Counter Phase B	0-99999999	–
23	–	1197	N/A	CBEMA Event Counter Phase C	0-99999999	–
24	–	--	N/A	Sectionalizer Counter for 3-Phase Ganged	0-99999999	–
25	–	2006	N/A	Sectionalizer Counter for Phase A	0-99999999	–
26	–	2007	N/A	Sectionalizer Counter for Phase B	0-99999999	–
27	–	2008	N/A	Sectionalizer Counter for Phase C	0-99999999	–
28	–	1100	N/A	GlobalLogic Counter #1	0-99999999	–
29	–	1101	N/A	GlobalLogic Counter #2	0-99999999	–
30	–	1102	N/A	GlobalLogic Counter #3	0-99999999	–
31	–	1103	N/A	GlobalLogic Counter #4	0-99999999	–
32	–	1104	N/A	GlobalLogic Counter #5	0-99999999	–
33	–	1105	N/A	GlobalLogic Counter #6	0-99999999	–
34	–	1106	N/A	GlobalLogic Counter #7	0-99999999	–
35	–	1107	N/A	GlobalLogic Counter #8	0-99999999	–

Table 5 Binary Counter Lists

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
0	–	4864	N/A	Input 1 Status	0 = inactive 1 = active	–
1	–	4864	N/A	Input 2 Status	0 = inactive 1 = active	–
2	–	4864	N/A	Input 3 Status	0 = inactive 1 = active	–
3	–	4864	N/A	Input 4 Status	0 = inactive 1 = active	–
4	–	4864	N/A	Input 5 Status	0 = inactive 1 = active	–
5	–	4864	N/A	Input 6 Status	0 = inactive 1 = active	–
6	–	4864	N/A	Input 7 Status	0 = inactive 1 = active	–
7	–	4864	N/A	Input 8 Status	0 = inactive 1 = active	–
8	–	4864	N/A	Input 9 Status	0 = inactive 1 = active	–
9	–	4864	N/A	Input 10 Status	0 = inactive 1 = active	–
10	–	4864	N/A	Input 11 Status	0 = inactive 1 = active	–
11	–	4864	N/A	Input 12 Status	0 = inactive 1 = active	–
12	–	4864	N/A	Input FL Status	0 = inactive 1 = active	–
13	–	2254	N/A	Output 1 Status	0 = inactive 1 = active	–
14	–	2254	N/A	Output 2 Status	0 = inactive 1 = active	–
15	–	2254	N/A	Output 3 Status	0 = inactive 1 = active	–
16	–	2254	N/A	Output 4 Status	0 = inactive 1 = active	–
17	–	2254	N/A	Output 5 Status	0 = inactive 1 = active	–
18	–	2254	N/A	Output 6 Status	0 = inactive 1 = active	–
19	–	2254	N/A	Output 7 Status	0 = inactive 1 = active	–
20	–	2254	N/A	Output 8 Status	0 = inactive 1 = active	–
21	–	2254	N/A	Output 9 Status	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
22	–	2254	N/A	Output 10 Status	0 = inactive 1 = active	–
23	–	2254	N/A	Output 11 Status	0 = inactive 1 = active	–
24	–	2254	N/A	Output 12 Status	0 = inactive 1 = active	–
25	–	20	N/A	Profile 1 Active Status	0 = inactive 1 = active	–
26	–	20	N/A	Profile 2 Active Status	0 = inactive 1 = active	–
27	–	20	N/A	Profile 3 Active Status	0 = inactive 1 = active	–
28	–	20	N/A	Profile 4 Active Status	0 = inactive 1 = active	–
29	–	20	N/A	Profile 5 Active Status	0 = inactive 1 = active	–
30	–	20	N/A	Profile 6 Active Status	0 = inactive 1 = active	–
31	–	20	N/A	Profile 7 Active Status	0 = inactive 1 = active	–
32	–	20	N/A	Profile 8 Active Status	0 = inactive 1 = active	–
33	1	4552	N/A	Reclose State 0 for Pole#1	0 = inactive 1 = active	–
34	1	4552	N/A	Reclose State 1 for Pole#1	0 = inactive 1 = active	–
35	1	4552	N/A	Reclose State 2 for Pole#1	0 = inactive 1 = active	–
36	1	4552	N/A	Reclose State 3 for Pole#1	0 = inactive 1 = active	–
37	1	4552	N/A	Reclose State 4 for Pole#1	0 = inactive 1 = active	–
38	1	4552	N/A	Reclose State 5 for Pole#1	0 = inactive 1 = active	–
39	1	4552	N/A	Reclose State 6 for Pole#1	0 = inactive 1 = active	–
40	1	4552	N/A	Reclose State 7 for Pole#1	0 = inactive 1 = active	–
41	1	4552	N/A	Reclose State 8 for Pole#1	0 = inactive 1 = active	–
42	1	4552	N/A	Reclose State 9 for Pole#1	0 = inactive 1 = active	–
43	1	4552	N/A	Reclose State 10 for Pole#1	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
44	1	4552	N/A	Control Lockout for Pole#1	0 = inactive 1 = active	—
45	1	4567	N/A	Reclose State 0 for Pole#2	0 = inactive 1 = active	—
46	1	4567	N/A	Reclose State 1 for Pole#2	0 = inactive 1 = active	—
47	1	4567	N/A	Reclose State 2 for Pole#2	0 = inactive 1 = active	—
48	1	4567	N/A	Reclose State 3 for Pole#2	0 = inactive 1 = active	—
49	1	4567	N/A	Reclose State 4 for Pole#2	0 = inactive 1 = active	—
50	1	4567	N/A	Reclose State 5 for Pole#2	0 = inactive 1 = active	—
51	1	4567	N/A	Reclose State 6 for Pole#2	0 = inactive 1 = active	—
52	1	4567	N/A	Reclose State 7 for Pole#2	0 = inactive 1 = active	—
53	1	4567	N/A	Reclose State 8 for Pole#2	0 = inactive 1 = active	—
54	1	4567	N/A	Reclose State 9 for Pole#2	0 = inactive 1 = active	—
55	1	4567	N/A	Reclose State 10 for Pole#2	0 = inactive 1 = active	—
56	1	4567	N/A	Control Lockout for Pole#2	0 = inactive 1 = active	—
57	—	2255	N/A	F25 Pole#1 pickup status bit	0 = inactive 1 = active	—
58	—	2265	N/A	F25 Pole#1 timeout status bit	0 = inactive 1 = active	—
59	—	2255	N/A	F27#1 Pole#1 pickup status bit	0 = inactive 1 = active	—
60	—	2265	N/A	F27#1 Pole#1 timeout status bit	0 = inactive 1 = active	—
61	—	2255	N/A	F27#1 Pole#2 pickup status bit	0 = inactive 1 = active	—
62	—	2265	N/A	F27#1 Pole#2 timeout status bit	0 = inactive 1 = active	—
63	—	2255	N/A	F27#2 Pole#1 pickup status bit	0 = inactive 1 = active	—
64	—	2265	N/A	F27#2 Pole#1 timeout status bit	0 = inactive 1 = active	—
65	—	2255	N/A	F27#2 Pole#2 pickup status bit	0 = inactive 1 = active	—

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
66	–	2265	N/A	F27#2 Pole#2 timeout status bit	0 = inactive 1 = active	–
67	–	2255	N/A	F27#3 Pole#1 pickup status bit	0 = inactive 1 = active	–
68	–	2265	N/A	F27#3 Pole#1 timeout status bit	0 = inactive 1 = active	–
69	–	2255	N/A	F27#3 Pole#2 pickup status bit	0 = inactive 1 = active	–
70	–	2265	N/A	F27#3 Pole#2 timeout status bit	0 = inactive 1 = active	–
71	–	2255	N/A	F27#4 Pole#1 pickup status bit	0 = inactive 1 = active	–
72	–	2265	N/A	F27#4 Pole#1 timeout status bit	0 = inactive 1 = active	–
73	–	2255	N/A	F27#4 Pole#2 pickup status bit	0 = inactive 1 = active	–
74	–	2265	N/A	F27#4 Pole#2 timeout status bit	0 = inactive 1 = active	–
75	–	2256	N/A	F32#1 Pole#1 pickup status bit	0 = inactive 1 = active	–
76	–	2266	N/A	F32#1 Pole#1 timeout status bit	0 = inactive 1 = active	–
77	–	2256	N/A	F32# Pole#2 pickup status bit	0 = inactive 1 = active	–
78	–	2266	N/A	F32#1 Pole#2 timeout status bit	0 = inactive 1 = active	–
79	–	2256	N/A	F32#2 Pole#1 pickup status bit	0 = inactive 1 = active	–
80	–	2266	N/A	F32#2 Pole#1 timeout status bit	0 = inactive 1 = active	–
81	–	2256	N/A	F32#2 Pole#2 pickup status bit	0 = inactive 1 = active	–
82	–	2266	N/A	F32#2 Pole#2 timeout status bit	0 = inactive 1 = active	–
83	–	2256	N/A	F32#3 Pole#1 pickup status bit	0 = inactive 1 = active	–
84	–	2266	N/A	F32#3 Pole#1 timeout status bit	0 = inactive 1 = active	–
85	–	2256	N/A	F32#3 Pole#2 pickup status bit	0 = inactive 1 = active	–
86	–	2266	N/A	F32#3 Pole#2 timeout status bit	0 = inactive 1 = active	–
87	–	2256	N/A	F32#4 Pole#1 pickup status bit	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
88	—	2266	N/A	F32#4 Pole#1 timeout status bit	0 = inactive 1 = active	—
89	—	2256	N/A	F32#4 Pole#2 pickup status bit	0 = inactive 1 = active	—
90	—	2266	N/A	F32#4 Pole#2 timeout status bit	0 = inactive 1 = active	—
91	—	2256	N/A	F50P#1 Pole#1 pickup status bit	0 = inactive 1 = active	—
92	—	2266	N/A	F50P#1 Pole#1 timeout status bit	0 = inactive 1 = active	—
93	—	2256	N/A	F50P#1 Pole#2 pickup status bit	0 = inactive 1 = active	—
94	—	2266	N/A	F50P#1 Pole#2 timeout status bit	0 = inactive 1 = active	—
95	—	2257	N/A	F50P#2 Pole#1 pickup status bit	0 = inactive 1 = active	—
96	—	2267	N/A	F50P#2 Pole#1 timeout status bit	0 = inactive 1 = active	—
97	—	2257	N/A	F50P#2 Pole#2 pickup status bit	0 = inactive 1 = active	—
98	—	2267	N/A	F50P#2 Pole#2 timeout status bit	0 = inactive 1 = active	—
99	—	2257	N/A	F50P#3 Pole#1 pickup status bit	0 = inactive 1 = active	—
100	—	2267	N/A	F50P#3 Pole#1 timeout status bit	0 = inactive 1 = active	—
101	—	2257	N/A	F50P#3 Pole#2 pickup status bit	0 = inactive 1 = active	—
102	—	2267	N/A	F50P#3 Pole#2 timeout status bit	0 = inactive 1 = active	—
103	—	2257	N/A	F50P#4 Pole#1 pickup status bit	0 = inactive 1 = active	—
104	—	2267	N/A	F50P#4 Pole#1 timeout status bit	0 = inactive 1 = active	—
105	—	2257	N/A	F50P#4 Pole#2 pickup status bit	0 = inactive 1 = active	—
106	—	2267	N/A	F50P#4 Pole#2 timeout status bit	0 = inactive 1 = active	—
107	—	2258	N/A	F50PBF pickup status bit	0 = inactive 1 = active	—
108	—	2268	N/A	F50BF timeout status bit	0 = inactive 1 = active	—
109	—	2258	N/A	F51P#1 Pole#1 pickup status bit	0 = inactive 1 = active	—

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
110	–	2268	N/A	F51P#1 Pole#1 timeout status bit	0 = inactive 1 = active	–
111	–	2258	N/A	F51P#1 Pole#2 pickup status bit	0 = inactive 1 = active	–
112	–	2268	N/A	F51P#1 Pole#2 timeout status bit	0 = inactive 1 = active	–
113	–	2258	N/A	F51P#2 Pole#1 pickup status bit	0 = inactive 1 = active	–
114	–	2268	N/A	F51P#2 Pole#1 timeout status bit	0 = inactive 1 = active	–
115	–	2259	N/A	F51P#2 Pole#2 pickup status bit	0 = inactive 1 = active	–
116	–	2269	N/A	F51P#2 Pole#2 timeout status bit	0 = inactive 1 = active	–
117	–	2259	N/A	F51P#3 Pole#1 pickup status bit	0 = inactive 1 = active	–
118	–	2269	N/A	F51P#3 Pole#1 timeout status bit	0 = inactive 1 = active	–
119	–	2259	N/A	F51P#3 Pole#2 pickup status bit	0 = inactive 1 = active	–
120	–	2269	N/A	F51P#3 Pole#2 timeout status bit	0 = inactive 1 = active	–
121	–	2259	N/A	F51P#4 Pole#1 pickup status bit	0 = inactive 1 = active	–
122	–	2269	N/A	F51P#4 Pole#1 timeout status bit	0 = inactive 1 = active	–
123	–	2259	N/A	F51P#4 Pole#2 pickup status bit	0 = inactive 1 = active	–
124	–	2269	N/A	F51P#4 Pole#2 timeout status bit	0 = inactive 1 = active	–
125	–	2260	N/A	F59#1 Pole#1 pickup status bit	0 = inactive 1 = active	–
126	–	2270	N/A	F59#1 Pole#1 timeout status bit	0 = inactive 1 = active	–
127	–	2260	N/A	F59#1 Pole#2 pickup status bit	0 = inactive 1 = active	–
128	–	2270	N/A	F59#1 Pole#2 timeout status bit	0 = inactive 1 = active	–
129	–	2260	N/A	F59#2 Pole#1 pickup status bit	0 = inactive 1 = active	–
130	–	2270	N/A	F59#2 Pole#1 timeout status bit	0 = inactive 1 = active	–
131	–	2260	N/A	F59#2 Pole#2 pickup status bit	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
132	–	2270	N/A	F59#2 Pole#2 timeout status bit	0 = inactive 1 = active	–
133	–	2261	N/A	F59#3 Pole#1 pickup status bit	0 = inactive 1 = active	–
134	–	2271	N/A	F59#3 Pole#1 timeout status bit	0 = inactive 1 = active	–
135	–	2261	N/A	F59#3 Pole#2 pickup status bit	0 = inactive 1 = active	–
136	–	2271	N/A	F59#3 Pole#2 timeout status bit	0 = inactive 1 = active	–
137	–	2261	N/A	F59#4 Pole#1 pickup status bit	0 = inactive 1 = active	–
138	–	2271	N/A	F59#4 Pole#1 timeout status bit	0 = inactive 1 = active	–
139	–	2261	N/A	F59#4 Pole#2 pickup status bit	0 = inactive 1 = active	–
140	–	2271	N/A	F59#4 Pole#2 timeout status bit	0 = inactive 1 = active	–
141	–	2263	N/A	TCM pickup status bit	0 = inactive 1 = active	–
142	–	2273	N/A	TCM timeout status bit	0 = inactive 1 = active	–
143	–	2263	N/A	CCM pickup status bit	0 = inactive 1 = active	–
144	–	2273	N/A	CCM timeout status bit	0 = inactive 1 = active	–
145	–	2263	N/A	F81#1 pickup status bit	0 = inactive 1 = active	–
146	–	2273	N/A	F81#1 timeout status bit	0 = inactive 1 = active	–
147	–	2263	N/A	F81#2 pickup status bit	0 = inactive 1 = active	–
148	–	2273	N/A	F81#2 timeout status bit	0 = inactive 1 = active	–
149	–	2263	N/A	F81#3 pickup status bit	0 = inactive 1 = active	–
150	–	2273	N/A	F81#3 timeout status bit	0 = inactive 1 = active	–
151	–	2264	N/A	F81#4 pickup status bit	0 = inactive 1 = active	–
152	–	2274	N/A	F81#4 timeout status bit	0 = inactive 1 = active	–
153	–	2264	N/A	F81R#1 pickup status bit	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
154	–	2274	N/A	F81R#1 timeout status bit	0 = inactive 1 = active	–
155	–	2264	N/A	F81R#2 pickup status bit	0 = inactive 1 = active	–
156	–	2274	N/A	F81R#2 timeout status bit	0 = inactive 1 = active	–
157	–	2264	N/A	FCLP Pole#1 pickup status bit	0 = inactive 1 = active	–
158	–	2274	N/A	FCLP Pole#1 timeout status bit	0 = inactive 1 = active	–
159	–	2264	N/A	IPSLOGIC#1 pickup status bit	0 = inactive 1 = active	–
160	–	2274	N/A	IPSLOGIC#1 timeout status bit	0 = inactive 1 = active	–
161	–	2264	N/A	IPSLOGIC#2 pickup status bit	0 = inactive 1 = active	–
162	–	2274	N/A	IPSLOGIC#2 timeout status bit	0 = inactive 1 = active	–
163	–	2264	N/A	IPSLOGIC#3 pickup status bit	0 = inactive 1 = active	–
164	–	2274	N/A	IPSLOGIC#3 timeout status bit	0 = inactive 1 = active	–
165	–	2264	N/A	IPSLOGIC#4 pickup status bit	0 = inactive 1 = active	–
166	–	2274	N/A	IPSLOGIC#4 timeout status bit	0 = inactive 1 = active	–
167	–	2264	N/A	IPSLOGIC#5 pickup status bit	0 = inactive 1 = active	–
168	–	2274	N/A	IPSLOGIC#5 timeout status bit	0 = inactive 1 = active	–
169	–	2264	N/A	IPSLOGIC#6 pickup status bit	0 = inactive 1 = active	–
170	–	2274	N/A	IPSLOGIC#6 timeout status bit	0 = inactive 1 = active	–
171	–	2264	N/A	IPSLOGIC#7 pickup status bit	0 = inactive 1 = active	–
172	–	2274	N/A	IPSLOGIC#7 timeout status bit	0 = inactive 1 = active	–
173	–	2264	N/A	IPSLOGIC#8 pickup status bit	0 = inactive 1 = active	–
174	–	2274	N/A	IPSLOGIC#8 timeout status bit	0 = inactive 1 = active	–
175	–	2264	N/A	PSM pickup status bit	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
176	–	2274	N/A	PSM timeout status bit	0 = inactive 1 = active	–
177	–	2264	N/A	FTHD/TDD #1 Pole#1 pickup status bit	0 = inactive 1 = active	–
178	–	2274	N/A	FTHD/TDD #1 Pole#1 timeout status bit	0 = inactive 1 = active	–
179	–	1935	N/A	FTHD/TDD #1 Pole#2 pickup status bit	0 = inactive 1 = active	–
180	–	1936	N/A	FTHD/TDD #1 Pole#2 timeout status bit	0 = inactive 1 = active	–
181	–	1935	N/A	FTHD/TDD #2 Pole#1 pickup status bit	0 = inactive 1 = active	–
182	–	1936	N/A	FTHD/TDD #2 Pole#1 timeout status bit	0 = inactive 1 = active	–
183	–	1935	N/A	FTHD/TDD #2 Pole#2 pickup status bit	0 = inactive 1 = active	–
184	–	1936	N/A	FTHD/TDD #2 Pole#2 timeout status bit	0 = inactive 1 = active	–
185	–	1935	N/A	F59I Pole#1 pickup status bit	0 = inactive 1 = active	–
186	–	1936	N/A	F59I Pole#1 timeout status bit	0 = inactive 1 = active	–
187	–	1935	N/A	F59I Pole#2 pickup status bit	0 = inactive 1 = active	–
188	–	1936	N/A	F59I Pole#2 timeout status bit	0 = inactive 1 = active	–
189	–	1935	N/A	FBM#1 pickup status bit	0 = inactive 1 = active	–
190	–	1936	N/A	FBM#1 timeout status bit	0 = inactive 1 = active	–
191	–	1996	N/A	HLT F50P Pole#1 pickup status bit	0 = inactive 1 = active	–
192	–	1997	N/A	HLT F50P Pole#1 timeout status bit	0 = inactive 1 = active	–
193	–	1996	N/A	HLT F50P Pole#2 pickup status bit	0 = inactive 1 = active	–
194	–	1997	N/A	HLT F50P Pole#2 timeout status bit	0 = inactive 1 = active	–
195	–	1996	N/A	HLT F51P Pole#1 pickup status bit	0 = inactive 1 = active	–
196	–	1997	N/A	HLT F51P Pole#1 timeout status bit	0 = inactive 1 = active	–
197	–	1996	N/A	HLT F51P Pole#2 pickup status bit	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
198	–	1997	N/A	HLT F51P Pole#2 timeout status bit	0 = inactive 1 = active	–
199	–	1996	N/A	Sectionalizer Pole#1 pickup status bit	0 = inactive 1 = active	–
200	–	1997	N/A	Sectionalizer Pole#1 timeout status bit	0 = inactive 1 = active	–
201	–	1996	N/A	Sectionalizer Pole#2 pickup status bit	0 = inactive 1 = active	–
202	–	1997	N/A	Sectionalizer Pole#2 timeout status bit	0 = inactive 1 = active	–
203	–	1996	N/A	Close Block Pole#1 pickup status bit	0 = inactive 1 = active	–
204	–	1997	N/A	Close Block Pole#1 timeout status bit	0 = inactive 1 = active	–
205	–	2085	N/A	Close Block Pole#2 pickup status bit	0 = inactive 1 = active	–
206	–	2086	N/A	Close Block Pole#2 timeout status bit	0 = inactive 1 = active	–
207	–	2085	N/A	Loop Scheme Pole#1 pickup status bit	0 = inactive 1 = active	–
208	–	2086	N/A	Loop Scheme Pole#1 timeout status bit	0 = inactive 1 = active	–
209	–	3780	N/A	F25 timeout target status bit	0 = inactive 1 = active	–
210	–	3780	N/A	F27#1 Pole#1 timeout target status bit	0 = inactive 1 = active	–
211	–	3780	N/A	F27#1 Pole#2 timeout target status bit	0 = inactive 1 = active	–
212	–	3780	N/A	F27#2 Pole#1 timeout target status bit	0 = inactive 1 = active	–
213	–	3780	N/A	F27#2 Pole#2 timeout target status bit	0 = inactive 1 = active	–
214	–	3780	N/A	F27#3 Pole#1 timeout target status bit	0 = inactive 1 = active	–
215	–	3780	N/A	F27#3 Pole#2 timeout target status bit	0 = inactive 1 = active	–
216	–	3780	N/A	F27#4 Pole#1 timeout target status bit	0 = inactive 1 = active	–
217	–	3780	N/A	F27#4 Pole#2 timeout target status bit	0 = inactive 1 = active	–
218	–	3781	N/A	F32#1 Pole#1 timeout target status bit	0 = inactive 1 = active	–
219	–	3781	N/A	F32#1 Pole#2 timeout target status bit	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
220	–	3781	N/A	F32#2 Pole#1 timeout target status bit	0 = inactive 1 = active	–
221	–	3781	N/A	F32#2 Pole#2 timeout target status bit	0 = inactive 1 = active	–
222	–	3781	N/A	F32#3 Pole#1 timeout target status bit	0 = inactive 1 = active	–
223	–	3781	N/A	F32#3 Pole#2 timeout target status bit	0 = inactive 1 = active	–
224	–	3781	N/A	F32#4 Pole#1 timeout target status bit	0 = inactive 1 = active	–
225	–	3781	N/A	F32#4 Pole#2 timeout target status bit	0 = inactive 1 = active	–
226	–	3781	N/A	F50P#1 Pole#1 timeout target status bit	0 = inactive 1 = active	–
227	–	3781	N/A	F50P#1 Pole#2 timeout target status bit	0 = inactive 1 = active	–
228	–	3782	N/A	F50P#2 Pole#1 timeout target status bit	0 = inactive 1 = active	–
229	–	3782	N/A	F50P#2 Pole#2 timeout target status bit	0 = inactive 1 = active	–
230	–	3782	N/A	F50P#3 Pole#1 timeout target status bit	0 = inactive 1 = active	–
231	–	3782	N/A	F50P#3 Pole#2 timeout target status bit	0 = inactive 1 = active	–
232	–	3782	N/A	F50P#4 Pole#1 timeout target status bit	0 = inactive 1 = active	–
233	–	3782	N/A	F50P#4 Pole#2 timeout target status bit	0 = inactive 1 = active	–
234	–	3783	N/A	F50BF timeout target status bit	0 = inactive 1 = active	–
235	–	3783	N/A	F51P #1 Pole#1 timeout target status bit	0 = inactive 1 = active	–
236	–	3783	N/A	F51P #1 Pole#2 timeout target status bit	0 = inactive 1 = active	–
237	–	3783	N/A	F51P #2 Pole#1 timeout target status bit	0 = inactive 1 = active	–
238	–	3784	N/A	F51P #2 Pole#2 timeout target status bit	0 = inactive 1 = active	–
239	–	3784	N/A	F51P #3 Pole#1 timeout target status bit	0 = inactive 1 = active	–
240	–	3784	N/A	F51P #3 Pole#2 timeout target status bit	0 = inactive 1 = active	–
241	–	3784	N/A	F51P #4 Pole#1 timeout target status bit	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
242	–	3784	N/A	F51P #4 Pole#2 timeout target status bit	0 = inactive 1 = active	–
243	–	3785	N/A	F59#1 Pole#1 timeout target status bit	0 = inactive 1 = active	–
244	–	3785	N/A	F59#1 Pole#2 timeout target status bit	0 = inactive 1 = active	–
245	–	3785	N/A	F59#2 Pole#1 timeout target status bit	0 = inactive 1 = active	–
246	–	3785	N/A	F59#2 Pole#2 timeout target status bit	0 = inactive 1 = active	–
247	–	3786	N/A	F59#3 Pole#1 timeout target status bit	0 = inactive 1 = active	–
248	–	3786	N/A	F59#3 Pole#2 timeout target status bit	0 = inactive 1 = active	–
249	–	3786	N/A	F59#4 Pole#1 timeout target status bit	0 = inactive 1 = active	–
250	–	3786	N/A	F59#4 Pole#2 timeout target status bit	0 = inactive 1 = active	–
251	–	3788	N/A	TCM timeout target status bit	0 = inactive 1 = active	–
252	–	3788	N/A	CCM timeout target status bit	0 = inactive 1 = active	–
253	–	3788	N/A	F81#1 timeout target status bit	0 = inactive 1 = active	–
254	–	3788	N/A	F81#2 timeout target status bit	0 = inactive 1 = active	–
255	–	3788	N/A	F81#3 timeout target status bit	0 = inactive 1 = active	–
256	–	3789	N/A	F81#4 timeout target status bit	0 = inactive 1 = active	–
257	–	3789	N/A	F81R#1 timeout target status bit	0 = inactive 1 = active	–
258	–	3789	N/A	F81R#2 timeout target status bit	0 = inactive 1 = active	–
259	–	3789	N/A	FCLP Pole#1 timeout target status bit	0 = inactive 1 = active	–
260	–	3789	N/A	IPSLOGIC#1 timeout target status bit	0 = inactive 1 = active	–
261	–	3789	N/A	IPSLOGIC#2 timeout target status bit	0 = inactive 1 = active	–
262	–	3789	N/A	IPSLOGIC#3 timeout target status bit	0 = inactive 1 = active	–
263	–	3789	N/A	IPSLOGIC#4 timeout target status bit	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
264	–	3789	N/A	IPSLOGIC#5 timeout target status bit	0 = inactive 1 = active	–
265	–	3789	N/A	IPSLOGIC#6 timeout target status bit	0 = inactive 1 = active	–
266	–	3789	N/A	IPSLOGIC#7 timeout target status bit	0 = inactive 1 = active	–
267	–	3789	N/A	IPSLOGIC#8 timeout target status bit	0 = inactive 1 = active	–
268	–	3789	N/A	PSM timeout target status bit	0 = inactive 1 = active	–
269	–	3789	N/A	FTHD/TDD #1 Pole#1 timeout target status bit	0 = inactive 1 = active	–
270	–	1939	N/A	FTHD/TDD #1 Pole#2 timeout target status bit	0 = inactive 1 = active	–
271	–	1939	N/A	FTHD/TDD #2 Pole#1 timeout target status bit	0 = inactive 1 = active	–
272	–	1939	N/A	FTHD/TDD #2 Pole#2 timeout target status bit	0 = inactive 1 = active	–
273	–	1939	N/A	F59I Pole#1 timeout target status bit	0 = inactive 1 = active	–
274	–	1939	N/A	F59I Pole#2 timeout target status bit	0 = inactive 1 = active	–
275	–	1939	N/A	FBM #1 timeout target status bit	0 = inactive 1 = active	–
276	–	1992	N/A	HLT F50P Pole#1 timeout target status bit	0 = inactive 1 = active	–
277	–	1992	N/A	HLT F50P Pole#2 timeout target status bit	0 = inactive 1 = active	–
278	–	1992	N/A	HLT F51P Pole#1 timeout target status bit	0 = inactive 1 = active	–
279	–	1992	N/A	HLT F51P Pole#2 timeout target status bit	0 = inactive 1 = active	–
280	–	1992	N/A	Sectionalizer Pole#1 timeout target status bit	0 = inactive 1 = active	–
281	–	1992	N/A	Sectionalizer Pole#2 timeout target status bit	0 = inactive 1 = active	–
282	–	1992	N/A	Close Block Pole#1 timeout target status bit	0 = inactive 1 = active	–
283	–	2089	N/A	Close Block Pole#2 timeout target status bit	0 = inactive 1 = active	–
284	–	2089	N/A	Loop Scheme Pole#1 timeout target status bit	0 = inactive 1 = active	–
285	–	1906	N/A	Trouble State	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
286	–	1906	N/A	Control Status OK	0 = inactive 1 = active	–
287	–	3794	N/A	Recloser Malfunction	0 = inactive 1 = active	–
288	–	1906	N/A	Non-reclosing Active	0 = inactive 1 = active	–
289	–	1906	N/A	Local/Remote State	0 = inactive 1 = active	–
290	–	1906	N/A	Hot Line Tag Active State	0 = inactive 1 = active	–
291	–	1906	N/A	Recloser Closed	0 = inactive 1 = active	–
292	–	1906	N/A	Control Lockout State	0 = inactive 1 = active	–
293	–	1906	N/A	Normal Profile Active	0 = inactive 1 = active	–
294	–	3798	N/A	Battery Discharge test Fail	0 = inactive 1 = active	–
295	–	3798	N/A	Main Power is ON	0 = inactive 1 = active	–
296	–	3798	N/A	Battery Charging ON/OFF	0 = inactive 1 = active	–
297	–	3798	N/A	Battery Charging Board Condition(BCB)	0 = inactive 1 = active	–
298	–	4991	N/A	Charging Mode (boost or float, trickle)	0 = inactive 1 = active	–
299	–	3798	N/A	Battery Present (No batt)	0 = inactive 1 = active	–
300	–	2139	N/A	LED 1 RED Front Panel Status	0 = inactive 1 = active	–
301	–	2140	N/A	LED 2 RED Front Panel Status	0 = inactive 1 = active	–
302	–	2141	N/A	LED 3 RED Front Panel Status	0 = inactive 1 = active	–
303	–	2142	N/A	LED 4 RED Front Panel Status	0 = inactive 1 = active	–
304	–	2143	N/A	LED 5 RED Front Panel Status	0 = inactive 1 = active	–
305	–	2144	N/A	LED 6 RED Front Panel Status	0 = inactive 1 = active	–
306	–	2145	N/A	LED 7 RED Front Panel Status	0 = inactive 1 = active	–
307	–	2146	N/A	LED 8 RED Front Panel Status	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
308	–	2147	N/A	LED 9 RED Front Panel Status	0 = inactive 1 = active	–
309	–	2148	N/A	LED 10 RED Front Panel Status	0 = inactive 1 = active	–
310	–	2149	N/A	LED 11 RED Front Panel Status	0 = inactive 1 = active	–
311	–	2150	N/A	LED 12 RED Front Panel Status	0 = inactive 1 = active	–
312	–	2151	N/A	LED 13 RED Front Panel Status	0 = inactive 1 = active	–
313	–	2152	N/A	LED 14 RED Front Panel Status	0 = inactive 1 = active	–
314	–	2153	N/A	LED 15 RED Front Panel Status	0 = inactive 1 = active	–
315	–	2154	N/A	LED 16 RED Front Panel Status	0 = inactive 1 = active	–
316	–	2155	N/A	LED 17 RED Front Panel Status	0 = inactive 1 = active	–
317	–	2156	N/A	LED 18 RED Front Panel Status	0 = inactive 1 = active	–
318	–	2157	N/A	LED 19 RED Front Panel Status	0 = inactive 1 = active	–
319	–	2158	N/A	LED 20 RED Front Panel Status	0 = inactive 1 = active	–
320	–	2159	N/A	LED 21 RED Front Panel Status	0 = inactive 1 = active	–
321	–	2160	N/A	LED 22 RED Front Panel Status	0 = inactive 1 = active	–
322	–	2161	N/A	LED 23 RED Front Panel Status	0 = inactive 1 = active	–
323	–	2139	N/A	LED 1 GREEN Front Panel Status	0 = inactive 1 = active	–
324	–	2140	N/A	LED 2 GREEN Front Panel Status	0 = inactive 1 = active	–
325	–	2141	N/A	LED 3 GREEN Front Panel Status	0 = inactive 1 = active	–
326	–	2142	N/A	LED 4 GREEN Front Panel Status	0 = inactive 1 = active	–
327	–	2143	N/A	LED 5 GREEN Front Panel Status	0 = inactive 1 = active	–
328	–	2144	N/A	LED 6 GREEN Front Panel Status	0 = inactive 1 = active	–
329	–	2145	N/A	LED 7 GREEN Front Panel Status	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
330	–	2146	N/A	LED 8 GREEN Front Panel Status	0 = inactive 1 = active	–
331	–	2147	N/A	LED 9 GREEN Front Panel Status	0 = inactive 1 = active	–
332	–	2148	N/A	LED 10 GREEN Front Panel Status	0 = inactive 1 = active	–
333	–	2149	N/A	LED 11 GREEN Front Panel Status	0 = inactive 1 = active	–
334	–	2150	N/A	LED 12 GREEN Front Panel Status	0 = inactive 1 = active	–
335	–	2151	N/A	LED 13 GREEN Front Panel Status	0 = inactive 1 = active	–
336	–	2152	N/A	LED 14 GREEN Front Panel Status	0 = inactive 1 = active	–
337	–	2153	N/A	LED 15 GREEN Front Panel Status	0 = inactive 1 = active	–
338	–	2154	N/A	LED 16 GREEN Front Panel Status	0 = inactive 1 = active	–
339	–	2155	N/A	LED 17 GREEN Front Panel Status	0 = inactive 1 = active	–
340	–	2156	N/A	LED 18 GREEN Front Panel Status	0 = inactive 1 = active	–
341	–	2157	N/A	LED 19 GREEN Front Panel Status	0 = inactive 1 = active	–
342	–	2158	N/A	LED 20 GREEN Front Panel Status	0 = inactive 1 = active	–
343	–	2159	N/A	LED 21 GREEN Front Panel Status	0 = inactive 1 = active	–
344	–	2160	N/A	LED 22 GREEN Front Panel Status	0 = inactive 1 = active	–
345	–	2161	N/A	LED 23 GREEN Front Panel Status	0 = inactive 1 = active	–
346	–	2139	N/A	LED 1 AMBER Front Panel Status	0 = inactive 1 = active	–
347	–	2140	N/A	LED 2 AMBER Front Panel Status	0 = inactive 1 = active	–
348	–	2141	N/A	LED 3 AMBER Front Panel Status	0 = inactive 1 = active	–
349	–	2142	N/A	LED 4 AMBER Front Panel Status	0 = inactive 1 = active	–
350	–	2143	N/A	LED 5 AMBER Front Panel Status	0 = inactive 1 = active	–
351	–	2144	N/A	LED 6 AMBER Front Panel Status	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
352	–	2145	N/A	LED 7 AMBER Front Panel Status	0 = inactive 1 = active	–
353	–	2146	N/A	LED 8 AMBER Front Panel Status	0 = inactive 1 = active	–
354	–	2147	N/A	LED 9 AMBER Front Panel Status	0 = inactive 1 = active	–
355	–	2148	N/A	LED 10 AMBER Front Panel Status	0 = inactive 1 = active	–
356	–	2149	N/A	LED 11 AMBER Front Panel Status	0 = inactive 1 = active	–
357	–	2150	N/A	LED 12 AMBER Front Panel Status	0 = inactive 1 = active	–
358	–	2151	N/A	LED 13 AMBER Front Panel Status	0 = inactive 1 = active	–
359	–	2152	N/A	LED 14 AMBER Front Panel Status	0 = inactive 1 = active	–
360	–	2153	N/A	LED 15 AMBER Front Panel Status	0 = inactive 1 = active	–
361	–	2154	N/A	LED 16 AMBER Front Panel Status	0 = inactive 1 = active	–
362	–	2155	N/A	LED 17 AMBER Front Panel Status	0 = inactive 1 = active	–
363	–	2156	N/A	LED 18 AMBER Front Panel Status	0 = inactive 1 = active	–
364	–	2157	N/A	LED 19 AMBER Front Panel Status	0 = inactive 1 = active	–
365	–	2158	N/A	LED 20 AMBER Front Panel Status	0 = inactive 1 = active	–
366	–	2159	N/A	LED 21 AMBER Front Panel Status	0 = inactive 1 = active	–
367	–	2160	N/A	LED 22 AMBER Front Panel Status	0 = inactive 1 = active	–
368	–	2161	N/A	LED 23 AMBER Front Panel Status	0 = inactive 1 = active	–
369	–	4837	N/A	Sectionalizer Initiate Restraint Status Pole#1	0 = inactive 1 = active	–
370	–	4838	N/A	Sectionalizer Initiate Restraint Status Pole#2	0 = inactive 1 = active	–
371	–	4837	N/A	Sectionalizer Current Restraint Status Pole#1	0 = inactive 1 = active	–
372	–	4838	N/A	Sectionalizer Current Restraint Status Pole#2	0 = inactive 1 = active	–
373	–	3932	N/A	HLT active via IPSlogic	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
374	–	3932	N/A	HLT active via Communications	0 = inactive 1 = active	–
375	–	3932	N/A	HLT active via Local	0 = inactive 1 = active	–
376	–	2050	N/A	Fail to Trip Pole#1	0 = inactive 1 = active	–
377	–	2050	N/A	Fail to Trip Pole#2	0 = inactive 1 = active	–
378	–	2050	N/A	Fail to Close Pole#1	0 = inactive 1 = active	–
379	–	2050	N/A	Fail to Close Pole#2	0 = inactive 1 = active	–
380	–	2052	N/A	Ethernet Link Lost Port 2	0 = inactive 1 = active	–
381	–	2052	N/A	Ethernet Link Lost Port 3	0 = inactive 1 = active	–
382	–	3798	N/A	BECO Drive Not Ready	0 = inactive 1 = active	–
383	–	3798	N/A	BECO Drive Status	0 = inactive 1 = active	–
384	–	2052	N/A	Control Status	0 = inactive 1 = active	–
385	–	4555	N/A	Instantaneous Trip Lockout Pole#1	0 = inactive 1 = active	–
386	–	4570	N/A	Instantaneous Trip Lockout Pole#2	0 = inactive 1 = active	–
387	–	4864	N/A	Pole Failure Terminal#1 (Input 1 and Input 2)	0 = inactive 1 = active	–
388	–	4864	N/A	Pole Failure Terminal#2 (Input 6 and Input 7)	0 = inactive 1 = active	–
389	–	4864	N/A	Pole Failure Terminal#3 (Input 8 and Input 9)	0 = inactive 1 = active	–
390	–	4864	N/A	Control Circuit Interrupted Terminal#1 (Input 1 and Input 2)	0 = inactive 1 = active	–
391	–	4864	N/A	Control Circuit Interrupted Terminal#2 (Input 6 and Input 7)	0 = inactive 1 = active	–
392	–	4864	N/A	Control Circuit Interrupted Terminal#3 (Input 8 and Input 9)	0 = inactive 1 = active	–
393	–	4555	N/A	Cold Load Pickup Status Pole#1	0 = inactive 1 = active	–
394	–	4570	N/A	Cold Load Pickup Status Pole#2	0 = inactive 1 = active	–
395	–	2252	N/A	Control General Alarm	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
396	–	2054	N/A	Above OC Pickup	0 = inactive 1 = active	–
397	–	2076	N/A	Reclose in process Pole#1	0 = inactive 1 = active	–
398	–	2077	N/A	Reclose in process Pole#2	0 = inactive 1 = active	–
399	–	2006	N/A	Sectionalizer Count in process Pole#1	0 = inactive 1 = active	–
400	–	2007	N/A	Sectionalizer Count in process Pole#2	0 = inactive 1 = active	–
401	–	2050	N/A	Close Block Override Active Pole#1	0 = inactive 1 = active	–
402	–	2050	N/A	Close Block Override Active Pole#2	0 = inactive 1 = active	–
403	–	2050	N/A	Inrush Restraint Pickup	0 = inactive 1 = active	–
404	–	2050	N/A	Inrush Restraint Timeout	0 = inactive 1 = active	–
405	–	4818	N/A	Phase Slow Curve Active Pole#1	0 = inactive 1 = active	–
406	–	4818	N/A	Phase Fast Curve Active Pole#1	0 = inactive 1 = active	–
407	–	4819	N/A	Phase Slow Curve Active Pole#2	0 = inactive 1 = active	–
408	–	4819	N/A	Phase Fast Curve Active Pole#2	0 = inactive 1 = active	–
409	–	1419	N/A	Slow Elements Only (TSlow) Pole#1	0 = inactive 1 = active	–
410	–	1420	N/A	Slow Elements Only (TSlow) Pole#2	0 = inactive 1 = active	–
411	–	3926	N/A	Button 10 Status	0 = inactive 1 = active	–
412	–	3926	N/A	Button 11 Status	0 = inactive 1 = active	–
413	–	3926	N/A	Button 12 Status	0 = inactive 1 = active	–
414	–	3926	N/A	Button 13 Status	0 = inactive 1 = active	–
415	–	3926	N/A	Button 14 Status	0 = inactive 1 = active	–
416	–	3926	N/A	Button 15 Status	0 = inactive 1 = active	–
417	–	3926	N/A	Button 16 Status	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
418	–	3926	N/A	Button 17 Status	0 = inactive 1 = active	–
419	–	2096	N/A	Button 10 Toggle State	0 = inactive 1 = active	–
420	–	2096	N/A	Button 11 Toggle State	0 = inactive 1 = active	–
421	–	2096	N/A	Button 12 Toggle State	0 = inactive 1 = active	–
422		2096	N/A	Button 13 Toggle State	0 = inactive 1 = active	–
423	–	2096	N/A	Button 14 Toggle State	0 = inactive 1 = active	–
424		2096	N/A	Button 15 Toggle State	0 = inactive 1 = active	–
425	–	2096	N/A	Button 16 Toggle State	0 = inactive 1 = active	–
426	–	2096	N/A	Button 17 Toggle State	0 = inactive 1 = active	–
427	–	1538	N/A	Button 10 Function Status	0 = inactive 1 = active	–
428	–	1538	N/A	Button 11 Function Status	0 = inactive 1 = active	–
429	–	1538	N/A	Button 12 Function Status	0 = inactive 1 = active	–
430	–	1538	N/A	Button 13 Function Status	0 = inactive 1 = active	–
431	–	1538	N/A	Button 14 Function Status	0 = inactive 1 = active	–
432	–	1538	N/A	Button 15 Function Status	0 = inactive 1 = active	–
433	–	1538	N/A	Button 16 Function Status	0 = inactive 1 = active	–
434	–	1538	N/A	Button 17 Function Status	0 = inactive 1 = active	–
435	–	4700	N/A	Loop Scheme Pole#1 DeadLineSens Vy Pickup Status	0 = inactive 1 = active	–
436	–	4700	N/A	Loop Scheme Pole#1 DeadLineSens Vy Timeout Status	0 = inactive 1 = active	–
437	–	4700	N/A	Loop Scheme Pole#1 DeadLineSens Vz Pickup Status	0 = inactive 1 = active	–
438	–	4700	N/A	Loop Scheme Pole#1 DeadLineSens Vz Timeout Status	0 = inactive 1 = active	–
439	–	4700	N/A	Loop Scheme Pole#1 Voltage Restore Vy Pickup Status	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
440	–	4700	N/A	Loop Scheme Pole#1 LiveLineSens Vy Timeout Status	0 = inactive 1 = active	–
441	–	4700	N/A	Loop Scheme Pole#1 Voltage Restore Vz Pickup Status	0 = inactive 1 = active	–
442	–	4700	N/A	Loop Scheme Pole#1 LiveLineSens Vz Timeout Status	0 = inactive 1 = active	–
443	–	4700	N/A	Loop Scheme Pole#1 DeadLine Y Pickup Status	0 = inactive 1 = active	–
444	–	4700	N/A	Loop Scheme Pole#1 DeadLine Y Timeout Status	0 = inactive 1 = active	–
445	–	4700	N/A	Loop Scheme Pole#1 DeadLine Z Pickup Status	0 = inactive 1 = active	–
446	–	4700	N/A	Loop Scheme Pole#1 DeadLine Z Timeout Status	0 = inactive 1 = active	–
447	–	4700	N/A	Loop Scheme Pole#1 LiveLine Y Pickup Status	0 = inactive 1 = active	–
448	–	4700	N/A	Loop Scheme Pole#1 LiveLineWindow Y Timeout Status	0 = inactive 1 = active	–
449	–	4700	N/A	Loop Scheme Pole#1 LiveLine Z Pickup Status	0 = inactive 1 = active	–
450	–	4700	N/A	Loop Scheme Pole#1 LiveLineWindow Z Timeout Status	0 = inactive 1 = active	–
451	–	4701	N/A	Loop Scheme Pole#1 DeadLine Y Action Executed	0 = inactive 1 = active	–
452	–	4701	N/A	Loop Scheme Pole#1 DeadLine Z Action Executed	0 = inactive 1 = active	–
453	–	4701	N/A	Loop Scheme Pole#1 LiveLine Y Action Executed	0 = inactive 1 = active	–
454	–	4701	N/A	Loop Scheme Pole#1 LiveLine Z Action Executed	0 = inactive 1 = active	–
455	–	4701	N/A	Loop Scheme Pole#1 Action Breaker Trip	0 = inactive 1 = active	–
456	–	4701	N/A	Loop Scheme Pole#1 Action Breaker Close	0 = inactive 1 = active	–
457	–	4701	N/A	Loop Scheme Pole#1 After Action Reclosing Block	0 = inactive 1 = active	–
458	–	4701	N/A	Loop Scheme Pole#1 After Action Profile Switch	0 = inactive 1 = active	–
459	–	4701	N/A	Loop Scheme Pole#1 Reset	0 = inactive 1 = active	–
460	–	4701	N/A	Loop Scheme Pole#1 LiveLineConfirm Vy Timeout Status	0 = inactive 1 = active	–
461	–	4701	N/A	Loop Scheme Pole#1 LiveLineConfirm Vz Timeout Status	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
462	–	4691	N/A	Loop Scheme Pole#1 Sectionalizer Mode	0 = inactive 1 = active	–
463	–	4691	N/A	Loop Scheme Pole#1 Tie Mode	0 = inactive 1 = active	–
464	–	4691	N/A	Loop Scheme Pole#1 Midpoint Mode	0 = inactive 1 = active	–
465	–	4691	N/A	Loop Scheme Pole#1 Half LS Vy Direction Selected	0 = inactive 1 = active	–
466	–	4691	N/A	Loop Scheme Pole#1 Half LS Vz Direction Selected	0 = inactive 1 = active	–
467	–	4691	N/A	Loop Scheme Pole#1 Full LS Selected	0 = inactive 1 = active	–
468	–	4701	N/A	Loop Scheme Pole#1 Ready	0 = inactive 1 = active	–
469	–	4691	N/A	Loop Scheme Pole#1 Wait for Reset	0 = inactive 1 = active	–
470	–	4691	N/A	Loop Scheme Pole#1 Status Lockout	0 = inactive 1 = active	–
471	–	4691	N/A	Loop Scheme Pole#1 Dual Voltage Loss	0 = inactive 1 = active	–
472	–	4691	N/A	Loop Scheme Pole#1 LiveLine Action Block	0 = inactive 1 = active	–
473	–	4691	N/A	Loop Scheme Pole#1 Auto Reset Operated	0 = inactive 1 = active	–
474	–	4691	N/A	Loop Scheme Pole#1 Mid point Fwd Power Detection	0 = inactive 1 = active	–
475	–	4691	N/A	Loop Scheme Pole#1 Tie Directional Power Detection	0 = inactive 1 = active	–
476	–	4824	N/A	Loop Scheme Pole#2 DeadLineSens Vy Pickup Status	0 = inactive 1 = active	–
477	–	4824	N/A	Loop Scheme Pole#2 DeadLineSens Vy Timeout Status	0 = inactive 1 = active	–
478	–	4824	N/A	Loop Scheme Pole#2 DeadLineSens Vz Pickup Status	0 = inactive 1 = active	–
479	–	4824	N/A	Loop Scheme Pole#2 DeadLineSens Vz Timeout Status	0 = inactive 1 = active	–
480	–	4824	N/A	Loop Scheme Pole#2 Voltage Restore Vy Pickup Status	0 = inactive 1 = active	–
481	–	4824	N/A	Loop Scheme Pole#2 LiveLineSens Vy Timeout Status	0 = inactive 1 = active	–
482	–	4824	N/A	Loop Scheme Pole#2 Voltage Restore Vz Pickup Status	0 = inactive 1 = active	–
483	–	4824	N/A	Loop Scheme Pole#2 LiveLineSens Vz Timeout Status	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
484	–	4824	N/A	Loop Scheme Pole#2 DeadLine Y Pickup Status	0 = inactive 1 = active	–
485	–	4824	N/A	Loop Scheme Pole#2 DeadLine Y Timeout Status	0 = inactive 1 = active	–
486	–	4824	N/A	Loop Scheme Pole#2 DeadLine Z Pickup Status	0 = inactive 1 = active	–
487	–	4824	N/A	Loop Scheme Pole#2 DeadLine Z Timeout Status	0 = inactive 1 = active	–
488	–	4824	N/A	Loop Scheme Pole#2 LiveLine Y Pickup Status	0 = inactive 1 = active	–
489	–	4824	N/A	Loop Scheme Pole#2 LiveLineWindow Y Timeout Status	0 = inactive 1 = active	–
490	–	4824	N/A	Loop Scheme Pole#2 LiveLine Z Pickup Status	0 = inactive 1 = active	–
491	–	4824	N/A	Loop Scheme Pole#2 LiveLineWindow Z Timeout Status	0 = inactive 1 = active	–
492	–	4824	N/A	Loop Scheme Pole#2 DeadLine Y Action Executed	0 = inactive 1 = active	–
493	–	4825	N/A	Loop Scheme Pole#2 DeadLine Z Action Executed	0 = inactive 1 = active	–
494	–	4825	N/A	Loop Scheme Pole#2 LiveLine Y Action Executed	0 = inactive 1 = active	–
495	–	4825	N/A	Loop Scheme Pole#2 LiveLine Z Action Executed	0 = inactive 1 = active	–
496	–	4825	N/A	Loop Scheme Pole#2 Action Breaker Trip	0 = inactive 1 = active	–
497	–	4825	N/A	Loop Scheme Pole#2 Action Breaker Close	0 = inactive 1 = active	–
498	–	4825	N/A	Loop Scheme Pole#2 After Action Reclosing Block	0 = inactive 1 = active	–
499	–	4825	N/A	Loop Scheme Pole#2 After Action Profile Switch	0 = inactive 1 = active	–
500	–	4825	N/A	Loop Scheme Pole#2 Reset	0 = inactive 1 = active	–
501	–	4825	N/A	Loop Scheme Pole#2 LiveLineConfirm Vy Timeout Status	0 = inactive 1 = active	–
502	–	4825	N/A	Loop Scheme Pole#2 LiveLineConfirm Vz Timeout Status	0 = inactive 1 = active	–
503	–	4826	N/A	Loop Scheme Pole#2 Sectionalizer Mode	0 = inactive 1 = active	–
504	–	4826	N/A	Loop Scheme Pole#2 Tie Mode	0 = inactive 1 = active	–
505	–	4826	N/A	Loop Scheme Pole#2 Midpoint Mode	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
506	–	4826	N/A	Loop Scheme Pole#2 Half LS Vy Direction Selected	0 = inactive 1 = active	–
507	–	4826	N/A	Loop Scheme Pole#2 Half LS Vz Direction Selected	0 = inactive 1 = active	–
508	–	4826	N/A	Loop Scheme Pole#2 Full LS Selected	0 = inactive 1 = active	–
509	–	4825	N/A	Loop Scheme Pole#2 Ready	0 = inactive 1 = active	–
510	–	4826	N/A	Loop Scheme Pole#2 Wait for Reset	0 = inactive 1 = active	–
511	–	4826	N/A	Loop Scheme Pole#2 Status Lockout	0 = inactive 1 = active	–
512	–	4826	N/A	Loop Scheme Pole#2 Dual Voltage Loss	0 = inactive 1 = active	–
513	–	4826	N/A	Loop Scheme Pole#2 LiveLine Action Block	0 = inactive 1 = active	–
514	–	4826	N/A	Loop Scheme Pole#2 Auto Reset Operated	0 = inactive 1 = active	–
515	–	4826	N/A	Loop Scheme Pole#2 Mid Point Fwd Power Detection	0 = inactive 1 = active	–
516		4826	N/A	Loop Scheme Pole#2 Tie Directional Power Detection	0 = inactive 1 = active	–
517	–	4820	N/A	HCA Pole#1	0 = inactive 1 = active	–
518	–	4820	N/A	LOS Pole#1 Initiate Pickup	0 = inactive 1 = active	–
519	–	4820	N/A	LOS Pole#1 Initiate Timeout	0 = inactive 1 = active	–
520	–	4820	N/A	LOS Pole#1 Reset Pickup	0 = inactive 1 = active	–
521	–	4820	N/A	LOS Pole#1 Reset Timeout	0 = inactive 1 = active	–
522	–	4820	N/A	CLP Pole#1 Arm Pickup	0 = inactive 1 = active	–
523	–	4820	N/A	CLP Pole#1 Arm Timeout	0 = inactive 1 = active	–
524	–	4820	N/A	CLP Pole#1 Override Active Pickup	0 = inactive 1 = active	–
525	–	4820	N/A	CLP Pole#1 Override Active Timeout	0 = inactive 1 = active	–
526	–	4820	N/A	CLP Pole#1 Low Current Pickup	0 = inactive 1 = active	–
527	–	4820	N/A	CLP Pole#1 Low Current Timeout	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
528	—	4821	N/A	HCA Pole#2	0 = inactive 1 = active	—
529	—	4821	N/A	LOS Pole#2 Initiate Pickup	0 = inactive 1 = active	—
530	—	4821	N/A	LOS Pole#2 Initiate Timeout	0 = inactive 1 = active	—
531	—	4821	N/A	LOS Pole#2 Reset Pickup	0 = inactive 1 = active	—
532	—	4821	N/A	LOS Pole#2 Reset Timeout	0 = inactive 1 = active	—
533	—	4821	N/A	CLP Pole#2 Arm Pickup	0 = inactive 1 = active	—
534	—	4821	N/A	CLP Pole#2 Arm Timeout	0 = inactive 1 = active	—
535	—	4821	N/A	CLP Pole#2 Override Active Pickup	0 = inactive 1 = active	—
536	—	4821	N/A	CLP Pole#2 Override Active Timeout	0 = inactive 1 = active	—
537	—	4821	N/A	CLP Pole#2 Low Current Pickup	0 = inactive 1 = active	—
538	—	4821	N/A	CLP Pole#2 Low Current Timeout	0 = inactive 1 = active	—
539	—	2085	N/A	GlobalLOGIC#1 pickup status bit	0 = inactive 1 = active	—
540	—	2086	N/A	GlobalLOGIC#1 timeout status bit	0 = inactive 1 = active	—
541	—	2085	N/A	GlobalLOGIC#2 pickup status bit	0 = inactive 1 = active	—
542	—	2086	N/A	GlobalLOGIC#2 timeout status bit	0 = inactive 1 = active	—
543	—	2089	N/A	GlobalLOGIC#3 pickup status bit	0 = inactive 1 = active	—
544	—	2086	N/A	GlobalLOGIC#3 timeout status bit	0 = inactive 1 = active	—
545	—	2085	N/A	GlobalLOGIC#4 pickup status bit	0 = inactive 1 = active	—
546	—	2086	N/A	GlobalLOGIC#4 timeout status bit	0 = inactive 1 = active	—
547	—	2085	N/A	GlobalLOGIC#5 pickup status bit	0 = inactive 1 = active	—
548	—	2086	N/A	GlobalLOGIC#5 timeout status bit	0 = inactive 1 = active	—
549	—	2085	N/A	GlobalLOGIC#6 pickup status bit	0 = inactive 1 = active	—

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
550	—	2086	N/A	GlobalLOGIC#6 timeout status bit	0 = inactive 1 = active	—
551	—	2085	N/A	GlobalLOGIC#7 pickup status bit	0 = inactive 1 = active	—
552	—	2086	N/A	GlobalLOGIC#7 timeout status bit	0 = inactive 1 = active	—
553	—	2085	N/A	GlobalLOGIC#8 pickup status bit	0 = inactive 1 = active	—
554	—	2086	N/A	GlobalLOGIC#8 timeout status bit	0 = inactive 1 = active	—
555	—	2089	N/A	GlobalLOGIC#1 timeout target status bit	0 = inactive 1 = active	—
556	—	2089	N/A	GlobalLOGIC#2 timeout target status bit	0 = inactive 1 = active	—
557	—	2089	N/A	GlobalLOGIC#3 timeout target status bit	0 = inactive 1 = active	—
558	—	2089	N/A	GlobalLOGIC#4 timeout target status bit	0 = inactive 1 = active	—
559	—	2089	N/A	GlobalLOGIC#5 timeout target status bit	0 = inactive 1 = active	—
560	—	2089	N/A	GlobalLOGIC#6 timeout target status bit	0 = inactive 1 = active	—
561	—	2089	N/A	GlobalLOGIC#7 timeout target status bit	0 = inactive 1 = active	—
562	—	2089	N/A	GlobalLOGIC#8 timeout target status bit	0 = inactive 1 = active	—
563	—	46658	N/A	Sectionalizer Pole#1 SCM Enabled	0 = inactive 1 = active	—
564	—	46684	N/A	Sectionalizer Pole#2 SCM Enabled	0 = inactive 1 = active	—
565	—	4771	N/A	Pole#1 OC Trip	0 = inactive 1 = active	—
566	—	4771	N/A	Pole#2 OC Trip	0 = inactive 1 = active	—
567	—	4802	N/A	Fault Latch	0 = inactive 1 = active	—
568	—	3917	N/A	Reclose Disable	0 = inactive 1 = active	—
569	—	4702	N/A	79U Pole#1 pickup status bit	0 = inactive 1 = active	—
570	—	4702	N/A	79U Pole#2 pickup status bit	0 = inactive 1 = active	—
571	—	4702	N/A	79U F51 Pole#1 pickup status bit	0 = inactive 1 = active	—

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
572	—	4702	N/A	79U F51 Pole#2 pickup status bit	0 = inactive 1 = active	—
573	—	4702	N/A	79U F50HCT Pole#1 pickup status bit	0 = inactive 1 = active	—
574	—	4702	N/A	79U F50HCT Pole#2 pickup status bit	0 = inactive 1 = active	—
575	—	4702	N/A	79U F50HCL Pole#1 pickup status bit	0 = inactive 1 = active	—
576	—	4804	N/A	79U F50HCL Pole#2 pickup status bit	0 = inactive 1 = active	—
577	—	4804	N/A	F25 Pole#2 pickup status bit	0 = inactive 1 = active	—
578	—	4804	N/A	F27BSVS#1 pickup status bit	0 = inactive 1 = active	—
579	—	4804	N/A	F27BSVS#2 pickup status bit	0 = inactive 1 = active	—
580	—	4804	N/A	F50BF#2 pickup status bit	0 = inactive 1 = active	—
581	—	4804	N/A	FCLP Pole#2 pickup status bit	0 = inactive 1 = active	—
582	—	4804	N/A	Loop Scheme Pole#2 pickup status bit	0 = inactive 1 = active	—
583	—	4804	N/A	LCP Pole#1 pickup status bit	0 = inactive 1 = active	—
584	—	4804	N/A	LCP Pole#2 pickup status bit	0 = inactive 1 = active	—
585	—	4804	N/A	LOS Pole#1 pickup status bit	0 = inactive 1 = active	—
586	—	4804	N/A	LOS Pole#2 pickup status bit	0 = inactive 1 = active	—
587	—	4804	N/A	HCA Pole#1 pickup status bit	0 = inactive 1 = active	—
588	—	4804	N/A	HCA Pole#2 pickup status bit	0 = inactive 1 = active	—
589	—	4804	N/A	F81#5 pickup status bit	0 = inactive 1 = active	—
590	—	4805	N/A	F81#6 pickup status bit	0 = inactive 1 = active	—
591	—	4805	N/A	Sect Pole#1 27 pickup status bit	0 = inactive 1 = active	—
592	—	4805	N/A	Sect Pole#2 27 pickup status bit	0 = inactive 1 = active	—
593	—	4805	N/A	Sect Pole#1 50P pickup status bit	0 = inactive 1 = active	—

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
594	—	4805	N/A	Sect Pole#2 50P pickup status bit	0 = inactive 1 = active	—
595	—	4805	N/A	Sect Pole#1 51P pickup status bit	0 = inactive 1 = active	—
596	—	4805	N/A	Sect Pole#2 51P pickup status bit	0 = inactive 1 = active	—
597	—	4805	N/A	Sect Pole#1 59 pickup status bit	0 = inactive 1 = active	—
598	—	4805	N/A	Sect Pole#2 59 pickup status bit	0 = inactive 1 = active	—
599	—	4805	N/A	F81#7 pickup status bit	0 = inactive 1 = active	—
600	—	4805	N/A	F81#8 pickup status bit	0 = inactive 1 = active	—
601	—	4805	N/A	F81R#3 pickup status bit	0 = inactive 1 = active	—
602	—	4847	N/A	F81R#4 pickup status bit	0 = inactive 1 = active	—
603	—	4847	N/A	F81R#5 pickup status bit	0 = inactive 1 = active	—
604	—	4847	N/A	F81R#6 pickup status bit	0 = inactive 1 = active	—
605	—	4847	N/A	F81R#7 pickup status bit	0 = inactive 1 = active	—
606	—	4847	N/A	F81R#8 pickup status bit	0 = inactive 1 = active	—
607	—	4847	N/A	FBM #2 pickup status bit	0 = inactive 1 = active	—
608	—	4703	N/A	79U Pole#1 timeout status bit	0 = inactive 1 = active	—
609	—	4703	N/A	79U Pole#2 timeout status bit	0 = inactive 1 = active	—
610	—	4703	N/A	79U F51 Pole#1 timeout status bit	0 = inactive 1 = active	—
611	—	4703	N/A	79U F51 Pole#2 timeout status bit	0 = inactive 1 = active	—
612	—	4703	N/A	79U F50HCT Pole#1 timeout status bit	0 = inactive 1 = active	—
613	—	4703	N/A	79U F50HCT Pole#2 timeout status bit	0 = inactive 1 = active	—
614	—	4703	N/A	79U F50HCL Pole#1 timeout status bit	0 = inactive 1 = active	—
615	—	4806	N/A	79U F50HCL Pole#2 timeout status bit	0 = inactive 1 = active	—

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
616	–	4806	N/A	F25 Pole#2 timeout status bit	0 = inactive 1 = active	–
617	–	4806	N/A	F27BSVS#1 timeout status bit	0 = inactive 1 = active	–
618	–	4806	N/A	F27BSVS#2 timeout status bit	0 = inactive 1 = active	–
619	–	4806	N/A	F50BF#2 timeout status bit	0 = inactive 1 = active	–
620	–	4806	N/A	FCLP Pole#2 timeout status bit	0 = inactive 1 = active	–
621	–	4806	N/A	Loop Scheme Pole#2 timeout status bit	0 = inactive 1 = active	–
622	–	4806	N/A	LCP Pole#1 timeout status bit	0 = inactive 1 = active	–
623	–	4806	N/A	LCP Pole#2 timeout status bit	0 = inactive 1 = active	–
624	–	4806	N/A	LOS Pole#1 timeout status bit	0 = inactive 1 = active	–
625	–	4806	N/A	LOS Pole#2 timeout status bit	0 = inactive 1 = active	–
626	–	4806	N/A	HCA Pole#1 timeout status bit	0 = inactive 1 = active	–
627	–	4806	N/A	HCA Pole#2 timeout status bit	0 = inactive 1 = active	–
628	–	4806	N/A	F81#5 timeout status bit	0 = inactive 1 = active	–
629	–	4807	N/A	F81#6 timeout status bit	0 = inactive 1 = active	–
630	–	4807	N/A	Sect Pole#1 27 timeout status bit	0 = inactive 1 = active	–
631	–	4807	N/A	Sect Pole#2 27 timeout status bit	0 = inactive 1 = active	–
632	–	4807	N/A	Sect Pole#1 50P timeout status bit	0 = inactive 1 = active	–
633	–	4807	N/A	Sect Pole#2 50P timeout status bit	0 = inactive 1 = active	–
634	–	4807	N/A	Sect Pole#1 51P timeout status bit	0 = inactive 1 = active	–
635	–	4807	N/A	Sect Pole#2 51P timeout status bit	0 = inactive 1 = active	–
636	–	4807	N/A	Sect Pole#1 59 timeout status bit	0 = inactive 1 = active	–
637	–	4807	N/A	Sect Pole#2 59 timeout status bit	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
638	–	4807	N/A	F81#7 timeout status bit	0 = inactive 1 = active	–
639	–	4807	N/A	F81#8 timeout status bit	0 = inactive 1 = active	–
640	–	4807	N/A	F81R#3 timeout status bit	0 = inactive 1 = active	–
641	–	4849	N/A	F81R#4 timeout status bit	0 = inactive 1 = active	–
642	–	4849	N/A	F81R#5 timeout status bit	0 = inactive 1 = active	–
643	–	4849	N/A	F81R#6 timeout status bit	0 = inactive 1 = active	–
644	–	4849	N/A	F81R#7 timeout status bit	0 = inactive 1 = active	–
645	–	4849	N/A	F81R#8 timeout status bit	0 = inactive 1 = active	–
646	–	4849	N/A	FBM #2 timeout status bit	0 = inactive 1 = active	–
647	–	4706	N/A	79U Pole#1 timeout target status bit	0 = inactive 1 = active	–
648	–	4706	N/A	79U Pole#2 timeout target status bit	0 = inactive 1 = active	–
649	–	4706	N/A	79U F51 Pole#1 timeout target status bit	0 = inactive 1 = active	–
650	–	4706	N/A	79U F51 Pole#2 timeout target status bit	0 = inactive 1 = active	–
651	–	4706	N/A	79U F50HCT Pole#1 timeout target status bit	0 = inactive 1 = active	–
652	–	4706	N/A	79U F50HCT Pole#2 timeout target status bit	0 = inactive 1 = active	–
653	–	4706	N/A	79U F50HCL Pole#1 timeout target status bit	0 = inactive 1 = active	–
654	–	4812	N/A	79U F50HCL Pole#2 timeout target status bit	0 = inactive 1 = active	–
655	–	4812	N/A	F25 Pole#2 timeout target status bit	0 = inactive 1 = active	–
656	–	4812	N/A	F27BSVS#1 timeout target status bit	0 = inactive 1 = active	–
657	–	4812	N/A	F27BSVS#2 timeout target status bit	0 = inactive 1 = active	–
658	–	4812	N/A	F50BF#2 timeout target status bit	0 = inactive 1 = active	–
659	–	4812	N/A	FCLP Pole#2 timeout target status bit	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
660	–	4812	N/A	Loop Scheme Pole#2 timeout target status bit	0 = inactive 1 = active	–
661	–	4812	N/A	LCP Pole#1 timeout target status bit	0 = inactive 1 = active	–
662	–	4812	N/A	LCP Pole#2 timeout target status bit	0 = inactive 1 = active	–
663	–	4812	N/A	LOS Pole#1 timeout target status bit	0 = inactive 1 = active	–
664	–	4812	N/A	LOS Pole#2 timeout target status bit	0 = inactive 1 = active	–
665	–	4812	N/A	HCA Pole#1 timeout target status bit	0 = inactive 1 = active	–
666	–	4812	N/A	HCA Pole#2 timeout target status bit	0 = inactive 1 = active	–
667	–	4812	N/A	F81#5 timeout target status bit	0 = inactive 1 = active	–
668	–	4813	N/A	F81#6 timeout target status bit	0 = inactive 1 = active	–
669	–	4813	N/A	Sect Pole#1 27 timeout target status bit	0 = inactive 1 = active	–
670	–	4813	N/A	Sect Pole#2 27 timeout target status bit	0 = inactive 1 = active	–
671	–	4813	N/A	Sect Pole#1 50 timeout target status bit	0 = inactive 1 = active	–
672	–	4813	N/A	Sect Pole#2 50 timeout target status bit	0 = inactive 1 = active	–
673	–	4813	N/A	Sect Pole#1 51 timeout target status bit	0 = inactive 1 = active	–
674	–	4813	N/A	Sect Pole#2 51 timeout target status bit	0 = inactive 1 = active	–
675	–	4813	N/A	Sect Pole#1 59 timeout target status bit	0 = inactive 1 = active	–
676	–	4813	N/A	Sect Pole#2 59 timeout target status bit	0 = inactive 1 = active	–
677	–	4813	N/A	F81#7 timeout target status bit	0 = inactive 1 = active	–
678	–	4813	N/A	F81#8 timeout target status bit	0 = inactive 1 = active	–
679	–	4813	N/A	F81R#3 timeout target status bit	0 = inactive 1 = active	–
680	–	4855	N/A	F81R#4 timeout target status bit	0 = inactive 1 = active	–
681	–	4855	N/A	F81R#5 timeout target status bit	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
682	–	4855	N/A	F81R#6 timeout target status bit	0 = inactive 1 = active	–
683	–	4855	N/A	F81R#7 timeout target status bit	0 = inactive 1 = active	–
684	–	4855	N/A	F81R#8 timeout target status bit	0 = inactive 1 = active	–
685	–	4855	N/A	FBM #2 timeout target status bit	0 = inactive 1 = active	–
686	–	4864	N/A	Virtual Input 1 Status	0 = inactive 1 = active	–
687	–	4864	N/A	Virtual Input 2 Status	0 = inactive 1 = active	–
688	–	4864	N/A	Virtual Input 3 Status	0 = inactive 1 = active	–
689	–	4865	N/A	Virtual Input 4 Status	0 = inactive 1 = active	–
690	–	4865	N/A	Virtual Input 5 Status	0 = inactive 1 = active	–
691	–	4865	N/A	Virtual Input 6 Status	0 = inactive 1 = active	–
692	–	4865	N/A	Virtual Input 7 Status	0 = inactive 1 = active	–
693	–	4865	N/A	Virtual Input 8 Status	0 = inactive 1 = active	–
694	–	4865	N/A	Virtual Input 9 Status	0 = inactive 1 = active	–
695	–	4865	N/A	Virtual Input 10 Status	0 = inactive 1 = active	–
696	–	4865	N/A	Virtual Input 11 Status	0 = inactive 1 = active	–
697	–	4865	N/A	Virtual Input 12 Status	0 = inactive 1 = active	–
698	–	4865	N/A	Virtual Input 13 Status	0 = inactive 1 = active	–
699	–	4865	N/A	Virtual Input 14 Status	0 = inactive 1 = active	–
700	–	4865	N/A	Virtual Input 15 Status	0 = inactive 1 = active	–
701	–	4865	N/A	Virtual Input 16 Status	0 = inactive 1 = active	–
702	–	4865	N/A	Virtual Input 17 Status	0 = inactive 1 = active	–
703	–	4865	N/A	Virtual Input 18 Status	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
704	–	4865	N/A	Virtual Input 19 Status	0 = inactive 1 = active	–
705	–	4891	N/A	FBM #1 percent pickup	0 = inactive 1 = active	–
706	–	4891	N/A	FBM #1 percent timeout	0 = inactive 1 = active	–
707	–	4892	N/A	FBM #2 percent pickup	0 = inactive 1 = active	–
708	–	4892	N/A	FBM #2 percent timeout	0 = inactive 1 = active	–
709	–	7086	N/A	F27#1 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
710	–	7091	N/A	F27#1 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
711	–	7101	N/A	F27#2 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
712	–	7106	N/A	F27#2 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
713	–	7116	N/A	F27#3 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
714	–	7121	N/A	F27#3 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
715	–	7131	N/A	F27#4 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
716	–	7136	N/A	F27#4 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
717	–	46479	N/A	27BSVS Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
718	–	46536	N/A	27BSVS Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
719	–	8026	N/A	F59#1 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
720	–	8031	N/A	F59#1 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
721	–	8041	N/A	F59#2 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
722	–	8046	N/A	F59#2 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
723	–	8056	N/A	F59#3 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
724	–	8061	N/A	F59#3 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
725	–	8071	N/A	F59#4 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
726	–	8076	N/A	F59#4 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
727	–	9150	N/A	F59I Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
728	–	9155	N/A	F59I Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
729	–	7304	N/A	F50P#1 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
730	–	7309	N/A	F50P#1 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
731	–	7319	N/A	F50P#2 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
732	–	7324	N/A	F50P#2 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
733	–	7334	N/A	F50P#3 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
734	–	7339	N/A	F50P#3 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
735	–	7349	N/A	F50P#4 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
736	–	7354	N/A	F50P#4 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
737	–	7568	N/A	F51P #1 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
738	–	7581	N/A	F51P #1 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
739	–	7607	N/A	F51P #2 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
740	–	7620	N/A	F51P #2 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
741	–	7646	N/A	F51P #3 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
742	–	7659	N/A	F51P #3 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
743	–	7685	N/A	F51P #4 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
744	–	7698	N/A	F51P #4 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
745	–	7022	N/A	F25 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
746	–	7036	N/A	F25 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
747	–	7184	N/A	F32#1 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
748	–	7191	N/A	F32#1 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
749	–	7205	N/A	F32#2 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
750	–	7212	N/A	F32#2 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
751	–	7226	N/A	F32#3 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
752	–	7233	N/A	F32#3 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
753	–	7247	N/A	F32#4 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
754	–	7254	N/A	F32#4 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
755	–	46622	N/A	F50BFU Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
756	–	46631	N/A	F50BFU Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
757	–	8564	N/A	F81#1 Enabled/Disabled	0 = inactive 1 = active	–
758	–	8573	N/A	F81#2 Enabled/Disabled	0 = inactive 1 = active	–
759	–	8582	N/A	F81#3 Enabled/Disabled	0 = inactive 1 = active	–
760	–	8591	N/A	F81#4 Enabled/Disabled	0 = inactive 1 = active	–
761	–	46712	N/A	F81#5 Enabled/Disabled	0 = inactive 1 = active	–
762	–	46721	N/A	F81#6 Enabled/Disabled	0 = inactive 1 = active	–
763	–	46730	N/A	F81#7 Enabled/Disabled	0 = inactive 1 = active	–
764	–	46739	N/A	F81#8 Enabled/Disabled	0 = inactive 1 = active	–
765	–	8674	N/A	F81R#1 Enabled/Disabled	0 = inactive 1 = active	–
766	–	8683	N/A	F81R#2 Enabled/Disabled	0 = inactive 1 = active	–
767	–	46748	N/A	F81R#3 Enabled/Disabled	0 = inactive 1 = active	–
768	–	46758	N/A	F81R#4 Enabled/Disabled	0 = inactive 1 = active	–
769	–	46768	N/A	F81R#5 Enabled/Disabled	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
770	–	46778	N/A	F81R#6 Enabled/Disabled	0 = inactive 1 = active	–
771	–	46788	N/A	F81R#7 Enabled/Disabled	0 = inactive 1 = active	–
772	–	46798	N/A	F81R#8 Enabled/Disabled	0 = inactive 1 = active	–
773	–	46571	N/A	FCLP Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
774	–	46594	N/A	FCLP Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
775	–	8774	N/A	F50P in HLT Enabled/Disabled	0 = inactive 1 = active	–
776	–	8780	N/A	F51P in HLT Enabled/Disabled	0 = inactive 1 = active	–
777	–	9102	N/A	IPSLLogic #1 Enabled/Disabled	0 = inactive 1 = active	–
778	–	9106	N/A	IPSLLogic #2 Enabled/Disabled	0 = inactive 1 = active	–
779	–	9110	N/A	IPSLLogic #3 Enabled/Disabled	0 = inactive 1 = active	–
780	–	9114	N/A	IPSLLogic #4 Enabled/Disabled	0 = inactive 1 = active	–
781	–	9118	N/A	IPSLLogic #5 Enabled/Disabled	0 = inactive 1 = active	–
782	–	9122	N/A	IPSLLogic #6 Enabled/Disabled	0 = inactive 1 = active	–
783	–	9126	N/A	IPSLLogic #7 Enabled/Disabled	0 = inactive 1 = active	–
784	–	9130	N/A	IPSLLogic #8 Enabled/Disabled	0 = inactive 1 = active	–
785	–	46045	N/A	Breaker Monitor #1 Enabled/Disabled	0 = inactive 1 = active	–
786	–	47472	N/A	Breaker Monitor #2 Enabled/Disabled	0 = inactive 1 = active	–
787	–	46640	N/A	Load Current Present Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
788	–	46645	N/A	Load Current Present Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
789	–	46650	N/A	HCA Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
790	–	46653	N/A	HCA Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
791	–	46451	N/A	79U Recloser Relay Pole#1 Enabled/Disabled	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
792	–	46508	N/A	79U Recloser Relay Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
793	–	46657	N/A	Sectionalizer Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
794	–	46683	N/A	Sectionalizer Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
795	–	46319	N/A	Loop Scheme Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
796	–	46401	N/A	Loop Scheme Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
797	–	25457	N/A	PSM Enabled/Disabled	0 = inactive 1 = active	–
798	–	6507	N/A	Battery Charger Enabled/Disabled	0 = inactive 1 = active	–
799	–	25432	N/A	TCM Enabled/Disabled	0 = inactive 1 = active	–
800	–	25437	N/A	CCM Enabled/Disabled	0 = inactive 1 = active	–
801	–	28016	N/A	THD #1 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
802	–	28022	N/A	THD #1 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
803	–	28034	N/A	THD #2 Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
804	–	28040	N/A	THD #2 Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
805	–	28069	N/A	Close Block Pole#1 Enabled/Disabled	0 = inactive 1 = active	–
806	–	28078	N/A	Close Block Pole#2 Enabled/Disabled	0 = inactive 1 = active	–
807	–	65002	N/A	GlobalLogic#1 Enabled/Disabled	0 = inactive 1 = active	–
808	–	65011	N/A	GlobalLogic#2 Enabled/Disabled	0 = inactive 1 = active	–
809	–	65020	N/A	GlobalLogic#3 Enabled/Disabled	0 = inactive 1 = active	–
810	–	65029	N/A	GlobalLogic#4 Enabled/Disabled	0 = inactive 1 = active	–
811	–	65038	N/A	GlobalLogic#5 Enabled/Disabled	0 = inactive 1 = active	–
812	–	65047	N/A	GlobalLogic#6 Enabled/Disabled	0 = inactive 1 = active	–
813	–	65056	N/A	GlobalLogic#7 Enabled/Disabled	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
814	—	65065	N/A	GlobalLogic#8 Enabled/Disabled	0 = inactive 1 = active	—
815	—	65080	N/A	LOS Pole#1 Enabled/Disabled	0 = inactive 1 = active	—
816	—	65084	N/A	LOS Pole#2 Enabled/Disabled	0 = inactive 1 = active	—
817	—	4893	N/A	SD Card port Active	0 = inactive 1 = active	—
818	—	4893	N/A	USB Front Port active	0 = inactive 1 = active	—
819	—	4893	N/A	Port 1 Serial Active	0 = inactive 1 = active	—
820	—	4893	N/A	Port 2 Ethernet Active	0 = inactive 1 = active	—
821	—	4893	N/A	Port 3 Ethernet Active	0 = inactive 1 = active	—
822	—	4893	N/A	Port 4 Serial Active	0 = inactive 1 = active	—
823	—	62	N/A	Source Orientation Vy	0 = inactive 1 = active	—
824	—	62	N/A	Source Orientation Vz	0 = inactive 1 = active	—
825	—	1410	N/A	Remote Input 1	0 = inactive 1 = active	—
826	—	1410	N/A	Remote Input 2	0 = inactive 1 = active	—
827	—	1410	N/A	Remote Input 3	0 = inactive 1 = active	—
828	—	1410	N/A	Remote Input 4	0 = inactive 1 = active	—
829	—	1410	N/A	Remote Input 5	0 = inactive 1 = active	—
830	—	1410	N/A	Remote Input 6	0 = inactive 1 = active	—
831	—	1410	N/A	Remote Input 7	0 = inactive 1 = active	—
832	—	1410	N/A	Remote Input 8	0 = inactive 1 = active	—
833	—	1410	N/A	Remote Input 9	0 = inactive 1 = active	—
834	—	1410	N/A	Remote Input 10	0 = inactive 1 = active	—
835	—	1410	N/A	Remote Input 11	0 = inactive 1 = active	—

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
836	–	1410	N/A	Remote Input 12	0 = inactive 1 = active	–
837	–	1410	N/A	Remote Input 13	0 = inactive 1 = active	–
838	–	1410	N/A	Remote Input 14	0 = inactive 1 = active	–
839	–	1410	N/A	Remote Input 15	0 = inactive 1 = active	–
840	–	1410	N/A	Remote Input 16	0 = inactive 1 = active	–
841	–	1422	N/A	Remote Input 17	0 = inactive 1 = active	–
842	–	1422	N/A	Remote Input 18	0 = inactive 1 = active	–
843	–	1422	N/A	Remote Input 19	0 = inactive 1 = active	–
844	–	1422	N/A	Remote Input 20	0 = inactive 1 = active	–
845	–	1422	N/A	Remote Input 21	0 = inactive 1 = active	–
846	–	1422	N/A	Remote Input 22	0 = inactive 1 = active	–
847	–	1422	N/A	Remote Input 23	0 = inactive 1 = active	–
848	–	1422	N/A	Remote Input 24	0 = inactive 1 = active	–
849	–	1422	N/A	Remote Input 25	0 = inactive 1 = active	–
850	–	1422	N/A	Remote Input 26	0 = inactive 1 = active	–
851	–	1422	N/A	Remote Input 27	0 = inactive 1 = active	–
852	–	1422	N/A	Remote Input 28	0 = inactive 1 = active	–
853	–	1422	N/A	Remote Input 29	0 = inactive 1 = active	–
854	–	1422	N/A	Remote Input 30	0 = inactive 1 = active	–
855	–	1422	N/A	Remote Input 31	0 = inactive 1 = active	–
856	–	1422	N/A	Remote Input 32	0 = inactive 1 = active	–
857	–	1423	N/A	Remote Input 33	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
858	—	1423	N/A	Remote Input 34	0 = inactive 1 = active	—
859	—	1423	N/A	Remote Input 35	0 = inactive 1 = active	—
860	—	1423	N/A	Remote Input 36	0 = inactive 1 = active	—
861	—	1423	N/A	Remote Input 37	0 = inactive 1 = active	—
862	—	1423	N/A	Remote Input 38	0 = inactive 1 = active	—
863	—	1423	N/A	Remote Input 39	0 = inactive 1 = active	—
864	—	1423	N/A	Remote Input 40	0 = inactive 1 = active	—
865	—	1423	N/A	Remote Input 41	0 = inactive 1 = active	—
866	—	1423	N/A	Remote Input 42	0 = inactive 1 = active	—
867	—	1423	N/A	Remote Input 43	0 = inactive 1 = active	—
868	—	1423	N/A	Remote Input 44	0 = inactive 1 = active	—
869	—	1423	N/A	Remote Input 45	0 = inactive 1 = active	—
870	—	1423	N/A	Remote Input 46	0 = inactive 1 = active	—
871	—	1423	N/A	Remote Input 47	0 = inactive 1 = active	—
872	—	1423	N/A	Remote Input 48	0 = inactive 1 = active	—
873	—	1424	N/A	Remote Input 49	0 = inactive 1 = active	—
874	—	1424	N/A	Remote Input 50	0 = inactive 1 = active	—
875	—	1424	N/A	Remote Input 51	0 = inactive 1 = active	—
876	—	1424	N/A	Remote Input 52	0 = inactive 1 = active	—
877	—	1424	N/A	Remote Input 53	0 = inactive 1 = active	—
878	—	1424	N/A	Remote Input 54	0 = inactive 1 = active	—
879	—	1424	N/A	Remote Input 55	0 = inactive 1 = active	—

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
880	–	1424	N/A	Remote Input 56	0 = inactive 1 = active	–
881	–	1424	N/A	Remote Input 57	0 = inactive 1 = active	–
882	–	1424	N/A	Remote Input 58	0 = inactive 1 = active	–
883	–	1424	N/A	Remote Input 59	0 = inactive 1 = active	–
884	–	1424	N/A	Remote Input 60	0 = inactive 1 = active	–
885	–	1424	N/A	Remote Input 61	0 = inactive 1 = active	–
886	–	1424	N/A	Remote Input 62	0 = inactive 1 = active	–
887	–	1424	N/A	Remote Input 63	0 = inactive 1 = active	–
888	–	1424	N/A	Remote Input 64	0 = inactive 1 = active	–
889	–	4893	N/A	Port 1 Serial Comms Lost	0 = inactive 1 = active	–
890	–	4893	N/A	Port 2 Ethernet Comms Lost	0 = inactive 1 = active	–
891	–	4893	N/A	Port 3 Ethernet Comms Lost	0 = inactive 1 = active	–
892	–	4893	N/A	Port 4 Serial Comms Lost	0 = inactive 1 = active	–
893	–	4894	N/A	Contact wear Pole#1 (BM function) 95% used	0 = inactive 1 = active	–
894	–	4894	N/A	Contact wear Pole#2 (BM function) 95% used	0 = inactive 1 = active	–
895	–	3798	N/A	Battery Charger Test in Progress	0 = inactive 1 = active	–
896	–	3798	N/A	Battery Charger Mode Absorb	0 = inactive 1 = active	–
897	–	3798	N/A	Battery Charger Cap Charged	0 = inactive 1 = active	–
898	–	4991	N/A	Battery Charger Mode Bulk	0 = inactive 1 = active	–
899	–	4991	N/A	Battery Charger Mode Float	0 = inactive 1 = active	–
900	–	4470	N/A	32H Battery Charger ON/OFF	0 = inactive 1 = active	–
901	–	4901	N/A	32H Internal Door Status	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
902	–	4901	N/A	32H External Door Status	0 = inactive 1 = active	–
903	–	4901	N/A	32H Low Capacitor Voltage	0 = inactive 1 = active	–
904	–	4901	N/A	32H High Temperature Alarm	0 = inactive 1 = active	–
905	–	4901	N/A	32H Low Battery Alarm	0 = inactive 1 = active	–
906	–	4962	N/A	Any F27#4 Pole#2 timeout status bitPole#1 Pickup	0 = inactive 1 = active	–
907	–	4962	N/A	Any F27 Pole#2 Pickup	0 = inactive 1 = active	–
908	–	4962	N/A	Any F50P Pole#1 Pickup	0 = inactive 1 = active	–
909	–	4962	N/A	Any F50P Pole#2 Pickup	0 = inactive 1 = active	–
910	–	4962	N/A	Any F51P Pole#1 Pickup	0 = inactive 1 = active	–
911	–	4962	N/A	Any F51P Pole#2 Pickup	0 = inactive 1 = active	–
912	–	4962	N/A	Any F59 Pole#1 Pickup	0 = inactive 1 = active	–
913	–	4962	N/A	Any F59 Pole#2 Pickup	0 = inactive 1 = active	–
914	–	4962	N/A	Any F81 Pickup	0 = inactive 1 = active	–
915	–	4962	N/A	Any HLT Pole#1 Pickup	0 = inactive 1 = active	–
916	–	4962	N/A	Any HLT Pole#2 Pickup	0 = inactive 1 = active	–
917	–	4962	N/A	Any 81R Pickup	0 = inactive 1 = active	–
918	–	4964	N/A	Any F27 Pole#1 Timeout	0 = inactive 1 = active	–
919	–	4964	N/A	Any F27 Pole#2 Timeout	0 = inactive 1 = active	–
920	–	4964	N/A	Any F50P Pole#1 Timeout	0 = inactive 1 = active	–
921	–	4964	N/A	Any F50P Pole#2 Timeout	0 = inactive 1 = active	–
922	–	4964	N/A	Any F51P Pole#1 Timeout	0 = inactive 1 = active	–
923	–	4964	N/A	Any F51P Pole#2 Timeout	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
924	—	4964	N/A	Any F59 Pole#1 Timeout	0 = inactive 1 = active	—
925	—	4964	N/A	Any F59 Pole#2 Pickup	0 = inactive 1 = active	—
926	—	4964	N/A	Any F81 Timeout	0 = inactive 1 = active	—
927	—	4964	N/A	Any HLT Pole #1 Timeout	0 = inactive 1 = active	—
928	—	4964	N/A	Any HLT Pole #2 Timeout	0 = inactive 1 = active	—
929	—	4964	N/A	Any 81R Timeout	0 = inactive 1 = active	—
930	—	4990	N/A	Power Target #1	0 = inactive 1 = active	—
931	—	4990	N/A	Power Target #2	0 = inactive 1 = active	—
932	—	4990	N/A	Power Target #3	0 = inactive 1 = active	—
933	—	4990	N/A	Power Target #4	0 = inactive 1 = active	—
934	—	4990	N/A	Power Target #5	0 = inactive 1 = active	—
935	—	4990	N/A	Power Target #6	0 = inactive 1 = active	—
936	—	4990	N/A	Power Target #7	0 = inactive 1 = active	—
937	—	4990	N/A	Power Target #8	0 = inactive 1 = active	—
938	—	4990	N/A	Power Target #9	0 = inactive 1 = active	—
939	—	4990	N/A	Power Target #10	0 = inactive 1 = active	—
940	—	4990	N/A	Power Target #11	0 = inactive 1 = active	—
941	—	4990	N/A	Power Target #12	0 = inactive 1 = active	—
942	—	1501	N/A	VLogic Output 1 Active Profile	0 = inactive 1 = active	—
943	—	1501	N/A	VLogic Output 2 Active Profile	0 = inactive 1 = active	—
944	—	1501	N/A	VLogic Output 3 Active Profile	0 = inactive 1 = active	—
945	—	1501	N/A	VLogic Output 4 Active Profile	0 = inactive 1 = active	—

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
946	–	1501	N/A	VLogic Output 5 Active Profile	0 = inactive 1 = active	–
947	–	1501	N/A	VLogic Output 6 Active Profile	0 = inactive 1 = active	–
948	–	1501	N/A	VLogic Output 7 Active Profile	0 = inactive 1 = active	–
949	–	1501	N/A	VLogic Output 8 Active Profile	0 = inactive 1 = active	–
950	–	1501	N/A	VLogic Output 9 Active Profile	0 = inactive 1 = active	–
951	–	1501	N/A	VLogic Output 10 Active Profile	0 = inactive 1 = active	–
952	–	1501	N/A	VLogic Output 11 Active Profile	0 = inactive 1 = active	–
953	–	1501	N/A	VLogic Output 12 Active Profile	0 = inactive 1 = active	–
954	–	1501	N/A	VLogic Output 13 Active Profile	0 = inactive 1 = active	–
955	–	1501	N/A	VLogic Output 14 Active Profile	0 = inactive 1 = active	–
956	–	1501	N/A	VLogic Output 15 Active Profile	0 = inactive 1 = active	–
957	–	1501	N/A	VLogic Output 16 Active Profile	0 = inactive 1 = active	–
958	–	1502	N/A	VLogic Output 17 Active Profile	0 = inactive 1 = active	–
959	–	1502	N/A	VLogic Output 18 Active Profile	0 = inactive 1 = active	–
960	–	1502	N/A	VLogic Output 19 Active Profile	0 = inactive 1 = active	–
961	–	1502	N/A	VLogic Output 20 Active Profile	0 = inactive 1 = active	–
962	–	1502	N/A	VLogic Output 21 Active Profile	0 = inactive 1 = active	–
963	–	1502	N/A	VLogic Output 22 Active Profile	0 = inactive 1 = active	–
964	–	1502	N/A	VLogic Output 23 Active Profile	0 = inactive 1 = active	–
965	–	1502	N/A	VLogic Output 24 Active Profile	0 = inactive 1 = active	–
966	–	1502	N/A	VLogic Output 25 Active Profile	0 = inactive 1 = active	–
967	–	1502	N/A	VLogic Output 26 Active Profile	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
968	–	1502	N/A	VLogic Output 27 Active Profile	0 = inactive 1 = active	–
969	–	1502	N/A	VLogic Output 28 Active Profile	0 = inactive 1 = active	–
970	–	1502	N/A	VLogic Output 29 Active Profile	0 = inactive 1 = active	–
971	–	1502	N/A	VLogic Output 30 Active Profile	0 = inactive 1 = active	–
972	–	1502	N/A	VLogic Output 31 Active Profile	0 = inactive 1 = active	–
973	–	1502	N/A	VLogic Output 32 Active Profile	0 = inactive 1 = active	–
974	–	1503	N/A	VLogic Output 1 Common Profile	0 = inactive 1 = active	–
975	–	1503	N/A	VLogic Output 2 Common Profile	0 = inactive 1 = active	–
976	–	1503	N/A	VLogic Output 3 Common Profile	0 = inactive 1 = active	–
977	–	1503	N/A	VLogic Output 4 Common Profile	0 = inactive 1 = active	–
978	–	1503	N/A	VLogic Output 5 Common Profile	0 = inactive 1 = active	–
979	–	1503	N/A	VLogic Output 6 Common Profile	0 = inactive 1 = active	–
980	–	1503	N/A	VLogic Output 7 Common Profile	0 = inactive 1 = active	–
981	–	1503	N/A	VLogic Output 8 Common Profile	0 = inactive 1 = active	–
982	–	1503	N/A	VLogic Output 9 Common Profile	0 = inactive 1 = active	–
983	–	1503	N/A	VLogic Output 10 Common Profile	0 = inactive 1 = active	–
984	–	1503	N/A	VLogic Output 11 Common Profile	0 = inactive 1 = active	–
985	–	1503	N/A	VLogic Output 12 Common Profile	0 = inactive 1 = active	–
986	–	1503	N/A	VLogic Output 13 Common Profile	0 = inactive 1 = active	–
987	–	1503	N/A	VLogic Output 14 Common Profile	0 = inactive 1 = active	–
988	–	1503	N/A	VLogic Output 15 Common Profile	0 = inactive 1 = active	–
989	–	1503	N/A	VLogic Output 16 Common Profile	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
990	–	1504	N/A	VLogic Output 17 Common Profile	0 = inactive 1 = active	–
991	–	1504	N/A	VLogic Output 18 Common Profile	0 = inactive 1 = active	–
992	–	1504	N/A	VLogic Output 19 Common Profile	0 = inactive 1 = active	–
993	–	1504	N/A	VLogic Output 20 Common Profile	0 = inactive 1 = active	–
994	–	1504	N/A	VLogic Output 21 Common Profile	0 = inactive 1 = active	–
995	–	1504	N/A	VLogic Output 22 Common Profile	0 = inactive 1 = active	–
996	–	1504	N/A	VLogic Output 23 Common Profile	0 = inactive 1 = active	–
997	–	1504	N/A	VLogic Output 24 Common Profile	0 = inactive 1 = active	–
998	–	1504	N/A	VLogic Output 25 Common Profile	0 = inactive 1 = active	–
999	–	1504	N/A	VLogic Output 26 Common Profile	0 = inactive 1 = active	–
1000	–	1504	N/A	VLogic Output 27 Common Profile	0 = inactive 1 = active	–
1001	–	1504	N/A	VLogic Output 28 Common Profile	0 = inactive 1 = active	–
1002	–	1504	N/A	VLogic Output 29 Common Profile	0 = inactive 1 = active	–
1003	–	1504	N/A	VLogic Output 30 Common Profile	0 = inactive 1 = active	–
1004	–	1504	N/A	VLogic Output 31 Common Profile	0 = inactive 1 = active	–
1005	–	1504	N/A	VLogic Output 32 Common Profile	0 = inactive 1 = active	–
1006	–	46166	N/A	One Shot Sectionalizing Pole #1 Enabled/Disabled	0 = inactive 1 = active	–
1007	–	46182	N/A	One Shot Sectionalizing Pole #2 Enabled/Disabled	0 = inactive 1 = active	–
1008	–	3794	N/A	Control Alarm Max Breaker Operations	0 = inactive 1 = active	–
1009	–	3794	N/A	Gas Pressure Alarm	0 = inactive 1 = active	–
1010	–	3794	N/A	Trip/Close Error Alarm	0 = inactive 1 = active	–
1011	–	3917	N/A	79 Reclosing Blocked	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1012	–	4473	N/A	Any Phase Protection Function Block	0 = inactive 1 = active	–
1013	–	4473	N/A	Any OverCurrent Block	0 = inactive 1 = active	–
1014	–	4473	N/A	Any Phase OverCurrent Block	0 = inactive 1 = active	–
1015	–	4473	N/A	Any Protection Function Block	0 = inactive 1 = active	–
1016	–	4473	N/A	Any Overvoltage Function Block	0 = inactive 1 = active	–
1017	–	4473	N/A	Any Undervoltage Function Block	0 = inactive 1 = active	–
1018	–	4962	N/A	Any F50P Pickup	0 = inactive 1 = active	–
1019	–	4964	N/A	Any F50P Timeout	0 = inactive 1 = active	–
1020	–	4962	N/A	Any F51P Pickup	0 = inactive 1 = active	–
1021	–	4964	N/A	Any F51P Timeout	0 = inactive 1 = active	–
1022	–	4994	N/A	Target Any F50P/51P Pole#1	0 = inactive 1 = active	–
1023	–	4994	N/A	Target Any F50P/51P Pole#2	0 = inactive 1 = active	–
1024	–	2050	N/A	Mechanical Trip Failure	0 = inactive 1 = active	–
1025	–	2050	N/A	Mechanical Close Failure	0 = inactive 1 = active	–
1026	–	2050	N/A	Mechanical Trip/Close Failure	0 = inactive 1 = active	–
1027	–	2513	N/A	F79 Sequence Co-ordination in progress	0 = inactive 1 = active	–
1028	–	4996	N/A	Control Lockout from 79 Function	0 = inactive 1 = active	–
1029	–	4996	N/A	Close Block on Frequency Pole#1	0 = inactive 1 = active	–
1030	–	4996	N/A	Close Block on Frequency Pole#2	0 = inactive 1 = active	–
1031	–	4996	N/A	Close Block on Voltage Pole#1	0 = inactive 1 = active	–
1032	–	4996	N/A	Close Block on Voltage Pole#2	0 = inactive 1 = active	–
1033	–	4996	N/A	Close Block on Voltage Upper Limit Pole#1	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)						
Object: 01 Variation = 01 for class 0 poll						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
1034	–	4996	N/A	Close Block on Voltage Upper Limit Pole#2	0 = inactive 1 = active	–
1035	–	4996	N/A	Close Block on Voltage Lower Limit Pole#1	0 = inactive 1 = active	–
1036	–	4996	N/A	Close Block on Voltage Lower Limit Pole#2	0 = inactive 1 = active	–

Table 6 Single-Bit Binary Inputs (Status) (Pole 1 & Pole 2)

Binary Output Points List (CROB) (Pole 1 & Pole 2)

Control Relay Output Block (Direct Control Method)

Object: 12 (Write Only)

These points are written via: obj 12, var 01 using the Direct Operate command

These points are read via: obj 01, var 01

Configure the control points by selecting LATCH ON/OFF or TRIP/CLOSE or PULSE ON/OFF or INVERSE

Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
0	—	1400	N/A	OPEN breaker command	—	—
1	—	1404	N/A	OPEN Pole#1 breaker command	—	—
2	—	1405	N/A	OPEN Pole#2 breaker command	—	—
3	—	1401	N/A	CLOSE breaker	—	—
4	—	1407	N/A	CLOSE Pole#1 breaker command	—	—
5	—	1408	N/A	CLOSE Pole#2 breaker command	—	—
6	—	1402	N/A	Hot Line Tag Command	—	—
7	—	1403	N/A	Reset command	—	—
8	—	0	N/A	Set to enable profile edit	—	—
9	—	3804	N/A	Energize/De-energize the output 1	—	—
10	—	3804	N/A	Energize/De-energize the output 2	—	—
11	—	3804	N/A	Energize/De-energize the output 3	—	—
12	—	3804	N/A	Energize/De-energize the output 4	—	—
13	—	3804	N/A	Energize/De-energize the output 5	—	—
14	—	3804	N/A	Energize/De-energize the output 6	—	—
15	—	3804	N/A	Energize/De-energize the output 7	—	—
16	—	3804	N/A	Energize/De-energize the output 8	—	—
17	—	3804	N/A	Energize/De-energize the output 9	—	—
18	—	3804	N/A	Energize/De-energize the output 10	—	—
19	—	3804	N/A	Energize/De-energize the output 11	—	—
20	—	3804	N/A	Energize/De-energize the output 12	—	—
21	—	1410	N/A	Remote IPSLOGIC Input 1	—	—
22	—	1410	N/A	Remote IPSLOGIC Input 2	—	—
23	—	1410	N/A	Remote IPSLOGIC Input 3	—	—
24	—	1410	N/A	Remote IPSLOGIC Input 4	—	—
25	—	1410	N/A	Remote IPSLOGIC Input 5	—	—
26	—	1410	N/A	Remote IPSLOGIC Input 6	—	—
27	—	1410	N/A	Remote IPSLOGIC Input 7	—	—

Table 7 Binary Output Points List (CROB) (Pole 1 & Pole 2)

Binary Output Points List (CROB) (Pole 1 & Pole 2)

Control Relay Output Block (Direct Control Method)

Object: 12 (Write Only)

These points are written via: obj 12, var 01 using the Direct Operate command

These points are read via: obj 01, var 01

Configure the control points by selecting LATCH ON/OFF or TRIP/CLOSE or PULSE ON/OFF or INVERSE

Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
28	—	1410	N/A	Remote IPSLOGIC Input 8	—	—
29	—	1410	N/A	Remote IPSLOGIC Input 9	—	—
30	—	1410	N/A	Remote IPSLOGIC Input 10	—	—
31	—	1410	N/A	Remote IPSLOGIC Input 11	—	—
32	—	1410	N/A	Remote IPSLOGIC Input 12	—	—
33	—	1410	N/A	Remote IPSLOGIC Input 13	—	—
34	—	1410	N/A	Remote IPSLOGIC Input 14	—	—
35	—	1410	N/A	Remote IPSLOGIC Input 15	—	—
36	—	1410	N/A	Remote IPSLOGIC Input 16	—	—
37	—	1422	N/A	Remote IPSLOGIC Input 17	—	—
38	—	1422	N/A	Remote IPSLOGIC Input 18	—	—
39	—	1422	N/A	Remote IPSLOGIC Input 19	—	—
40	—	1422	N/A	Remote IPSLOGIC Input 20	—	—
41	—	1422	N/A	Remote IPSLOGIC Input 21	—	—
42	—	1422	N/A	Remote IPSLOGIC Input 22	—	—
43	—	1422	N/A	Remote IPSLOGIC Input 23	—	—
44	—	1422	N/A	Remote IPSLOGIC Input 24	—	—
45	—	1422	N/A	Remote IPSLOGIC Input 25	—	—
46	—	1422	N/A	Remote IPSLOGIC Input 26	—	—
47	—	1422	N/A	Remote IPSLOGIC Input 27	—	—
48	—	1422	N/A	Remote IPSLOGIC Input 28	—	—
49	—	1422	N/A	Remote IPSLOGIC Input 29	—	—
50	—	1422	N/A	Remote IPSLOGIC Input 30	—	—
51	—	1422	N/A	Remote IPSLOGIC Input 31	—	—
52	—	1422	N/A	Remote IPSLOGIC Input 32	—	—
53	—	1423	N/A	Remote IPSLOGIC Input 33	—	—
54	—	1423	N/A	Remote IPSLOGIC Input 34	—	—
55	—	1423	N/A	Remote IPSLOGIC Input 35	—	—

Table 7 Binary Output Points List (CROB) (Pole 1 & Pole 2)

Binary Output Points List (CROB) (Pole 1 & Pole 2)

Control Relay Output Block (Direct Control Method)

Object: 12 (Write Only)

These points are written via: obj 12, var 01 using the Direct Operate command

These points are read via: obj 01, var 01

Configure the control points by selecting LATCH ON/OFF or TRIP/CLOSE or PULSE ON/OFF or INVERSE

Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
56	—	1423	N/A	Remote IPSLOGIC Input 36	—	—
57	—	1423	N/A	Remote IPSLOGIC Input 37	—	—
58	—	1423	N/A	Remote IPSLOGIC Input 38	—	—
59	—	1423	N/A	Remote IPSLOGIC Input 39	—	—
60	—	1423	N/A	Remote IPSLOGIC Input 40	—	—
61	—	1423	N/A	Remote IPSLOGIC Input 41	—	—
62	—	1423	N/A	Remote IPSLOGIC Input 42	—	—
63	—	1423	N/A	Remote IPSLOGIC Input 43	—	—
64	—	1423	N/A	Remote IPSLOGIC Input 44	—	—
65	—	1423	N/A	Remote IPSLOGIC Input 45	—	—
66	—	1423	N/A	Remote IPSLOGIC Input 46	—	—
67	—	1423	N/A	Remote IPSLOGIC Input 47	—	—
68	—	1423	N/A	Remote IPSLOGIC Input 48	—	—
69	—	1424	N/A	Remote IPSLOGIC Input 49	—	—
70	—	1424	N/A	Remote IPSLOGIC Input 50	—	—
71	—	1424	N/A	Remote IPSLOGIC Input 51	—	—
72	—	1424	N/A	Remote IPSLOGIC Input 52	—	—
73	—	1424	N/A	Remote IPSLOGIC Input 53	—	—
74	—	1424	N/A	Remote IPSLOGIC Input 54	—	—
75	—	1424	N/A	Remote IPSLOGIC Input 55	—	—
76	—	1424	N/A	Remote IPSLOGIC Input 56	—	—
77	—	1424	N/A	Remote IPSLOGIC Input 57	—	—
78	—	1424	N/A	Remote IPSLOGIC Input 58	—	—
79	—	1424	N/A	Remote IPSLOGIC Input 59	—	—
80	—	1424	N/A	Remote IPSLOGIC Input 60	—	—
81	—	1424	N/A	Remote IPSLOGIC Input 61	—	—
82	—	1424	N/A	Remote IPSLOGIC Input 62	—	—
83	—	1424	N/A	Remote IPSLOGIC Input 63	—	—

Table 7 Binary Output Points List (CROB) (Pole 1 & Pole 2)

Binary Output Points List (CROB) (Pole 1 & Pole 2)

Control Relay Output Block (Direct Control Method)

Object: 12 (Write Only)

These points are written via: obj 12, var 01 using the Direct Operate command

These points are read via: obj 01, var 01

Configure the control points by selecting LATCH ON/OFF or TRIP/CLOSE or PULSE ON/OFF or INVERSE

Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
84	—	1424	N/A	Remote IPSLOGIC Input 64	—	—
85	—	20	N/A	Change to Profile 1	—	—
86	—	20	N/A	Change to Profile 2	—	—
87	—	20	N/A	Change to Profile 3	—	—
88	—	20	N/A	Change to Profile 4	—	—
89	—	20	N/A	Change to Profile 5	—	—
90	—	20	N/A	Change to Profile 6	—	—
91	—	20	N/A	Change to Profile 7	—	—
92	—	20	N/A	Change to Profile 8	—	—
93	—	—	N/A	RESET DEMAND	—	—
94	—	1403	N/A	RESET ALARMS	—	—
95	—	1150	N/A	Reset Pickup OC Counter Pole#1	—	—
96	—	1154	N/A	Reset Pickup OC Counter Pole#2	—	—
97	—	1152	N/A	Reset Total Trip OC Counter Pole#1	—	—
98	—	1156	N/A	Reset Total Trip OC Counter Pole#2	—	—
99	—	1174	N/A	Reset IPSlogic Counter #1	—	—
100	—	1175	N/A	Reset IPSlogic Counter #2	—	—
101	—	1176	N/A	Reset IPSlogic Counter #3	—	—
102	—	1177	N/A	Reset IPSlogic Counter #4	—	—
103	—	1178	N/A	Reset IPSlogic Counter #5	—	—
104	—	1179	N/A	Reset IPSlogic Counter #6	—	—
105	—	1180	N/A	Reset IPSlogic Counter #7	—	—
106	—	1181	N/A	Reset IPSlogic Counter #8	—	—
107	—	6515	N/A	Battery Test Schedule Enable/Disable	—	—
108	—	6508	N/A	Start Battery Test Enable/Disable	—	—
109	—	1419	N/A	Disable Fast/Slow Curves Pole#1	—	—
110	—	1419	N/A	Fast Curve command Pole#1	—	—
111	—	1419	N/A	Slow Curve command Pole#1	—	—

Table 7 Binary Output Points List (CROB) (Pole 1 & Pole 2)

Binary Output Points List (CROB) (Pole 1 & Pole 2)

Control Relay Output Block (Direct Control Method)

Object: 12 (Write Only)

These points are written via: obj 12, var 01 using the Direct Operate command

These points are read via: obj 01, var 01

Configure the control points by selecting LATCH ON/OFF or TRIP/CLOSE or PULSE ON/OFF or INVERSE

Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
112	—	1419	N/A	Skip Fast Curve command Pole#1	—	—
113	—	1420	N/A	Disable Fast/Slow Curves Pole#2	—	—
114	—	1420	N/A	Fast Curve command Pole#2	—	—
115	—	1420	N/A	Slow Curve command Pole#2	—	—
116	—	1420	N/A	Skip Fast Curve command Pole#2	—	—
117	—	1412	N/A	Enable Close Block Override	—	—
118	—	1413	N/A	Activate Button S10	—	—
119	—	1413	N/A	Activate Button S11	—	—
120	—	1413	N/A	Activate Button S12	—	—
121	—	1413	N/A	Activate Button S13	—	—
122	—	1413	N/A	Activate Button S14	—	—
123	—	1413	N/A	Activate Button S15	—	—
124	—	1413	N/A	Activate Button S16	—	—
125	—	1413	N/A	Activate Button S17	—	—
126	—	1415	N/A	Remote IPSLOGIC Pulse Input 1	—	—
127	—	1415	N/A	Remote IPSLOGIC Pulse Input 2	—	—
128	—	1415	N/A	Remote IPSLOGIC Pulse Input 3	—	—
129	—	1415	N/A	Remote IPSLOGIC Pulse Input 4	—	—
130	—	1415	N/A	Remote IPSLOGIC Pulse Input 5	—	—
131	—	1415	N/A	Remote IPSLOGIC Pulse Input 6	—	—
132	—	1415	N/A	Remote IPSLOGIC Pulse Input 7	—	—
133	—	1415	N/A	Remote IPSLOGIC Pulse Input 8	—	—
134	—	1415	N/A	Remote IPSLOGIC Pulse Input 9	—	—
135	—	1415	N/A	Remote IPSLOGIC Pulse Input 10	—	—
136	—	1415	N/A	Remote IPSLOGIC Pulse Input 11	—	—
137	—	1415	N/A	Remote IPSLOGIC Pulse Input 12	—	—
138	—	3942	N/A	Loop Scheme Inhibit	—	—
139	—	1417	N/A	Clear Alarms	—	—

Table 7 Binary Output Points List (CROB) (Pole 1 & Pole 2)

Binary Output Points List (CROB) (Pole 1 & Pole 2)

Control Relay Output Block (Direct Control Method)

Object: 12 (Write Only)

These points are written via: obj 12, var 01 using the Direct Operate command

These points are read via: obj 01, var 01

Configure the control points by selecting LATCH ON/OFF or TRIP/CLOSE or PULSE ON/OFF or INVERSE

Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
140	—	1418	N/A	Buttons Init to Default	—	—
141	—	1400	N/A	Breaker Operation Both Poles	—	—
142	—	1404	N/A	Breaker Operation Pole#1	—	—
143	—	1405	N/A	Breaker Operation Pole#2	—	—
144	—	1100	N/A	Reset COMIPSlogic Counter #1	—	—
145	—	1101	N/A	Reset COMIPSlogic Counter #2	—	—
146	—	1102	N/A	Reset COMIPSlogic Counter #3	—	—
147	—	1103	N/A	Reset COMIPSlogic Counter #4	—	—
148	—	1104	N/A	Reset COMIPSlogic Counter #5	—	—
149	—	1105	N/A	Reset COMIPSlogic Counter #6	—	—
150	—	1106	N/A	Reset COMIPSlogic Counter #7	—	—
151	—	1107	N/A	Reset COMIPSlogic Counter #8	—	—
152	—	148	N/A	Enable Fault Current	—	—
153	—	0	N/A	CLP Initiate Pole#1	—	—
154	—	0	N/A	CLP Initiate Pole#2	—	—
155	—	7086	N/A	27#1 Pole#1 Enable/Disable	—	—
156	—	7091	N/A	27#1 Pole#2 Enable/Disable	—	—
157	—	7101	N/A	27#2 Pole#1 Enable/Disable	—	—
158	—	7106	N/A	27#2 Pole#2 Enable/Disable	—	—
159	—	7116	N/A	27#3 Pole#1 Enable/Disable	—	—
160	—	7121	N/A	27#3 Pole#2 Enable/Disable	—	—
161	—	7131	N/A	27#4 Pole#1 Enable/Disable	—	—
162	—	7136	N/A	27#4 Pole#2 Enable/Disable	—	—
163	—	8728	N/A	27VzS Pole#1 Enable/Disable	—	—
164	—	8733	N/A	27VzS Pole#2 Enable/Disable	—	—
165	—	46479	N/A	27BSVS Pole#1 Enable/Disable	—	—
166	—	46536	N/A	27BSVS Pole#2 Enable/Disable	—	—
167	—	8026	N/A	59#1 Pole#1 Enable/Disable	—	—

Table 7 Binary Output Points List (CROB) (Pole 1 & Pole 2)

Binary Output Points List (CROB) (Pole 1 & Pole 2)

Control Relay Output Block (Direct Control Method)

Object: 12 (Write Only)

These points are written via: obj 12, var 01 using the Direct Operate command

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Configure the control points by selecting LATCH ON/OFF or TRIP/CLOSE or PULSE ON/OFF or INVERSE

Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
168	—	8031	N/A	59#1 Pole#2 Enable/Disable	—	—
169	—	8041	N/A	59#2 Pole#1 Enable/Disable	—	—
170	—	8046	N/A	59#2 Pole#2 Enable/Disable	—	—
171	—	8056	N/A	59#3 Pole#1 Enable/Disable	—	—
172	—	8061	N/A	59#3 Pole#2 Enable/Disable	—	—
173	—	8071	N/A	59#4 Pole#1 Enable/Disable	—	—
174	—	8076	N/A	59#4 Pole#2 Enable/Disable	—	—
175	—	8096	N/A	59VzS Pole#1 Enable/Disable	—	—
176	—	8101	N/A	59VzS Pole#2 Enable/Disable	—	—
177	—	9150	N/A	59I Pole#1 Enable/Disable	—	—
178	—	9155	N/A	59I Pole#2 Enable/Disable	—	—
179	—	7304	N/A	50P#1 Pole#1 Enable/Disable	—	—
180	—	7309	N/A	50P#1 Pole#2 Enable/Disable	—	—
181	—	7319	N/A	50P#2 Pole#1 Enable/Disable	—	—
182	1	7324	N/A	50P#2 Pole#2 Enable/Disable	—	—
183	1	7334	N/A	50P#3 Pole#1 Enable/Disable	—	—
184	1	7339	N/A	50P#3 Pole#2 Enable/Disable	—	—
185	1	7349	N/A	50P#4 Pole#1 Enable/Disable	—	—
186	1	7354	N/A	50P#4 Pole#2 Enable/Disable	—	—
187	1	7568	N/A	51P#1 Pole#1 Enable/Disable	—	—
188	1	7581	N/A	51P#1 Pole#2 Enable/Disable	—	—
189	1	7607	N/A	51P#2 Pole#1 Enable/Disable	—	—
190	1	7620	N/A	51P#2 Pole#2 Enable/Disable	—	—
191	1	7646	N/A	51P#3 Pole#1 Enable/Disable	—	—
192	1	7659	N/A	51P#3 Pole#2 Enable/Disable	—	—
193	1	7685	N/A	51P#4 Pole#1 Enable/Disable	—	—
194	1	7698	N/A	51P#4 Pole#2 Enable/Disable	—	—
195	1	7022	N/A	Sync Check Rly (25) Pole#1 Enable/Disable	—	—

Table 7 Binary Output Points List (CROB) (Pole 1 & Pole 2)

Binary Output Points List (CROB) (Pole 1 & Pole 2)

Control Relay Output Block (Direct Control Method)

Object: 12 (Write Only)

These points are written via: obj 12, var 01 using the Direct Operate command

These points are read via: obj 01, var 01

Configure the control points by selecting LATCH ON/OFF or TRIP/CLOSE or PULSE ON/OFF or INVERSE

Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
196	1	7036	N/A	Sync Check Rly (25) Pole#2 Enable/Disable	—	—
197	1	7184	N/A	32#1 Pole#1 Enable/Disable	—	—
198	1	7191	N/A	32#1 Pole#2 Enable/Disable	—	—
199	1	7205	N/A	32#2 Pole#1 Enable/Disable	—	—
200	1	7212	N/A	32#2 Pole#2 Enable/Disable	—	—
201	1	7226	N/A	32#3 Pole#1 Enable/Disable	—	—
202	1	7233	N/A	32#3 Pole#2 Enable/Disable	—	—
203	1	7247	N/A	32#4 Pole#1 Enable/Disable	—	—
204	1	7254	N/A	32#4 Pole#2 Enable/Disable	—	—
205	1	46622	N/A	50BF Pole#1 Enable/Disable	—	—
206	1	46631	N/A	50BF Pole#2 Enable/Disable	—	—
207	1	8564	N/A	81#1 Enable/Disable	—	—
208	1	8573	N/A	81#2 Enable/Disable	—	—
209	1	8582	N/A	81#3 Enable/Disable	—	—
210	1	8591	N/A	81#4 Enable/Disable	—	—
211	1	46712	N/A	81#5 Enable/Disable	—	—
212	1	46721	N/A	81#6 Enable/Disable	—	—
213	1	46730	N/A	81#7 Enable/Disable	—	—
214	1	46739	N/A	81#8 Enable/Disable	—	—
215	1	8674	N/A	81R#1 Enable/Disable	—	—
216	1	8683	N/A	81R#2 Enable/Disable	—	—
217	1	46748	N/A	81R#3 Enable/Disable	—	—
218	1	46758	N/A	81R#4 Enable/Disable	—	—
219	1	46768	N/A	81R#5 Enable/Disable	—	—
220	1	46778	N/A	81R#6 Enable/Disable	—	—
221	1	46788	N/A	81R#7 Enable/Disable	—	—
222	1	46798	N/A	81R#8 Enable/Disable	—	—
223	1	46571	N/A	Cold Load Unbalanced Pole#1 Enable/Disable	—	—

Table 7 Binary Output Points List (CROB) (Pole 1 & Pole 2)

Binary Output Points List (CROB) (Pole 1 & Pole 2)

Control Relay Output Block (Direct Control Method)

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These points are written via: obj 12, var 01 using the Direct Operate command

These points are read via: obj 01, var 01

Configure the control points by selecting LATCH ON/OFF or TRIP/CLOSE or PULSE ON/OFF or INVERSE

Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
224	1	46594	N/A	Cold Load Unbalanced Pole#2 Enable/Disable	–	–
225	1	8774	N/A	HLT 50P Enable/Disable	–	–
226	1	8780	N/A	HLT 51P Enable/Disable	–	–
227	1	9102	N/A	IPSLOGIC#1 Enable/Disable	–	–
228	1	9106	N/A	IPSLOGIC#2 Enable/Disable	–	–
229	1	9110	N/A	IPSLOGIC#3 Enable/Disable	–	–
230	1	9114	N/A	IPSLOGIC#4 Enable/Disable	–	–
231	1	9118	N/A	IPSLOGIC#5 Enable/Disable	–	–
232	1	9122	N/A	IPSLOGIC#6 Enable/Disable	–	–
233	1	9126	N/A	IPSLOGIC#7 Enable/Disable	–	–
234	1	9130	N/A	IPSLOGIC#8 Enable/Disable	–	–
235	1	46045	N/A	Breaker Monitor #1 Enable/Disable	–	–
236	1	47472	N/A	Breaker Monitor #2 Enable/Disable	–	–
237	1	46640	N/A	Load Current Present Pole#1 Enable/Disable	–	–
238	1	46645	N/A	Load Current Present Pole#2 Enable/Disable	–	–
239	1	46650	N/A	HCA Pole#1 Enable/Disable	–	–
240	1	46653	N/A	HCA Pole#2 Enable/Disable	–	–
241	1	46451	N/A	79U Pole#1 Enable/Disable	–	–
242	1	46508	N/A	79U Pole#2 Enable/Disable	–	–
243	1	46657	N/A	Sectionalizer Pole#1 Enable/Disable	–	–
244	1	46683	N/A	Sectionalizer Pole#2 Enable/Disable	–	–
245	1	46319	N/A	Loop Scheme Pole#1 Enable/Disable	–	–
2456	1	46401	N/A	Loop Scheme Pole#2 Enable/Disable	–	–
247	1	25457	N/A	PSM Enable/Disable	–	–
248	1	6507	N/A	Battery Charger Enable/Disable	–	–
249	1	25432	N/A	TCM Enable/Disable	–	–
250	1	25437	N/A	CCM Enable/Disable	–	–
251	–	28016	N/A	THD #1 Pole#1 Enable/Disable	–	–

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Binary Output Points List (CROB) (Pole 1 & Pole 2)

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Configure the control points by selecting LATCH ON/OFF or TRIP/CLOSE or PULSE ON/OFF or INVERSE

Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
252	–	28034	N/A	THD #2 Pole#1 Enable/Disable	–	–
253	–	28022	N/A	THD #1 Pole#2 Enable/Disable	–	–
254	–	28040	N/A	THD #2 Pole#2 Enable/Disable	–	–
255	–	28069	N/A	Close Block Pole#1 Enable/Disable	–	–
256	–	28078	N/A	Close Block Pole#2 Enable/Disable	–	–
257	1	65002	N/A	GlobalLOGIC#1 Enable/Disable	–	–
258	1	65011	N/A	GlobalLOGIC#2 Enable/Disable	–	–
259	1	65020	N/A	GlobalLOGIC#3 Enable/Disable	–	–
260	1	65029	N/A	GlobalLOGIC#4 Enable/Disable	–	–
261	1	65038	N/A	GlobalLOGIC#5 Enable/Disable	–	–
262	1	65047	N/A	GlobalLOGIC#6 Enable/Disable	–	–
263	1	65056	N/A	GlobalLOGIC#7 Enable/Disable	–	–
264	1	65065	N/A	GlobalLOGIC#8 Enable/Disable	–	–
265	–	1400	N/A	OPEN Pole#1 and Pole#2 with Remote Disable	–	–
266	–	1404	N/A	OPEN Pole#1 with Remote Disable	–	–
267	–	1166	N/A	Reset Switching Device Operations Pole#1	–	–
268	–	1168	N/A	Reset Switching Device Operations Pole#2	–	–
269	–	1405	N/A	OPEN Pole#2 with Remote Disable	–	–
270	–	1419	N/A	Disable Fast/Slow Curves Pole#1 and Pole#2	–	–
271	–	1419	N/A	Fast Curve command Pole#1 and Pole#2	–	–
272	–	1419	N/A	Slow Curve command Pole#1 and Pole#2	–	–
273	–	1419	N/A	Skip Fast Curve command Pole#1 and Pole#2	–	–
274	–	20	N/A	Profile toggle Alternate Prof 1 / Previous Active Prof X	–	–
275	–	20	N/A	Profile toggle Alternate Prof 2 / Previous Active Prof X	–	–
276	–	20	N/A	Profile toggle Alternate Prof 3 / Previous Active Prof X	–	–
277	–	20	N/A	Profile toggle Alternate Prof 4 / Previous Active Prof X	–	–
278	–	20	N/A	Profile toggle Alternate Prof 5 / Previous Active Prof X	–	–
279	–	20	N/A	Profile toggle Alternate Prof 6 / Previous Active Prof X	–	–

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Configure the control points by selecting LATCH ON/OFF or TRIP/CLOSE or PULSE ON/OFF or INVERSE

Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
280	–	20	N/A	Profile toggle Alternate Prof 7 / Previous Active Prof X	–	–
281	–	20	N/A	Profile toggle Alternate Prof 8 / Previous Active Prof X	–	–
282	–	20	N/A	Profile toggle Alternate Prof 1 / Default Active Prof	–	–
283	–	20	N/A	Profile toggle Alternate Prof 2 / Default Active Prof	–	–
284	–	20	N/A	Profile toggle Alternate Prof 3 / Default Active Prof	–	–
285	–	20	N/A	Profile toggle Alternate Prof 4 / Default Active Prof	–	–
286	–	20	N/A	Profile toggle Alternate Prof 5 / Default Active Prof	–	–
287	–	20	N/A	Profile toggle Alternate Prof 6 / Default Active Prof	–	–
288	–	20	N/A	Profile toggle Alternate Prof 7 / Default Active Prof	–	–
289	–	20	N/A	Profile toggle Alternate Prof 8 / Default Active Prof	–	–
290	–	3917	N/A	Reclose Disable	–	–
291	–	3917	N/A	Reclose Enable	–	–
292	–	3942	N/A	Sectionalizer Disable	–	–
293	–	46303	N/A	IHR 50P #1 Enable/Disable	–	–
294	–	46303	N/A	IHR 50P #2 Enable/Disable	–	–
295	–	46303	N/A	IHR 50P #3 Enable/Disable	–	–
296	–	46303	N/A	IHR 50P #4 Enable/Disable	–	–
297	–	46305	N/A	IHR 51P #1 Enable/Disable	–	–
298	–	46305	N/A	IHR 51P #2 Enable/Disable	–	–
299	–	46305	N/A	IHR 51P #3 Enable/Disable	–	–
300	–	46305	N/A	IHR 51P #4 Enable/Disable	–	–
301	–	4185	N/A	LOCK Enable/Disable	–	–
302	–	46708	N/A	One Shot Sectionalizing Pole#1 Enable/Disable	–	–
303	–	46709	N/A	One Shot Sectionalizing Pole#2 Enable/Disable	–	–
304	–	20	N/A	Change Active Prof to Default	–	–
305	–	20	N/A	Change Active Prof to Alternate	–	–
306	–	46319	N/A	Loop Auto Pole#1 Disable	–	–
307	–	46319	N/A	Loop Auto Pole#1 Enable	–	–

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Configure the control points by selecting LATCH ON/OFF or TRIP/CLOSE or PULSE ON/OFF or INVERSE

Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
308	—	46401	N/A	Loop Auto Pole#2 Disable	—	—
309	—	46401	N/A	Loop Auto Pole#2 Enable	—	—
310	—	1402	N/A	Hot Line Tag Off Control	—	—
311	—	1402	N/A	Hot Line Tag On Control	—	—
312	—	1419	N/A	Turn Fast Operate Off Pole#1	—	—
313	—	1419	N/A	Turn Fast Operate On Pole#1	—	—
314	—	1420	N/A	Turn Fast Operate Off Pole#2	—	—
315	—	1420	N/A	Turn Fast Operate On Pole#2	—	—
316	—	1419	N/A	Turn Fast Operate Off Pole#1 and Pole#2	—	—
317	—	1419	N/A	Turn Fast Operate On Pole#1 and Pole#2	—	—
318	—	3917	N/A	Reclosing (79) Block/Unblock	—	—
319	—	4473	N/A	Any Phase Protection Function Block/Unblock	—	—
320	—	4473	N/A	Any Overcurrent Block/Unblock	—	—
321	—	4473	N/A	Any Phase Overcurrent Block/Unblock	—	—
322	—	4473	N/A	Any Protection Function Block/Unblock	—	—
323	—	4473	N/A	Any Overvoltage Function Block/Unblock	—	—
324	—	4473	N/A	Any Undervoltage Function Block/Unblock	—	—
325	—		N/A	Select Pole 1/Pole 2/Both Poles	—	—
326	—	4413	N/A	F27 #1 Pole#1 Block/Unblock	—	—
327	—	4413	N/A	F27 #1 Pole#2 Block/Unblock	—	—
328	—	4413	N/A	F27 #1 Both Poles Block/Unblock	—	—
329	—	4413	N/A	F27 #2 Pole#1 Block/Unblock	—	—
330	—	4413	N/A	F27 #2 Pole#2 Block/Unblock	—	—
331	—	4413	N/A	F27 #2 Both Poles Block/Unblock	—	—
332	—	4413	N/A	F27 #3 Pole#1 Block/Unblock	—	—
333	—	4413	N/A	F27 #3 Pole#2 Block/Unblock	—	—
334	—	4413	N/A	F27 #3 Both Poles Block/Unblock	—	—
335	—	4413	N/A	F27 #4 Pole#1 Block/Unblock	—	—

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Configure the control points by selecting LATCH ON/OFF or TRIP/CLOSE or PULSE ON/OFF or INVERSE

Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
336	—	4413	N/A	F27 #4 Pole#2 Block/Unblock	—	—
337	—	4413	N/A	F27 #4 Both Poles Block/Unblock	—	—
338	—	4418	N/A	F59 #1 Pole#1 Block/Unblock	—	—
339	—	4418	N/A	F59 #1 Pole#2 Block/Unblock	—	—
340	—	4418	N/A	F59 #1 Both Poles Block/Unblock	—	—
341	—	4418	N/A	F59 #2 Pole#1 Block/Unblock	—	—
342	—	4418	N/A	F59 #2 Pole#2 Block/Unblock	—	—
343	—	4418	N/A	F59 #2 Both Poles Block/Unblock	—	—
344	—	4419	N/A	F59 #3 Pole#1 Block/Unblock	—	—
345	—	4419	N/A	F59 #3 Pole#2 Block/Unblock	—	—
346	—	4419	N/A	F59 #3 Both Poles Block/Unblock	—	—
347	—	4419	N/A	F59 #4 Pole#1 Block/Unblock	—	—
348	—	4419	N/A	F59 #4 Pole#2 Block/Unblock	—	—
349	—	4419	N/A	F59 #4 Both Poles Block/Unblock	—	—
350	—	4423	N/A	F59I Pole#1 Block/Unblock	—	—
351	—	4423	N/A	F59I Pole#2 Block/Unblock	—	—
352	—	4423	N/A	F59I Both Poles Block/Unblock	—	—
353	—	4414	N/A	F50P #1 Pole#1 Block/Unblock	—	—
354	—	4414	N/A	F50P #1 Pole#2 Block/Unblock	—	—
355	—	4414	N/A	F50P #1 Both Poles Block/Unblock	—	—
356	—	4415	N/A	F50P #2 Pole#1 Block/Unblock	—	—
357	—	4415	N/A	F50P #2 Pole#2 Block/Unblock	—	—
358	—	4415	N/A	F50P #2 Both Poles Block/Unblock	—	—
359	—	4415	N/A	F50P #3 Pole#1 Block/Unblock	—	—
360	—	4415	N/A	F50P #3 Pole#2 Block/Unblock	—	—
361	—	4415	N/A	F50P #3 Both Poles Block/Unblock	—	—
362	—	4415	N/A	F50P #4 Pole#1 Block/Unblock	—	—
363	—	4415	N/A	F50P #4 Pole#2 Block/Unblock	—	—

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Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
364	—	4415	N/A	F50P #4 Both Poles Block/Unblock	—	—
365	—	4416	N/A	F50BF Pole#1 Block/Unblock	—	—
366	—	4433	N/A	F50BF Pole#2 Block/Unblock	—	—
367	—	4416	N/A	F51P #1 Pole#1 Block/Unblock	—	—
368	—	4416	N/A	F51P #1 Pole#2 Block/Unblock	—	—
369	—	4416	N/A	F51P #1 Both Poles Block/Unblock	—	—
370	—	4416	N/A	F51P #2 Pole#1 Block/Unblock	—	—
371	—	4417	N/A	F51P #2 Pole#2 Block/Unblock	—	—
372	—	4474	N/A	F51P #2 Both Poles Block/Unblock	—	—
373	—	4417	N/A	F51P #3 Pole#1 Block/Unblock	—	—
374	—	4417	N/A	F51P #3 Pole#2 Block/Unblock	—	—
375	—	4417	N/A	F51P #3 Both Poles Block/Unblock	—	—
376	—	4417	N/A	F51P #4 Pole#1 Block/Unblock	—	—
377	—	4417	N/A	F51P #4 Pole#2 Block/Unblock	—	—
378	—	4417	N/A	F51P #4 Both Poles Block/Unblock	—	—
379	—	4421	N/A	F81 #1 Block/Unblock	—	—
380	—	4421	N/A	F81 #2 Block/Unblock	—	—
381	—	4421	N/A	F81 #3 Block/Unblock	—	—
382	—	4422	N/A	F81 #4 Block/Unblock	—	—
383	—	4433	N/A	F81 #5 Block/Unblock	—	—
384	—	4434	N/A	F81 #6 Block/Unblock	—	—
385	—	4434	N/A	F81 #7 Block/Unblock	—	—
386	—	4434	N/A	F81 #8 Block/Unblock	—	—
387	—	4474	N/A	All F81 Block/Unblock	—	—
388	—	3942	N/A	Over Freq Block/Unblock	—	—
389	—	3942	N/A	Under Freq Block/Unblock	—	—
390	—	4422	N/A	F81R #1 Block/Unblock	—	—
391	—	4422	N/A	F81R #2 Block/Unblock	—	—

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Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
392	—	4434	N/A	F81R #3 Block/Unblock	—	—
393	—	4441	N/A	F81R #4 Block/Unblock	—	—
394	—	4441	N/A	F81R #5 Block/Unblock	—	—
395	—	4441	N/A	F81R #6 Block/Unblock	—	—
396	—	4441	N/A	F81R #7 Block/Unblock	—	—
397	—	4441	N/A	F81R #8 Block/Unblock	—	—
398	—	4474	N/A	All F81R Block/Unblock	—	—
399	—	4474	N/A	All Freq Funcs Block/Unblock	—	—
400	—	4414	N/A	F32 #1 Pole#1 Block/Unblock	—	—
401	—	4414	N/A	F32 #1 Pole#2 Block/Unblock	—	—
402	—	4414	N/A	F32 #1 Both Poles Block/Unblock	—	—
403	—	4414	N/A	F32 #2 Pole#1 Block/Unblock	—	—
404	—	4414	N/A	F32 #2 Pole#2 Block/Unblock	—	—
405	—	4414	N/A	F32 #2 Both Poles Block/Unblock	—	—
406	—	4414	N/A	F32 #3 Pole#1 Block/Unblock	—	—
407	—	4414	N/A	F32 #3 Pole#2 Block/Unblock	—	—
408	—	4414	N/A	F32 #3 Both Poles Block/Unblock	—	—
409	—	4414	N/A	F32 #4 Pole#1 Block/Unblock	—	—
410	—	4414	N/A	F32 #4 Pole#2 Block/Unblock	—	—
411	—	4414	N/A	F32 #4 Both Poles Block/Unblock	—	—
412	—	3942	N/A	F25 Synchrocheck Magnitude Block/Unblock	—	—
413	—	3942	N/A	F25 Synchrocheck Angle Block/Unblock	—	—
414	—	3942	N/A	F25 Synchrocheck Freq Block/Unblock	—	—
415	—	4413	N/A	F25 Pole#1 Block/Unblock	—	—
416	—	4432	N/A	F25 Pole#2 Block/Unblock	—	—
417	—	4433	N/A	F79 Unbalanced 51 Pole#1 Block/Unblock	—	—
418	—	4433	N/A	F79 Unbalanced 51 Pole#2 Block/Unblock	—	—
419	—	4433	N/A	F79 Unbalanced HCT Pole#1 Block/Unblock	—	—

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Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
420	—	4433	N/A	F79 Unbalanced HCT Pole#2 Block/Unblock	—	—
421	—	4433	N/A	F79 Unbalanced HCL Pole#1 Block/Unblock	—	—
422	—	4434	N/A	F79 Unbalanced HCL Pole#2 Block/Unblock	—	—
423	—	4421	N/A	F74 Trip Coil Block/Unblock Block/Unblock	—	—
424	—	4421	N/A	F74 Close Coil Block/Unblock Block/Unblock	—	—
425	—	4433	N/A	LCP Pole#1 Block/Unblock Block/Unblock	—	—
426	—	4433	N/A	LCP Pole#2 Block/Unblock Block/Unblock	—	—
427	—	4433	N/A	LOS Pole#1 Block/Unblock Block/Unblock	—	—
428	—	4433	N/A	LOS Pole#2 Block/Unblock Block/Unblock	—	—
429	—	47482	N/A	F50P Minimum Trip Pickup in HLT	—	—
430	—	47482	N/A	F51P Minimum Trip Pickup in HLT	—	—
431	--	28070	N/A	Close Block Pole#1 Freq Enable/Disable	—	—
432	--	28079	N/A	Close Block Pole#2 Freq Enable/Disable	—	—
433	--	28070	N/A	Close Block Pole#1 Volt Upper Limit Enable/Disable	—	—
434	--	28079	N/A	Close Block Pole#2 Volt Upper Limit Enable/Disable	—	—
435	--	28070	N/A	Close Block Pole#1 Volt Lower Limit Enable/Disable	—	—
436	--	28079	N/A	Close Block Pole#2 Volt Lower Limit Enable/Disable	—	—
437	--	28070	N/A	Close Block Pole#1 Volt Upper/Lower Limit Enable/Disable	—	—
438	--	28079	N/A	Close Block Pole#2 Volt Upper/Lower Limit Enable/Disable	—	—
439	--	2002	N/A	Reset Peak Demands	—	—
440	--	1403	N/A	Reset Front Panel Targets ONLY	—	—
441	--	62	N/A	Source Orientation Vy	—	—
442	--	62	N/A	Source Orientation Vz	—	—
443	--	1419	N/A	Slow Elements Only (TSlow) Pole#1	—	—
444	--	1420	N/A	Slow Elements Only (TSlow) Pole#2	—	—
445	--	1538	N/A	Activate Blocking Func. S10	—	—
446	--	1538	N/A	Activate Blocking Func. S11	—	—

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These points are read via: obj 01, var 01

Configure the control points by selecting LATCH ON/OFF or TRIP/CLOSE or PULSE ON/OFF or INVERSE

Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
447	--	1538	N/A	Activate Blocking Func. S12	—	—
448	--	1538	N/A	Activate Blocking Func. S13	—	—
449	--	1538	N/A	Activate Blocking Func. S14	—	—
450	--	1538	N/A	Activate Blocking Func. S15	—	—
451	--	1538	N/A	Activate Blocking Func. S16	—	—
452	--	1538	N/A	Activate Blocking Func. S17	—	—

Table 7 Binary Output Points List (CROB) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
0	—	1743	N/A	Current IP address	—	—
1	—	1745	N/A	CURRENT NET MASK ADDRESS	—	—
2	—	1747	N/A	CURRENT GATEWAY ADDRESS	—	—
3	—	31	N/A	Nominal Frequency	—	—
4	—	32	N/A	Secondary CT Rating	—	—
5	—	2200	N/A	Real component of Pole#1 current phasor	—	—
6	—	2201	N/A	Reactive component of Pole#1 current phasor	—	—
7	—	2202	N/A	Real component of Pole#2 current phasor	—	—
8	—	2203	N/A	Reactive component of Pole#2 current phasor	—	—
9	—	2208	N/A	Real component of Pole#1 voltage phasor	—	—
10	—	2209	N/A	Reactive component of Pole#1 voltage phasor	—	—
11	—	2210	N/A	Real component of Pole#2 voltage phasor	—	—
12	—	2211	N/A	Reactive component of Pole#2 voltage phasor	—	—
13	—	1940	N/A	Real component of Pole#2 voltage phasor load side	—	—
14	—	1941	N/A	Reactive component of Pole#2 voltage phasor load side	—	—
15	—	2214	N/A	Real component of Vz1 phasor/Pole#1 load side	—	—
16	—	2215	N/A	Reactive component of Vz1 phasor/Pole#1 load side	—	—
17	100	2222	N/A	Measured frequency	—	—
18	—	4654	N/A	Measured frequency on Z-side	—	—
19	—	2223	N/A	Rate of change of frequency	—	—
20	—	4655	N/A	Rate of change of frequency on Z-side	—	—
21	100	2225	N/A	Real Pole#1 Power in Sec Watts	—	—
22	100	2227	N/A	Reactive Pole#1 Power in Sec var	—	—
23	100	2229	N/A	Real Pole#2 Power in Sec Watts	—	—
24	100	2231	N/A	Reactive Pole#2 Power in Sec var	—	—
25	100	2241	N/A	Apparent Pole#1 Power in Sec VA	—	—
26	100	2243	N/A	Apparent Pole#2 Power in Sec VA	—	—
27	—	1884	N/A	Phase Angle Voltage Pole#1 Y-side in deg	—	—
28	—	1885	N/A	Phase Angle Voltage Pole#2 Y-side in deg	—	—
29	—	1887	N/A	Phase Angle Voltage Pole#1 Z-side/Vx in deg	—	—
30	—	1955	N/A	Phase Angle Voltage Pole#2 Z-side in deg	—	—
31	—	1888	N/A	Phase Angle Current Pole#1 in deg	—	—
32	—	1889	N/A	Phase Angle Current Pole#2 in deg	—	—
33	1000	2249	N/A	Pole#1 power factor	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
34	1000	2250	N/A	Pole#2 power factor	—	—
35	1000	2275	N/A	Sec Pole#1 Current Magnitude	—	—
36	1000	2276	N/A	Sec Pole#2 Current Magnitude	—	—
37	100	2279	N/A	Sec Pole#1 Voltage Magnitude	—	—
38	100	2280	N/A	Sec Pole#2 Voltage Magnitude	—	—
39	100	1942	N/A	Sec Pole#2 Voltage Magnitude load side	—	—
40	100	2282	N/A	Sec Sync Voltage/Pole#1 load side Magnitude	—	—
41	1000	2283	N/A	Demand Pole#1 Current	—	—
42	1000	2284	N/A	Demand Pole#2 Current	—	—
43	100	2287	N/A	Demand Pole#1 Voltage	—	—
44	100	2288	N/A	Demand Pole#2 Voltage	—	—
45	100	2290	N/A	Demand Sync/Pole#1 load side Voltage	—	—
46	100	2291	N/A	Demand Pole#1 Real Power	—	—
47	100	2293	N/A	Demand Pole#2 Real Power	—	—
48	100	2299	N/A	Demand Pole#1 Reactive Power	—	—
49	100	2301	N/A	Demand Pole#2 Reactive Power	—	—
50	100	2307	N/A	Demand Pole#1 Apparent Power	—	—
51	100	2309	N/A	Demand Pole#2 Apparent Power	—	—
52	1000	2315	N/A	Maximum Pole#1 Current	—	—
53	1000	2316	N/A	Maximum Pole#2 Current	—	—
54	100	2319	N/A	Maximum Pole#1 Voltage	—	—
55	100	2320	N/A	Maximum Pole#2 Voltage	—	—
56	100	2322	N/A	Maximum Sync/Pole#1 load side Voltage	—	—
57	100	2323	N/A	Maximum Pole#1 Real Power	—	—
58	100	2325	N/A	Maximum Pole#2 Real Power	—	—
59	100	2331	N/A	Maximum Pole#1 Reactive Power	—	—
60	100	2333	N/A	Maximum Pole#2 Reactive Power	—	—
61	100	2339	N/A	Maximum Pole#1 Apparent Power	—	—
62	100	2341	N/A	Maximum Pole#2 Apparent Power	—	—
63	1	2347	N/A	Time of Max Pole#1 Current Demand	—	—
64	1	2349	N/A	Time of Max Pole#2 Current Demand	—	—
65	1	2355	N/A	Time of Max Pole#1 Voltage Demand	—	—
66	1	2357	N/A	Time of Max Pole#2 Voltage Demand	—	—
67	1	2361	N/A	Time of Max sync/Pole#1 load side Voltage Demand	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
68	1	2363	N/A	Time of Max Pole#1 Real Power Demand	—	—
69	1	2365	N/A	Time of Max Pole#2 Real Power Demand	—	—
70	1	2371	N/A	Time of Max Pole#1 Reactive Power Demand	—	—
71	1	2373	N/A	Time of Max Pole#2 Reactive Power Demand	—	—
72	1	2379	N/A	Time of Max Pole#1 Apparent Power Demand	—	—
73	1	2381	N/A	Time of Max Pole#2 Apparent Power Demand	—	—
74	1000	2387	N/A	Minimum Pole#1 Current	—	—
75	1000	2388	N/A	Minimum Pole#2 Current	—	—
76	100	2391	N/A	Minimum Pole#1 Voltage	—	—
77	100	2392	N/A	Minimum Pole#2 Voltage	—	—
78	100	2394	N/A	Minimum Sync/Pole#1 load side Voltage	—	—
79	100	2395	N/A	Minimum Pole#1 Real Power	—	—
80	100	2397	N/A	Minimum Pole#2 Real Power	—	—
81	100	2403	N/A	Minimum Pole#1 Reactive Power	—	—
82	100	2405	N/A	Minimum Pole#2 Reactive Power	—	—
83	100	2411	N/A	Minimum Pole# Apparent Power	—	—
84	100	2413	N/A	Minimum Pole#2 Apparent Power	—	—
85	1	2419	N/A	Time of Min Pole#1 Current Demand	—	—
86	1	2421	N/A	Time of Min Pole#2 Current Demand	—	—
87	1	2427	N/A	Time of Min Pole#1 Voltage Demand	—	—
88	1	2429	N/A	Time of Min Pole#2 Voltage Demand	—	—
89	1	2433	N/A	Time of Min Sync/Pole#1 load side Voltage Demand	—	—
90	1	2435	N/A	Time of Min Pole#1 Real Power Demand	—	—
91	1	2437	N/A	Time of Min Pole#2 Real Power Demand	—	—
92	1	2443	N/A	Time of Min Pole#1 Reactive Power Demand	—	—
93	1	2445	N/A	Time of Min Pole#2 Reactive Power Demand	—	—
94	1	2451	N/A	Time of Min Pole#1 Apparent Power Demand	—	—
95	1	2453	N/A	Time of Min Pole#2 Apparent Power Demand	—	—
96	1000	1908	N/A	Sec Real Energy kWh Pole#1 Fwd	—	—
97	1	2461	N/A	Time of Real Energy Pole#1 Fwd	—	—
98	1000	1910	N/A	Sec Real Energy kWh Pole#1 Rev	—	—
99	1	2465	N/A	Time of Real Energy Pole#1 Rev	—	—
100	1000	1912	N/A	Sec Reactive Energy kVarh Pole#1 Lead	—	—
101	1	2469	N/A	Time of Reactive Energy Pole#1 Lead	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
102	1000	1914	N/A	Reactive Energy kvarh Pole#1 Lag	—	—
103	1	2473	N/A	Time of Reactive Energy Pole#1 Lag	—	—
104	1000	1916	N/A	Sec Real Energy kWh Pole#2 Fwd	—	—
105	1	2477	N/A	Time of Real Energy Pole#2 Fwd	—	—
106	1000	1918	N/A	Sec Real Energy kWh Pole#2 Rev	—	—
107	1	2481	N/A	Time of Real Energy Pole#2 Rev	—	—
108	1000	1920	N/A	Sec Reactive Energy kvarh Pole#2 Lead	—	—
109	1	2485	N/A	Time of Reactive Energy Pole#2 Lead	—	—
110	1000	1922	N/A	Sec Reactive Energy kvarh Pole#2 Lag	—	—
111	1	2489	N/A	Time of Reactive Energy Pole#2 Lag	—	—
112	1	4918	N/A	Pri Real Energy kWh Pole#1 Fwd	—	—
113	1	4924	N/A	Pri Real Energy kWh Pole#1 Rev	—	—
114	1	4930	N/A	Pri Reactive Energy kvarh Pole#1 Lead	—	—
115	1	4936	N/A	Pri Reactive Energy kvarh Pole#1 Lag	—	—
116	1	4920	N/A	Pri Real Energy kWh Pole#2 Fwd	—	—
117	1	4926	N/A	Pri Real Energy kWh Pole#2 Rev	—	—
118	1	4932	N/A	Pri Reactive Energy kvarh Pole#2 Lead	—	—
119	1	4938	N/A	Pri Reactive Energy kvarh Pole#2 Lag	—	—
120	1	2461	N/A	Last Energy Reset Time	—	—
121	1	1800	N/A	Primary Pole#1 Current Magnitude	—	—
122	1	1802	N/A	Primary Pole#2 Current Magnitude	—	—
123	1	1808	N/A	Primary Pole#1 Voltage Magnitude	—	—
124	1	1810	N/A	Primary Pole#2 Voltage Magnitude	—	—
125	1	1950	N/A	Primary Pole#2 Voltage Magnitude load side	—	—
126	1	1814	N/A	Primary Sync Voltage/Pole#1 load side Magnitude	—	—
127	1	1514	N/A	Primary Real Pole#1 Power in Watts	—	—
128	1	1516	N/A	Primary Reactive Pole#1 Power in var	—	—
129	1	1518	N/A	Primary Real Pole#2 Power in Watts	—	—
130	1	1520	N/A	Primary Reactive Pole#2 Power in var	—	—
131	1	1530	N/A	Primary Magnitude of Pole#1 apparent power in va	—	—
132	1	1532	N/A	Primary Magnitude of Pole#2 apparent power in va	—	—
133	—	3796	N/A	Battery Voltage	—	—
134	—	2513	N/A	Sequence Coordination	—	—
135	—	3769	N/A	Pole#1 current Total Harmonic Distortion	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
136	—	3384	N/A	Current 1st Harmonic Pole#1	—	—
137	—	3390	N/A	Current 2nd Harmonic Pole#1	—	—
138	—	3396	N/A	Current 3rd Harmonic Pole#1	—	—
139	—	3402	N/A	Current 4th Harmonic Pole#1	—	—
140	—	3408	N/A	Current 5th Harmonic Pole#1	—	—
141	—	3414	N/A	Current 6th Harmonic Pole#1	—	—
142	—	3420	N/A	Current 7th Harmonic Pole#1	—	—
143	—	3426	N/A	Current 8th Harmonic Pole#1	—	—
144	—	3432	N/A	Current 9th Harmonic Pole#1	—	—
145	—	3438	N/A	Current 10th Harmonic Pole#1	—	—
146	—	3444	N/A	Current 11th Harmonic Pole#1	—	—
147	—	3450	N/A	Current 12th Harmonic Pole#1	—	—
148	—	3456	N/A	Current 13th Harmonic Pole#1	—	—
149	—	3462	N/A	Current 14th Harmonic Pole#1	—	—
150	—	3468	N/A	Current 15th Harmonic Pole#1	—	—
151	—	3474	N/A	Current 16th Harmonic Pole#1	—	—
152	—	3480	N/A	Current 17th Harmonic Pole#1	—	—
153	—	3486	N/A	Current 18th Harmonic Pole#1	—	—
154	—	3492	N/A	Current 19th Harmonic Pole#1	—	—
155	—	3498	N/A	Current 20th Harmonic Pole#1	—	—
156	—	3504	N/A	Current 21st Harmonic Pole#1	—	—
157	—	3510	N/A	Current 22nd Harmonic Pole#1	—	—
158	—	3516	N/A	Current 23rd Harmonic Pole#1	—	—
159	—	3522	N/A	Current 24th Harmonic Pole#1	—	—
160	—	3528	N/A	Current 25th Harmonic Pole#1	—	—
161	—	3534	N/A	Current 26th Harmonic Pole#1	—	—
162	—	3540	N/A	Current 27th Harmonic Pole#1	—	—
163	—	3546	N/A	Current 28th Harmonic Pole#1	—	—
164	—	3552	N/A	Current 29th Harmonic Pole#1	—	—
165	—	3558	N/A	Current 30th Harmonic Pole#1	—	—
166	—	3564	N/A	Current 31st Harmonic Pole#1	—	—
167	—	3570	N/A	Current 32nd Harmonic Pole#1	—	—
168	—	3576	N/A	Current 33rd Harmonic Pole#1	—	—
169	—	3582	N/A	Current 34th Harmonic Pole#1	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
170	—	3588	N/A	Current 35th Harmonic Pole#1	—	—
171	—	3594	N/A	Current 36th Harmonic Pole#1	—	—
172	—	3600	N/A	Current 37th Harmonic Pole#1	—	—
173	—	3606	N/A	Current 38th Harmonic Pole#1	—	—
174	—	3612	N/A	Current 39th Harmonic Pole#1	—	—
175	—	3618	N/A	Current 40th Harmonic Pole#1	—	—
176	—	3624	N/A	Current 41st Harmonic Pole#1	—	—
177	—	3630	N/A	Current 42nd Harmonic Pole#1	—	—
178	—	3636	N/A	Current 43rd Harmonic Pole#1	—	—
179	—	3642	N/A	Current 44th Harmonic Pole#1	—	—
180	—	3648	N/A	Current 45th Harmonic Pole#1	—	—
181	—	3654	N/A	Current 46th Harmonic Pole#1	—	—
182	—	3660	N/A	Current 47th Harmonic Pole#1	—	—
183	—	3666	N/A	Current 48th Harmonic Pole#1	—	—
184	—	3672	N/A	Current 49th Harmonic Pole#1	—	—
185	—	3678	N/A	Current 50th Harmonic Pole#1	—	—
186	—	3684	N/A	Current 51st Harmonic Pole#1	—	—
187	—	3690	N/A	Current 52nd Harmonic Pole#1	—	—
188	—	3696	N/A	Current 53rd Harmonic Pole#1	—	—
189	—	3702	N/A	Current 54th Harmonic Pole#1	—	—
190	—	3708	N/A	Current 55th Harmonic Pole#1	—	—
191	—	3714	N/A	Current 56th Harmonic Pole#1	—	—
192	—	3720	N/A	Current 57th Harmonic Pole#1	—	—
193	—	3726	N/A	Current 58th Harmonic Pole#1	—	—
194	—	3732	N/A	Current 59th Harmonic Pole#1	—	—
195	—	3738	N/A	Current 60th Harmonic Pole#1	—	—
196	—	3744	N/A	Current 61st Harmonic Pole#1	—	—
197	—	3750	N/A	Current 62nd Harmonic Pole#1	—	—
198	—	3756	N/A	Current 63rd Harmonic Pole#1	—	—
199	—	3770	N/A	Pole#2 current Total Harmonic Distortion	—	—
200	—	3385	N/A	Current 1st Harmonic Pole#2	—	—
201	—	3391	N/A	Current 2nd Harmonic Pole#2	—	—
202	—	3397	N/A	Current 3rd Harmonic Pole#2	—	—
203	—	3403	N/A	Current 4th Harmonic Pole#2	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
204	—	3409	N/A	Current 5th Harmonic Pole#2	—	—
205	—	3415	N/A	Current 6th Harmonic Pole#2	—	—
206	—	3421	N/A	Current 7th Harmonic Pole#2	—	—
207	—	3427	N/A	Current 8th Harmonic Pole#2	—	—
208	—	3433	N/A	Current 9th Harmonic Pole#2	—	—
209	—	3439	N/A	Current 10th Harmonic Pole#2	—	—
210	—	3445	N/A	Current 11th Harmonic Pole#2	—	—
211	—	3451	N/A	Current 12th Harmonic Pole#2	—	—
212	—	3457	N/A	Current 13th Harmonic Pole#2	—	—
213	—	3463	N/A	Current 14th Harmonic Pole#2	—	—
214	—	3469	N/A	Current 15th Harmonic Pole#2	—	—
215	—	3475	N/A	Current 16th Harmonic Pole#2	—	—
216	—	3481	N/A	Current 17th Harmonic Pole#2	—	—
217	—	3487	N/A	Current 18th Harmonic Pole#2	—	—
218	—	3493	N/A	Current 19th Harmonic Pole#2	—	—
219	—	3499	N/A	Current 20th Harmonic Pole#2	—	—
220	—	3505	N/A	Current 21st Harmonic Pole#2	—	—
221	—	3511	N/A	Current 22nd Harmonic Pole#2	—	—
222	—	3517	N/A	Current 23rd Harmonic Pole#2	—	—
223	—	3523	N/A	Current 24th Harmonic Pole#2	—	—
224	—	3529	N/A	Current 25th Harmonic Pole#2	—	—
225	—	3535	N/A	Current 26th Harmonic Pole#2	—	—
226	—	3541	N/A	Current 27th Harmonic Pole#2	—	—
227	—	3547	N/A	Current 28th Harmonic Pole#2	—	—
228	—	3553	N/A	Current 29th Harmonic Pole#2	—	—
229	—	3559	N/A	Current 30th Harmonic Pole#2	—	—
230	—	3565	N/A	Current 31st Harmonic Pole#2	—	—
231	—	3571	N/A	Current 32nd Harmonic Pole#2	—	—
232	—	3577	N/A	Current 33rd Harmonic Pole#2	—	—
233	—	3583	N/A	Current 34th Harmonic Pole#2	—	—
234	—	3589	N/A	Current 35th Harmonic Pole#2	—	—
235	—	3595	N/A	Current 36th Harmonic Pole#2	—	—
236	—	3601	N/A	Current 37th Harmonic Pole#2	—	—
237	—	3607	N/A	Current 38th Harmonic Pole#2	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
238	—	3613	N/A	Current 39th Harmonic Pole#2	—	—
239	—	3619	N/A	Current 40th Harmonic Pole#2	—	—
240	—	3625	N/A	Current 41st Harmonic Pole#2	—	—
241	—	3631	N/A	Current 42nd Harmonic Pole#2	—	—
242	—	3637	N/A	Current 43rd Harmonic Pole#2	—	—
243	—	3643	N/A	Current 44th Harmonic Pole#2	—	—
244	—	3649	N/A	Current 45th Harmonic Pole#2	—	—
245	—	3655	N/A	Current 46th Harmonic Pole#2	—	—
246	—	3661	N/A	Current 47th Harmonic Pole#2	—	—
247	—	3667	N/A	Current 48th Harmonic Pole#2	—	—
248	—	3673	N/A	Current 49th Harmonic Pole#2	—	—
249	—	3679	N/A	Current 50th Harmonic Pole#2	—	—
250	—	3685	N/A	Current 51st Harmonic Pole#2	—	—
251	—	3691	N/A	Current 52nd Harmonic Pole#2	—	—
252	—	3697	N/A	Current 53rd Harmonic Pole#2	—	—
253	—	3703	N/A	Current 54th Harmonic Pole#2	—	—
254	—	3709	N/A	Current 55th Harmonic Pole#2	—	—
255	—	3715	N/A	Current 56th Harmonic Pole#2	—	—
256	—	3721	N/A	Current 57th Harmonic Pole#2	—	—
257	—	3727	N/A	Current 58th Harmonic Pole#2	—	—
258	—	3733	N/A	Current 59th Harmonic Pole#2	—	—
259	—	3739	N/A	Current 60th Harmonic Pole#2	—	—
260	—	3745	N/A	Current 61st Harmonic Pole#2	—	—
261	—	3751	N/A	Current 62nd Harmonic Pole#2	—	—
262	—	3757	N/A	Current 63rd Harmonic Pole#2	—	—
263	—	3772	N/A	Pole#1 Voltage Total Harmonic Distortion	—	—
264	—	3387	N/A	Voltage 1st Harmonic Pole#1	—	—
265	—	3393	N/A	Voltage 2nd Harmonic Pole#1	—	—
266	—	3399	N/A	Voltage 3rd Harmonic Pole#1	—	—
267	—	3405	N/A	Voltage 4th Harmonic Pole#1	—	—
268	—	3411	N/A	Voltage 5th Harmonic Pole#1	—	—
269	—	3417	N/A	Voltage 6th Harmonic Pole#1	—	—
270	—	3423	N/A	Voltage 7th Harmonic Pole#1	—	—
271	—	3429	N/A	Voltage 8th Harmonic Pole#1	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
272	—	3435	N/A	Voltage 9th Harmonic Pole#1	—	—
273	—	3441	N/A	Voltage 10th Harmonic Pole#1	—	—
274	—	3447	N/A	Voltage 11th Harmonic Pole#1	—	—
275	—	3453	N/A	Voltage 12th Harmonic Pole#1	—	—
276	—	3459	N/A	Voltage 13th Harmonic Pole#1	—	—
277	—	3465	N/A	Voltage 14th Harmonic Pole#1	—	—
278	—	3471	N/A	Voltage 15th Harmonic Pole#1	—	—
279	—	3477	N/A	Voltage 16th Harmonic Pole#1	—	—
280	—	3483	N/A	Voltage 17th Harmonic Pole#1	—	—
281	—	3489	N/A	Voltage 18th Harmonic Pole#1	—	—
282	—	3495	N/A	Voltage 19th Harmonic Pole#1	—	—
283	—	3501	N/A	Voltage 20th Harmonic Pole#1	—	—
284	—	3507	N/A	Voltage 21st Harmonic Pole#1	—	—
285	—	3513	N/A	Voltage 22nd Harmonic Pole#1	—	—
286	—	3519	N/A	Voltage 23rd Harmonic Pole#1	—	—
287	—	3525	N/A	Voltage 24th Harmonic Pole#1	—	—
288	—	3531	N/A	Voltage 25th Harmonic Pole#1	—	—
289	—	3537	N/A	Voltage 26th Harmonic Pole#1	—	—
290	—	3543	N/A	Voltage 27th Harmonic Pole#1	—	—
291	—	3549	N/A	Voltage 28th Harmonic Pole#1	—	—
292	—	3555	N/A	Voltage 29th Harmonic Pole#1	—	—
293	—	3561	N/A	Voltage 30th Harmonic Pole#1	—	—
294	—	3567	N/A	Voltage 31st Harmonic Pole#1	—	—
295	—	3573	N/A	Voltage 32nd Harmonic Pole#1	—	—
296	—	3579	N/A	Voltage 33rd Harmonic Pole#1	—	—
297	—	3585	N/A	Voltage 34th Harmonic Pole#1	—	—
298	—	3591	N/A	Voltage 35th Harmonic Pole#1	—	—
299	—	3597	N/A	Voltage 36th Harmonic Pole#1	—	—
300	—	3603	N/A	Voltage 37th Harmonic Pole#1	—	—
301	—	3609	N/A	Voltage 38th Harmonic Pole#1	—	—
302	—	3615	N/A	Voltage 39th Harmonic Pole#1	—	—
303	—	3621	N/A	Voltage 40th Harmonic Pole#1	—	—
304	—	3627	N/A	Voltage 41st Harmonic Pole#1	—	—
305	—	3633	N/A	Voltage 42nd Harmonic Pole#1	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
306	—	3639	N/A	Voltage 43rd Harmonic Pole#1	—	—
307	—	3645	N/A	Voltage 44th Harmonic Pole#1	—	—
308	—	3651	N/A	Voltage 45th Harmonic Pole#1	—	—
309	—	3657	N/A	Voltage 46th Harmonic Pole#1	—	—
310	—	3663	N/A	Voltage 47th Harmonic Pole#1	—	—
311	—	3669	N/A	Voltage 48th Harmonic Pole#1	—	—
312	—	3675	N/A	Voltage 49th Harmonic Pole#1	—	—
313	—	3681	N/A	Voltage 50th Harmonic Pole#1	—	—
314	—	3687	N/A	Voltage 51st Harmonic Pole#1	—	—
315	—	3693	N/A	Voltage 52nd Harmonic Pole#1	—	—
316	—	3699	N/A	Voltage 53rd Harmonic Pole#1	—	—
317	—	3705	N/A	Voltage 54th Harmonic Pole#1	—	—
318	—	3711	N/A	Voltage 55th Harmonic Pole#1	—	—
319	—	3717	N/A	Voltage 56th Harmonic Pole#1	—	—
320	—	3723	N/A	Voltage 57th Harmonic Pole#1	—	—
321	—	3729	N/A	Voltage 58th Harmonic Pole#1	—	—
322	—	3735	N/A	Voltage 59th Harmonic Pole#1	—	—
323	—	3741	N/A	Voltage 60th Harmonic Pole#1	—	—
324	—	3747	N/A	Voltage 61st Harmonic Pole#1	—	—
325	—	3753	N/A	Voltage 62nd Harmonic Pole#1	—	—
326	—	3759	N/A	Voltage 63rd Harmonic Pole#1	—	—
327	—	3773	N/A	Pole#2 Voltage Total Harmonic Distortion	—	—
328	—	3388	N/A	Voltage 1st Harmonic Pole#2	—	—
329	—	3394	N/A	Voltage 2nd Harmonic Pole#2	—	—
330	—	3400	N/A	Voltage 3rd Harmonic Pole#2	—	—
331	—	3406	N/A	Voltage 4th Harmonic Pole#2	—	—
332	—	3412	N/A	Voltage 5th Harmonic Pole#2	—	—
333	—	3418	N/A	Voltage 6th Harmonic Pole#2	—	—
334	—	3424	N/A	Voltage 7th Harmonic Pole#2	—	—
335	—	3430	N/A	Voltage 8th Harmonic Pole#2	—	—
336	—	3436	N/A	Voltage 9th Harmonic Pole#2	—	—
337	—	3442	N/A	Voltage 10th Harmonic Pole#2	—	—
338	—	3448	N/A	Voltage 11th Harmonic Pole#2	—	—
339	—	3454	N/A	Voltage 12th Harmonic Pole#2	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
340	—	3460	N/A	Voltage 13th Harmonic Pole#2	—	—
341	—	3466	N/A	Voltage 14th Harmonic Pole#2	—	—
342	—	3472	N/A	Voltage 15th Harmonic Pole#2	—	—
343	—	3478	N/A	Voltage 16th Harmonic Pole#2	—	—
344	—	3484	N/A	Voltage 17th Harmonic Pole#2	—	—
345	—	3490	N/A	Voltage 18th Harmonic Pole#2	—	—
346	—	3496	N/A	Voltage 19th Harmonic Pole#2	—	—
347	—	3502	N/A	Voltage 20th Harmonic Pole#2	—	—
348	—	3508	N/A	Voltage 21st Harmonic Pole#2	—	—
349	—	3514	N/A	Voltage 22nd Harmonic Pole#2	—	—
350	—	3520	N/A	Voltage 23rd Harmonic Pole#2	—	—
351	—	3526	N/A	Voltage 24th Harmonic Pole#2	—	—
352	—	3532	N/A	Voltage 25th Harmonic Pole#2	—	—
353	—	3538	N/A	Voltage 26th Harmonic Pole#2	—	—
354	—	3544	N/A	Voltage 27th Harmonic Pole#2	—	—
355	—	3550	N/A	Voltage 28th Harmonic Pole#2	—	—
356	—	3556	N/A	Voltage 29th Harmonic Pole#2	—	—
357	—	3562	N/A	Voltage 30th Harmonic Pole#2	—	—
358	—	3568	N/A	Voltage 31st Harmonic Pole#2	—	—
359	—	3574	N/A	Voltage 32nd Harmonic Pole#2	—	—
360	—	3580	N/A	Voltage 33rd Harmonic Pole#2	—	—
361	—	3586	N/A	Voltage 34th Harmonic Pole#2	—	—
362	—	3592	N/A	Voltage 35th Harmonic Pole#2	—	—
363	—	3598	N/A	Voltage 36th Harmonic Pole#2	—	—
364	—	3604	N/A	Voltage 37th Harmonic Pole#2	—	—
365	—	3610	N/A	Voltage 38th Harmonic Pole#2	—	—
366	—	3616	N/A	Voltage 39th Harmonic Pole#2	—	—
367	—	3622	N/A	Voltage 40th Harmonic Pole#2	—	—
368	—	3628	N/A	Voltage 41st Harmonic Pole#2	—	—
369	—	3634	N/A	Voltage 42nd Harmonic Pole#2	—	—
370	—	3640	N/A	Voltage 43rd Harmonic Pole#2	—	—
371	—	3646	N/A	Voltage 44th Harmonic Pole#2	—	—
372	—	3652	N/A	Voltage 45th Harmonic Pole#2	—	—
373	—	3658	N/A	Voltage 46th Harmonic Pole#2	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
374	—	3664	N/A	Voltage 47th Harmonic Pole#2	—	—
375	—	3670	N/A	Voltage 48th Harmonic Pole#2	—	—
376	—	3676	N/A	Voltage 49th Harmonic Pole#2	—	—
377	—	3682	N/A	Voltage 50th Harmonic Pole#2	—	—
378	—	3688	N/A	Voltage 51st Harmonic Pole#2	—	—
379	—	3694	N/A	Voltage 52nd Harmonic Pole#2	—	—
380	—	3700	N/A	Voltage 53rd Harmonic Pole#2	—	—
381	—	3706	N/A	Voltage 54th Harmonic Pole#2	—	—
382	—	3712	N/A	Voltage 55th Harmonic Pole#2	—	—
383	—	3718	N/A	Voltage 56th Harmonic Pole#2	—	—
384	—	3724	N/A	Voltage 57th Harmonic Pole#2	—	—
385	—	3730	N/A	Voltage 58th Harmonic Pole#2	—	—
386	—	3736	N/A	Voltage 59th Harmonic Pole#2	—	—
387	—	3742	N/A	Voltage 60th Harmonic Pole#2	—	—
388	—	3748	N/A	Voltage 61st Harmonic Pole#2	—	—
389	—	3754	N/A	Voltage 62nd Harmonic Pole#2	—	—
390	—	3760	N/A	Voltage 63rd Harmonic Pole#2	—	—
391	—	3765	N/A	Read ONLY value for the Active Profile	—	—
392	—	3790	N/A	Angle diff between reference Ph voltage and system voltage Pole#1	—	—
393	—	3791	N/A	Frequency diff between reference Ph voltage and system voltage Pole#1	—	—
394	—	3792	N/A	Voltage diff between reference Ph voltage and system voltage Pole#1	—	—
395	—	4839	N/A	Angle diff between reference Ph voltage and system voltage Pole#2	—	—
396	—	4840	N/A	Frequency diff between reference Ph voltage and system voltage Pole#2	—	—
397	—	4841	N/A	Voltage diff between reference Ph voltage and system voltage Pole#2	—	—
398	—	1932	N/A	Total Demand Distortion of Pole#1 current	—	—
399	—	1933	N/A	Total Demand Distortion of Pole#2 current	—	—
400	—	3961	N/A	UTC Time	—	—
401	—	3963	N/A	Local Time	—	—
402	—	1976	N/A	Pressure Metering	—	—
403	—	1991	N/A	IEC61850 Server Status	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
404	—	2009	N/A	Sectionalizer Reset Timer Pole#1	—	—
405	—	2010	N/A	Sectionalizer Reset Timer Pole#2	—	—
406	—	2043	N/A	Sectionalizer Current Restraint Active Timer Pole#1	—	—
407	—	2044	N/A	Sectionalizer Current Restraint Active Timer Pole#2	—	—
408	—	1977	N/A	Breaker Accumulator Pole#1	—	—
409	—	1979	N/A	Breaker Accumulator Pole#2	—	—
410	—	3797	N/A	Battery Amps	—	—
411	—	2055	N/A	Fault Element	—	—
412	—	2090	N/A	79 Fault Duration Pole#1	—	—
413	—	2092	N/A	79 Fault Duration Pole#2	—	—
414	—	1988	N/A	Fault Location	—	—
415	—	1990	N/A	Fault Type	—	—
416	—	2059	N/A	Fault Pri Amps Pole#1	—	—
417	—	2061	N/A	Fault Pri Amps Pole#2	—	—
418	—	2069	N/A	Fault Pri Amps Pole#1 Phase Angle	—	—
419	—	2070	N/A	Fault Pri Amps Pole#2 Phase Angle	—	—
420	—	2067	N/A	Fault Pri Amps Max	—	—
421	—	2072	N/A	Fault Voltage Pole#1	—	—
422	—	2073	N/A	Fault Voltage Pole#2	—	—
423	—	300	N/A	Firmware Version	—	—
424	—	84	N/A	Recloser Type	—	—
425	—	3794	N/A	Control Alarm Code	—	—
426	—	108	N/A	Manufacturer	—	—
427	—	6516	N/A	Battery Charger Num Tests in 24 Hrs	0–7	—
428	—	4899	N/A	32H Capacitor Voltage	—	—
429	—	4900	N/A	32H Charger Temperature	—	—
430	—	4698	N/A	Anlg Out Port 1 Error Status	1–24	—
431	—	2190	N/A	Anlg Out Port 1 FW Version	0–65535	—
432	—	2188	N/A	Port 1 Module ID installed	0–65535	—
433	—	4699	N/A	Anlg Out Port 4 Error Status	0–65535	—
434	—	2191	N/A	Anlg Out Port 4 FW Version	0–65535	—
435	—	2189	N/A	Port 4 Module ID installed	0–65535	—
436	—	7305	N/A	F50#1 Pole#1 Active Profile	—	—
437	—	7310	N/A	F50#1 Pole#2 Active Profile	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
438	—	7569	N/A	F51#1 Pole#1 Active Profile	—	—
439	—	7582	N/A	F51#1 Pole#2 Active Profile	—	—
440	—	4788	N/A	Fault Sec Amps Pole#1	—	—
441	—	4790	N/A	Fault Sec Amps Pole#2	—	—
442	—	4772	N/A	Fault Type 1stT	—	—
443	—	4773	N/A	Fault Pri Amps Pole#1 1stT	—	—
444	—	4775	N/A	Fault Pri Amps Pole#2 1stT	—	—
445	—	4783	N/A	Fault Sec Amps Pole#1 1stT	—	—
446	—	4784	N/A	Fault Sec Amps Pole#2 1stT	—	—
447	—	8775	N/A	FHLT50P MTT Active Profile	—	—
448	—	8781	N/A	FHLT51P MTT Active Profile	—	—
449	—	7087	N/A	F27 Pickup Pole#1 #1	—	—
450	—	7088	N/A	F27 Definite Time Pole#1 #1	—	—
451	—	7102	N/A	F27 Pickup Pole#1 #2	—	—
452	—	7103	N/A	F27 Definite Time Pole#1 #2	—	—
453	—	7117	N/A	F27 Pickup Pole#1 #3	—	—
454	—	7118	N/A	F27 Definite Time Pole#1 #3	—	—
455	—	7132	N/A	F27 Pickup Pole#1 #4	—	—
456	—	7133	N/A	F27 Definite Time Pole#1 #4	—	—
457	—	7092	N/A	F27 Pickup Pole#2 #1	—	—
458	—	7093	N/A	F27 Definite Time Pole#2 #1	—	—
459	—	7107	N/A	F27 Pickup Pole#2 #2	—	—
460	—	7108	N/A	F27 Definite Time Pole#2 #2	—	—
461	—	7117	N/A	F27 Pickup Pole#2 #3	—	—
462	—	7118	N/A	F27 Definite Time Pole#2 #3	—	—
463	—	7137	N/A	F27 Pickup Pole#2 #4	—	—
464	—	7138	N/A	F27 Definite Time Pole#2 #4	—	—
465	—	46475	N/A	F27 BSVS Pickup Pole#1	—	—
466	—	46476	N/A	F27 BSVS Definite Time Pole#1	—	—
467	—	46532	N/A	F27 BSVS Pickup Pole#2	—	—
468	—	46533	N/A	F27 BSVS Definite Time Pole#2	—	—
469	—	8027	N/A	F59 Pickup Pole#1 #1	—	—
470	—	8028	N/A	F59 Definite Time Pole#1 #1	—	—
471	—	8042	N/A	F59 Pickup Pole#1 #2	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
472	—	8043	N/A	F59 Definite Time Pole#1 #2	—	—
473	—	8057	N/A	F59 Pickup Pole#1 #3	—	—
474	—	8058	N/A	F59 Definite Time Pole#1 #3	—	—
475	—	8072	N/A	F59 Pickup Pole#1 #4	—	—
476	—	8073	N/A	F59 Definite Time Pole#1 #4	—	—
477	—	8032	N/A	F59 Pickup Pole#2 #1	—	—
478	—	8033	N/A	F59 Definite Time Pole#2 #1	—	—
479	—	8047	N/A	F59 Pickup Pole#2 #2	—	—
480	—	8048	N/A	F59 Definite Time Pole#2 #2	—	—
481	—	8062	N/A	F59 Pickup Pole#2 #3	—	—
482	—	8063	N/A	F59 Definite Time Pole#2 #3	—	—
483	—	8077	N/A	F59 Pickup Pole#2 #4	—	—
484	—	8078	N/A	F59 Definite Time Pole#2 #4	—	—
485	—	9151	N/A	F59I Pickup Pole#1	—	—
486	—	9152	N/A	F59I Definite time Pole#1	—	—
487	—	47276	N/A	F59I Blocking Inputs Pole#1	—	—
488	—	47277	N/A	F59I Outputs Pole#1	—	—
489	—	9156	N/A	F59I Pickup Pole#2	—	—
490	—	9157	N/A	F59I Definite time Pole#2	—	—
491	—	47279	N/A	F59I Blocking Inputs Pole#2	—	—
492	—	9154	N/A	F59I Outputs Pole#2	—	—
493	—	7305	N/A	F50 Pickup Pole#1 #1	—	—
494	—	7306	N/A	F50 Definite Time Pole#1 #1	—	—
495	—	46914	N/A	F50 Blocking Inputs, Pole#1 #1	—	—
496	—	7303	N/A	F50 Outputs, Pole#1 #1	—	—
497	—	7320	N/A	F50 Pickup Pole#1 #2	—	—
498	—	7321	N/A	F50 Definite Time Pole#1 #2	—	—
499	—	46920	N/A	F50 Blocking Inputs, Pole#1 #2	—	—
500	—	7318	N/A	F50 Outputs, Pole#1 #2	—	—
501	—	7335	N/A	F50 Pickup Pole#1 #3	—	—
502	—	7336	N/A	F50 Definite Time Pole#1 #3	—	—
503	—	46926	N/A	F50 Blocking Inputs, Pole#1 #3	—	—
504	—	7333	N/A	F50 Outputs, Pole#1 #3	—	—
505	—	7350	N/A	F50 Pickup Pole#1 #4	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
506	—	7351	N/A	F50 Definite Time Pole#1 #4	—	—
507	—	46932	N/A	F50 Blocking Inputs, Pole#1 #4	—	—
508	—	7348	N/A	F50 Outputs, Pole#1 #4	—	—
509	—	7310	N/A	F50 Pickup Pole#2 #1	—	—
510	—	7311	N/A	F50 Definite Time Pole#2 #1	—	—
511	—	46916	N/A	F50 Blocking Inputs, Pole#2 #1	—	—
512	—	7308	N/A	F50 Outputs, Pole#2 #1	—	—
513	—	7325	N/A	F50 Pickup Pole#2 #2	—	—
514	—	7326	N/A	F50 Definite Time Pole#2 #2	—	—
515	—	46922	N/A	F50 Blocking Inputs, Pole#2 #2	—	—
516	—	7323	N/A	F50 Outputs, Pole#2 #2	—	—
517	—	7340	N/A	F50 Pickup Pole#2 #3	—	—
518	—	7341	N/A	F50 Definite Time Pole#2 #3	—	—
519	—	46928	N/A	F50 Blocking Inputs, Pole#2 #3	—	—
520	—	7338	N/A	F50 Outputs, Pole#2 #3	—	—
521	—	7355	N/A	F50 Pickup Pole#2 #4	—	—
522	—	7356	N/A	F50 Definite Time Pole#2 #4	—	—
523	—	46934	N/A	F50 Blocking Inputs, Pole#2 #4	—	—
524	—	7353	N/A	F50 Outputs, Pole#2 #4	—	—
525	—	46984	N/A	F51 Blocking Inputs, Pole#1 #1	—	—
526	—	7567	N/A	F51 Outputs, Pole#1 #1	—	—
527	—	7571	N/A	Enable Electromechanical Reset for F51, Pole#1 #1	—	—
528	—	7569	N/A	F51 Pickup Pole#1 #1	—	—
529	—	7570	N/A	F51 Curve Selection Pole#1 #1	—	—
530	—	7572	N/A	F51 Reset Co-eff Pole#1 #1	—	—
531	—	7574	N/A	F51 Time Multiplier Pole#1 #1	—	—
532	—	7575	N/A	F51 Time Adder Pole#1 #1	—	—
533	—	7576	N/A	F51 Min response time adder Pole#1 #1	—	—
534	—	30727	N/A	F51 Voltage control/restraint enable Pole#1 #1	—	—
535	—	30728	N/A	F51 Voltage control/restraint pickup Pole#1 #1	—	—
536	—	46990	N/A	F51 Blocking Inputs, Pole#1 #2	—	—
537	—	7606	N/A	F51 Outputs, Pole#1 #2	—	—
538	—	7610	N/A	Enable Electromechanical Reset for F51, Pole#1 #2	—	—
539	—	7608	N/A	F51 Pickup Pole#1 #2	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
540	—	7609	N/A	F51 Curve Selection Pole#1 #2	—	—
541	—	7611	N/A	F51 Reset Co-eff Pole#1 #2	—	—
542	—	7613	N/A	F51 Time Multiplier Pole#1 #2	—	—
543	—	7614	N/A	F51 Time Adder Pole#1 #2	—	—
544	—	7615	N/A	F51 Min response time adder Pole#1 #2	—	—
545	—	30733	N/A	F51 Voltage control/restraint enable Pole#1 #2	—	—
546	—	30734	N/A	F51 Voltage control/restraint pickup Pole#1 #2	—	—
547	—	46996	N/A	F51 Blocking Inputs, Pole#1 #3	—	—
548	—	7645	N/A	F51 Outputs, Pole#1 #3	—	—
549	—	7649	N/A	Enable Electromechanical Reset for F51, Pole#1 #3	—	—
550	—	7647	N/A	F51 Pickup Pole#1 #3	—	—
551	—	7648	N/A	F51 Curve Selection Pole#1 #3	—	—
552	—	7650	N/A	F51 Reset Co-eff Pole#1 #3	—	—
553	—	7652	N/A	F51 Time Multiplier Pole#1 #3	—	—
554	—	7653	N/A	F51 Time Adder Pole#1 #3	—	—
555	—	7654	N/A	F51 Min response time adder Pole#1 #3	—	—
556	—	30739	N/A	F51 Voltage control/restraint enable Pole#1 #3	—	—
557	—	30740	N/A	F51 Voltage control/restraint pickup Pole#1 #3	—	—
558	—	47002	N/A	F51 Blocking Inputs, Pole#1 #4	—	—
559	—	7684	N/A	F51 Outputs, Pole#1 #4	—	—
560	—	7688	N/A	Enable Electromechanical Reset for F51, Pole#1 #4	—	—
561	—	7686	N/A	F51 Pickup Pole#1 #4	—	—
562	—	7687	N/A	F51 Curve Selection Pole#1 #4	—	—
563	—	7689	N/A	F51 Reset Co-eff Pole#1 #4	—	—
564	—	7691	N/A	F51 Time Multiplier Pole#1 #4	—	—
565	—	7692	N/A	F51 Time Adder Pole#1 #4	—	—
566	—	7693	N/A	F51 Min response time adder Pole#1 #4	—	—
567	—	30745	N/A	F51 Voltage control/restraint enable Pole#1 #4	—	—
568	—	30746	N/A	F51 Voltage control/restraint pickup Pole#1 #4	—	—
569	—	46986	N/A	F51 Blocking Inputs, Pole#2 #1	—	—
570	—	7580	N/A	F51 Outputs, Pole#2 #1	—	—
571	—	7584	N/A	Enable Electromechanical Reset for F51, Pole#2 #1	—	—
572	—	7582	N/A	F51 Pickup Pole#2 #1	—	—
573	—	7583	N/A	F51 Curve Selection Pole#2 #1	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
574	—	7585	N/A	F51 Reset Co-eff Pole#2 #1	—	—
575	—	7587	N/A	F51 Time Multiplier Pole#2 #1	—	—
576	—	7588	N/A	F51 Time Adder Pole#2 #1	—	—
577	—	7589	N/A	F51 Min response time adder Pole#2 #1	—	—
578	—	30729	N/A	F51 Voltage control/restraint enable Pole#2 #1	—	—
579	—	30730	N/A	F51 Voltage control/restraint pickup Pole#2 #1	—	—
580	—	46992	N/A	F51 Blocking Inputs, Pole#2 #2	—	—
581	—	7619	N/A	F51 Outputs, Pole#2 #2	—	—
582	—	7623	N/A	Enable Electromechanical Reset for F51, Pole#2 #2	—	—
583	—	7621	N/A	F51 Pickup Pole#2 #2	—	—
584	—	7622	N/A	F51 Curve Selection Pole#2 #2	—	—
585	—	7624	N/A	F51 Reset Co-eff Pole#2 #2	—	—
586	—	7626	N/A	F51 Time Multiplier Pole#2 #2	—	—
587	—	7627	N/A	F51 Time Adder Pole#2 #2	—	—
588	—	7628	N/A	F51 Min response time adder Pole#2 #2	—	—
589	—	30735	N/A	F51 Voltage control/restraint enable Pole#2 #2	—	—
590	—	30736	N/A	F51 Voltage control/restraint pickup Pole#2 #2	—	—
591	—	46998	N/A	F51 Blocking Inputs, Pole#2 #3	—	—
592	—	7658	N/A	F51 Outputs, Pole#2 #3	—	—
593	—	7662	N/A	Enable Electromechanical Reset for F51, Pole#2 #3	—	—
594	—	7660	N/A	F51 Pickup Pole#2 #3	—	—
595	—	7661	N/A	F51 Curve Selection Pole#2 #3	—	—
596	—	7663	N/A	F51 Reset Co-eff Pole#2 #3	—	—
597	—	7665	N/A	F51 Time Multiplier Pole#2 #3	—	—
598	—	7666	N/A	F51 Time Adder Pole#2 #3	—	—
599	—	7667	N/A	F51 Min response time adder Pole#2 #3	—	—
600	—	30741	N/A	F51 Voltage control/restraint enable Pole#2 #3	—	—
601	—	30742	N/A	F51 Voltage control/restraint pickup Pole#2 #3	—	—
602	—	47004	N/A	F51 Blocking Inputs, Pole#2 #4	—	—
603	—	7697	N/A	F51 Outputs, Pole#2 #4	—	—
604	—	7701	N/A	Enable Electromechanical Reset for F51, Pole#2 #4	—	—
605	—	7699	N/A	F51 Pickup Pole#2 #4	—	—
606	—	7700	N/A	F51 Curve Selection Pole#2 #4	—	—
607	—	7702	N/A	F51 Reset Co-eff Pole#2 #4	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
608	—	7704	N/A	F51 Time Multiplier Pole#2 #4	—	—
609	—	7705	N/A	F51 Time Adder Pole#2 #4	—	—
610	—	7706	N/A	F51 Min response time adder Pole#2 #4	—	—
611	—	30747	N/A	F51 Voltage control/restraint enable Pole#2 #4	—	—
612	—	30748	N/A	F51 Voltage control/restraint pickup Pole#2 #4	—	—
613	—	7025	N/A	F25 Line Min Voltage,UndVol Perm Pole#1	—	—
614	—	7026	N/A	F25 Bus Min Voltage,UndVol Perm Pole#1	—	—
615	—	7027	N/A	F25 Min Time,Sync Check Perm Pole#1	—	—
616	—	7028	N/A	F25 Min Voltage,Sync Check Perm Pole#1	—	—
617	—	7029	N/A	F25 Angle Diff,Sync Check Perm Pole#1	—	—
618	—	7030	N/A	F25 Magn Diff,Sync Check Perm Pole#1	—	—
619	—	7031	N/A	F25 Freq Diff,Sync Check Perm Pole#1	—	—
620	—	7032	N/A	F25 Max Voltage,Sync Check Perm Pole#1	—	—
621	—	46820	N/A	F25 Blocking Input Pole#1	—	—
622	—	7021	N/A	F25 Output Pole#1	—	—
623	—	7024	N/A	Enable F25 DeadLogic, Ang diff, Freq diff, Mag diff Pole#1	—	—
624	—	7039	N/A	F25 Line Min Voltage,UndVol Perm Pole#2	—	—
625	—	7040	N/A	F25 Bus Min Voltage,UndVol Perm Pole#2	—	—
626	—	7041	N/A	F25 Min Time,Sync Check Perm Pole#2	—	—
627	—	7042	N/A	F25 Min Voltage,Sync Check Perm Pole#2	—	—
628	—	7043	N/A	F25 Angle Diff,Sync Check Perm Pole#2	—	—
629	—	7044	N/A	F25 Magn Diff,Sync Check Perm Pole#2	—	—
630	—	7045	N/A	F25 Freq Diff,Sync Check Perm Pole#2	—	—
631	—	7046	N/A	F25 Max Voltage,Sync Check Perm Pole#2	—	—
632	—	46822	N/A	F25 Blocking Input Pole#2	—	—
633	—	7035	N/A	F25 Output Pole#2	—	—
634	—	7038	N/A	Enable F25 DeadLogic, Ang diff, Freq diff, Mag diff Pole#2	—	—
635	—	46872	N/A	F32 Blocking Inputs, Pole#1 #1	—	—
636	—	7183	N/A	F32 Outputs, Pole#1 #1	—	—
637	—	7185	N/A	F32 Pickup Pole#1 #1	—	—
638	—	7187	N/A	F32 Definite Time Pole#1 #1	—	—
639	—	7188	N/A	F32 Operating Power Pole#1 #1	—	—
640	—	46878	N/A	F32 Blocking Inputs, Pole#1 #2	—	—
641	—	7204	N/A	F32 Outputs, Pole#1 #2	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
642	—	7206	N/A	F32 Pickup Pole#1 #2	—	—
643	—	7208	N/A	F32 Definite Time Pole#1 #2	—	—
644	—	7209	N/A	F32 Operating Power Pole#1 #2	—	—
645	—	46884	N/A	F32 Blocking Inputs, Pole#1 #3	—	—
646	—	7225	N/A	F32 Outputs, Pole#1 #3	—	—
647	—	7227	N/A	F32 Pickup Pole#1 #3	—	—
648	—	7229	N/A	F32 Definite Time Pole#1 #3	—	—
649	—	7230	N/A	F32 Operating Power Pole#1 #3	—	—
650	—	46890	N/A	F32 Blocking Inputs, Pole#1 #4	—	—
651	—	7246	N/A	F32 Outputs, Pole#1 #4	—	—
652	—	7248	N/A	F32 Pickup Pole#1 #4	—	—
653	—	7250	N/A	F32 Definite Time Pole#1 #4	—	—
654	—	7251	N/A	F32 Operating Power Pole#1 #4	—	—
655	—	46874	N/A	F32 Blocking Inputs, Pole#2 #1	—	—
656	—	7190	N/A	F32 Outputs, Pole#2 #1	—	—
657	—	7192	N/A	F32 Pickup Pole#2 #1	—	—
658	—	7194	N/A	F32 Definite Time Pole#2 #1	—	—
659	—	7195	N/A	F32 Operating Power Pole#2 #1	—	—
660	—	46880	N/A	F32 Blocking Inputs, Pole#2 #2	—	—
661	—	7211	N/A	F32 Outputs, Pole#2 #2	—	—
662	—	7213	N/A	F32 Pickup Pole#2 #2	—	—
663	—	7215	N/A	F32 Definite Time Pole#2 #2	—	—
664	—	7216	N/A	F32 Operating Power Pole#2 #2	—	—
665	—	46886	N/A	F32 Blocking Inputs, Pole#2 #3	—	—
666	—	7232	N/A	F32 Outputs, Pole#2 #3	—	—
667	—	7234	N/A	F32 Pickup Pole#2 #3	—	—
668	—	7236	N/A	F32 Definite Time Pole#2 #3	—	—
669	—	7237	N/A	F32 Operating Power Pole#2 #3	—	—
670	—	46892	N/A	F32 Blocking Inputs, Pole#2 #4	—	—
671	—	7253	N/A	F32 Outputs, Pole#2 #4	—	—
672	—	7255	N/A	F32 Pickup Pole#2 #4	—	—
673	—	7257	N/A	F32 Definite Time Pole#2 #4	—	—
674	—	7258	N/A	F32 Operating Power Pole#2 #4	—	—
675	—	47300	N/A	F50BF Unbalanced Pole#1 Blocking Inputs	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
676	—	46621	N/A	F50BF Unbalanced Pole#1 Outputs	—	—
677	—	46623	N/A	F50BF Unbalanced Pole#1 Current Pickup	—	—
678	—	46624	N/A	F50BF Unbalanced Pole#1 Definite time	—	—
679	—	46625	N/A	F50BF Unbalanced Pole#1 Current Enable	—	—
680	—	46626	N/A	F50BF Unbalanced Pole#1 Retrip time	—	—
681	—	46812	N/A	F50BF Unbalanced Pole#1 Input Initiate	—	—
682	—	46628	N/A	F50BF Unbalanced Pole#1 Retrip Output	—	—
683	—	47302	N/A	F50BF Unbalanced Pole#2 Blocking Inputs	—	—
684	—	46630	N/A	F50BF Unbalanced Pole#2 Outputs	—	—
685	—	46632	N/A	F50BF Unbalanced Pole#2 Current Pickup	—	—
686	—	46633	N/A	F50BF Unbalanced Pole#2 Definite time	—	—
687	—	46634	N/A	F50BF Unbalanced Pole#2 Current Enable	—	—
688	—	46635	N/A	F50BF Unbalanced Pole#2 Retrip time	—	—
689	—	46814	N/A	F50BF Unbalanced Pole#2 Input Initiate	—	—
690	—	46637	N/A	F50BF Unbalanced Pole#2 Retrip Output	—	—
691	—	47172	N/A	F81#1 Blocking Inputs	—	—
692	—	8563	N/A	F81#1 Outputs	—	—
693	—	8565	N/A	F81#1 Pickup	—	—
694	—	8566	N/A	F81#1 Definite time	—	—
695	—	8567	N/A	F81#1 Hysteresis	—	—
696	—	8568	N/A	F81#1 Minimum Voltage	—	—
697	—	8569	N/A	F81#1 Minimum Load	—	—
698	—	8570	N/A	F81#1 Option	—	—
699	—	47174	N/A	F81#2 Blocking Inputs	—	—
700	—	8572	N/A	F81#2 Outputs	—	—
701	—	8574	N/A	F81#2 Pickup	—	—
702	—	8575	N/A	F81#2 Definite time	—	—
703	—	8576	N/A	F81#2 Hysteresis	—	—
704	—	8577	N/A	F81#2 Minimum Voltage	—	—
705	—	8578	N/A	F81#2 Minimum Load	—	—
706	—	8579	N/A	F81#2 Option	—	—
707	—	47176	N/A	F81#3 Blocking Inputs	—	—
708	—	8581	N/A	F81#3 Outputs	—	—
709	—	8583	N/A	F81#3 Pickup	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
710	—	8584	N/A	F81#3 Definite time	—	—
711	—	8585	N/A	F81#3 Hysteresis	—	—
712	—	8586	N/A	F81#3 Minimum Voltage	—	—
713	—	8587	N/A	F81#3 Minimum Load	—	—
714	—	8588	N/A	F81#3 Option	—	—
715	—	47178	N/A	F81#4 Blocking Inputs	—	—
716	—	8590	N/A	F81#4 Outputs	—	—
717	—	8592	N/A	F81#4 Pickup	—	—
718	—	8593	N/A	F81#4 Definite time	—	—
719	—	8594	N/A	F81#4 Hysteresis	—	—
720	—	8595	N/A	F81#4 Minimum Voltage	—	—
721	—	8596	N/A	F81#4 Minimum Load	—	—
722	—	8597	N/A	F81#4 Option	—	—
723	—	47180	N/A	F81#5 Blocking Inputs	—	—
724	—	46711	N/A	F81#5 Outputs	—	—
725	—	46713	N/A	F81#5 Pickup	—	—
726	—	46714	N/A	F81#5 Definite time	—	—
727	—	46715	N/A	F81#5 Hysteresis	—	—
728	—	46716	N/A	F81#5 Minimum Voltage	—	—
729	—	46717	N/A	F81#5 Minimum Load	—	—
730	—	46718	N/A	F81#5 Option	—	—
731	—	47182	N/A	F81#6 Blocking Inputs	—	—
732	—	46720	N/A	F81#6 Outputs	—	—
733	—	46722	N/A	F81#6 Pickup	—	—
734	—	46723	N/A	F81#6 Definite time	—	—
735	—	46724	N/A	F81#6 Hysteresis	—	—
736	—	46725	N/A	F81#6 Minimum Voltage	—	—
737	—	46726	N/A	F81#6 Minimum Load	—	—
738	—	46727	N/A	F81#6 Option	—	—
739	—	47184	N/A	F81#7 Blocking Inputs	—	—
740	—	46729	N/A	F81#7 Outputs	—	—
741	—	46731	N/A	F81#7 Pickup	—	—
742	—	46732	N/A	F81#7 Def Time	—	—
743	—	46733	N/A	F81#7 Hysteresis	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
744	—	46734	N/A	F81#7 Min Voltage	—	—
745	—	46735	N/A	F81#7 Min Load	—	—
746	—	46736	N/A	F81#7 Block Options	—	—
747	—	467186	N/A	F81#8 Blocking Inputs	—	—
748	—	46738	N/A	F81#8 Outputs	—	—
749	—	46740	N/A	F81#8 Pickup	—	—
750	—	46741	N/A	F81#8 Def Time	—	—
751	—	46742	N/A	F81#8 Hysteresis	—	—
752	—	46743	N/A	F81#8 Min Voltage	—	—
753	—	46744	N/A	F81#8 Min Load	—	—
754	—	46745	N/A	F81#8 Block Options	—	—
755	—	8675	N/A	F81R#1 Pickup	—	—
756	—	8676	N/A	F81R#1 Definite time	—	—
757	—	8677	N/A	F81R#1 Max Frequency	—	—
758	—	8678	N/A	F81R#1 Min Current	—	—
759	—	8679	N/A	F81R#1 Min Voltage	—	—
760	—	8680	N/A	F81R#1 Pickup cycle number	—	—
761	—	8690	N/A	F81R#1 Operating voltage side	—	—
762	—	8684	N/A	F81R#2 Pickup	—	—
763	—	8685	N/A	F81R#2 Definite time	—	—
764	—	8686	N/A	F81R#2 Max Frequency	—	—
765	—	8687	N/A	F81R#2 Min Current	—	—
766	—	8688	N/A	F81R#2 Min Voltage	—	—
767	—	8689	N/A	F81R#2 Pickup cycle number	—	—
768	—	8691	N/A	F81R#2 Operating voltage side	—	—
769	—	46749	N/A	F81R#3 Pickup	—	—
770	—	46750	N/A	F81R#3 Def Time	—	—
771	—	46751	N/A	F81R#3 Max Frequency	—	—
772	—	46752	N/A	F81R#3 Min Current	—	—
773	—	46753	N/A	F81R#3 Min Voltage	—	—
774	—	46754	N/A	F81R#3 Pickup Cycle Number	—	—
775	—	46755	N/A	F81R#3 Operating Voltage	—	—
776	—	46759	N/A	F81R#4 Pickup	—	—
777	—	46760	N/A	F81R#4 Def Time	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
778	—	46761	N/A	F81R#4 Max Frequency	—	—
779	—	46762	N/A	F81R#4 Min Current	—	—
780	—	46763	N/A	F81R#4 Min Voltage	—	—
781	—	46764	N/A	F81R#4 Pickup Cycle Number	—	—
782	—	46765	N/A	F81R#4 Operating Voltage	—	—
783	—	46769	N/A	F81R#5 Pickup	—	—
784	—	46770	N/A	F81R#5 Def Time	—	—
785	—	46771	N/A	F81R#5 Max Frequency	—	—
786	—	46772	N/A	F81R#5 Min Current	—	—
787	—	46773	N/A	F81R#5 Min Voltage	—	—
788	—	46774	N/A	F81R#5 Pickup Cycle Number	—	—
789	—	46775	N/A	F81R#5 Operating Voltage	—	—
790	—	46779	N/A	F81R#6 Pickup	—	—
791	—	46780	N/A	F81R#6 Def Time	—	—
792	—	46781	N/A	F81R#6 Max Frequency	—	—
793	—	46782	N/A	F81R#6 Min Current	—	—
794	—	46783	N/A	F81R#6 Min Voltage	—	—
795	—	46784	N/A	F81R#6 Pickup Cycle Number	—	—
796	—	46785	N/A	F81R#6 Operating Voltage	—	—
797	—	46789	N/A	F81R#7 Pickup	—	—
798	—	46790	N/A	F81R#7 Def Time	—	—
799	—	46791	N/A	F81R#7 Max Frequency	—	—
800	—	46792	N/A	F81R#7 Min Current	—	—
801	—	46793	N/A	F81R#7 Min Voltage	—	—
802	—	46794	N/A	F81R#7 Pickup Cycle Number	—	—
803	—	46795	N/A	F81R#7 Operating Voltage	—	—
804	—	46799	N/A	F81R#8 Pickup	—	—
805	—	46800	N/A	F81R#8 Def Time	—	—
806	—	46801	N/A	F81R#8 Max Frequency	—	—
807	—	46802	N/A	F81R#8 Min Current	—	—
808	—	46803	N/A	F81R#8 Min Voltage	—	—
809	—	46804	N/A	F81R#8 Pickup Cycle Number	—	—
810	—	46805	N/A	F81R#8 Operating Voltage	—	—
811	—	46572	N/A	FCLP Pole#1 Curr below % of nominal	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
812	—	46573	N/A	FCLP Pole#1 Active Duration	—	—
813	—	46574	N/A	FCLP Pole#1 Time Delay to Restore to normal	—	—
814	—	46575	N/A	FCLP Pole#1 Time Locked out to activate	—	—
815	—	46576	N/A	FCLP Pole#1 Reclose Count	—	—
816	—	46577	N/A	FCLP Pole#1 Enable HCL	—	—
817	—	46578	N/A	FCLP Pole#1 HCL Pickup	—	—
818	—	46579	N/A	FCLP Pole#1 HCL Definite Time	—	—
819	—	46580	N/A	FCLP Pole#1 50P Enable	—	—
820	—	46581	N/A	FCLP Pole#1 50P Pickup	—	—
821	—	46582	N/A	FCLP Pole#1 50P Definite Time	—	—
822	—	46583	N/A	FCLP Pole#1 51P Enable	—	—
823	—	46584	N/A	FCLP Pole#1 51P Pickup	—	—
824	—	46585	N/A	FCLP Pole#1 51P Curve	—	—
825	—	46586	N/A	FCLP Pole#1 Enable Electromechanical Reset for 51	—	—
826	—	46587	N/A	FCLP Pole#1 Reset Co-eff for 51	—	—
827	—	46589	N/A	FCLP Pole#1 Time Multiplier for 51	—	—
828	—	46590	N/A	FCLP Pole#1 Time Adder for 51	—	—
829	—	46591	N/A	FCLP Pole#1 Min response time adder	—	—
830	—	46595	N/A	FCLP Pole#2 Curr below % of nominal	—	—
831	—	46596	N/A	FCLP Pole#2 Active Duration	—	—
832	—	46597	N/A	FCLP Pole#2 Time Delay to Restore to normal	—	—
833	—	46598	N/A	FCLP Pole#2 Time Locked out to activate	—	—
834	—	46599	N/A	FCLP Pole#2 Reclose Count	—	—
835	—	46600	N/A	FCLP Pole#2 Enable HCL	—	—
836	—	46601	N/A	FCLP Pole#2 HCL Pickup	—	—
837	—	46602	N/A	FCLP Pole#2 HCL Definite Time	—	—
838	—	46603	N/A	FCLP Pole#2 50P Enable	—	—
839	—	46604	N/A	FCLP Pole#2 50P Pickup	—	—
840	—	46605	N/A	FCLP Pole#2 50P Definite Time	—	—
841	—	46606	N/A	FCLP Pole#2 51P Enable	—	—
842	—	46607	N/A	FCLP Pole#2 51P Pickup	—	—
843	—	46608	N/A	FCLP Pole#2 51P Curve	—	—
844	—	46609	N/A	FCLP Pole#2 Enable Electromechanical Reset for 51	—	—
845	—	46610	N/A	FCLP Pole#2 Reset Co-eff for 51	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
846	—	46612	N/A	FCLP Pole#2 Time Multiplier for 51	—	—
847	—	46613	N/A	FCLP Pole#2 Time Adder for 51	—	—
848	—	46614	N/A	FCLP Pole#2 Min response time adder	—	—
849	—	8775	N/A	F50P Pickup in HLT	—	—
850	—	8776	N/A	F50P Definite time in HLT	—	—
851	—	8781	N/A	F51P Pickup in HLT	—	—
852	—	8782	N/A	F51P Curve selection in HLT	—	—
853	—	8784	N/A	F51P Reset Coeff in HLT	—	—
854	—	8786	N/A	F51P Time Multiplier in HLT	—	—
855	—	8787	N/A	F51P Time Adder in HLT	—	—
856	—	8788	N/A	F51P Min response time adder in HLT	—	—
857	—	8783	N/A	Enable Electromechanical Reset for F51P in HLT	—	—
858	—	47258	N/A	IPSLOGIC-1 Inputs	—	—
859	—	9101	N/A	IPSLOGIC-1 Outputs	—	—
860	—	9103	N/A	IPSLOGIC-1 Delay	—	—
861	—	9132	N/A	IPSLOGIC-1 Profile Switching	—	—
862	—	46065	N/A	IPSLOGIC-1 Custom Message Option	—	—
863	—	47260	N/A	IPSLOGIC-2 Inputs	—	—
864	—	9105	N/A	IPSLOGIC-2 Outputs	—	—
865	—	9107	N/A	IPSLOGIC-2 Delay	—	—
866	—	9133	N/A	IPSLOGIC-2 Profile Switching	—	—
867	—	46066	N/A	IPSLOGIC-2 Custom Message Option	—	—
868	—	47262	N/A	IPSLOGIC-3 Inputs	—	—
869	—	9109	N/A	IPSLOGIC-3 Outputs	—	—
870	—	9111	N/A	IPSLOGIC-3 Delay	—	—
871	—	9134	N/A	IPSLOGIC-3 Profile Switching	—	—
872	—	46067	N/A	IPSLOGIC-3 Custom Message Option	—	—
873	—	47264	N/A	IPSLOGIC-4 Inputs	—	—
874	—	9113	N/A	IPSLOGIC-4 Outputs	—	—
875	—	9115	N/A	IPSLOGIC-4 Delay	—	—
876	—	9135	N/A	IPSLOGIC-4 Profile Switching	—	—
877	—	46068	N/A	IPSLOGIC-4 Custom Message Option	—	—
878	—	47266	N/A	IPSLOGIC-5 Inputs	—	—
879	—	9117	N/A	IPSLOGIC-5 Outputs	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
880	—	9119	N/A	IPSLOGIC-5 Delay	—	—
881	—	9136	N/A	IPSLOGIC-5 Profile Switching	—	—
882	—	46069	N/A	IPSLOGIC-5 Custom Message Option	—	—
883	—	47268	N/A	IPSLOGIC-6 Inputs	—	—
884	—	9121	N/A	IPSLOGIC-6 Outputs	—	—
885	—	9123	N/A	IPSLOGIC-6 Delay	—	—
886	—	9137	N/A	IPSLOGIC-6 Profile Switching	—	—
887	—	46070	N/A	IPSLOGIC-6 Custom Message Option	—	—
888	—	47270	N/A	IPSLOGIC-7 Inputs	—	—
889	—	9125	N/A	IPSLOGIC-7 Outputs	—	—
890	—	9127	N/A	IPSLOGIC-7 Delay	—	—
891	—	9138	N/A	IPSLOGIC-7 Profile Switching	—	—
892	—	46071	N/A	IPSLOGIC-7 Custom Message Option	—	—
893	—	47272	N/A	IPSLOGIC-8 Inputs	—	—
894	—	9129	N/A	IPSLOGIC-8 Outputs	—	—
895	—	9131	N/A	IPSLOGIC-8 Delay	—	—
896	—	9139	N/A	IPSLOGIC-8 Profile Switching	—	—
897	—	46072	N/A	IPSLOGIC-8 Custom Message Option	—	—
898	—	47282	N/A	Breaker Monitor Inputs	—	—
899	—	46044	N/A	Breaker Monitor Outputs	—	—
900	—	46052	N/A	Breaker Monitor Pickup	—	—
901	—	46053	N/A	Breaker Monitor Time Delay	—	—
902	—	46054	N/A	FBM Breaker Open Arc Current Delay	—	—
903	—	46055	N/A	FBM Arc Current Cycle	—	—
904	—	47476	N/A	Breaker Monitor Input Initiate Pole#1	—	—
905	—	47478	N/A	Breaker Monitor Input Initiate Pole#2	—	—
906	—	46049	N/A	Breaker Monitor Output Initiate Pole#1	—	—
907	—	46050	N/A	Breaker Monitor Output Initiate Pole#2	—	—
908	—	46300	N/A	Inrush Harmonic Restraint Pickup	—	—
909	—	46301	N/A	Inrush Harmonic Restraint Dropout	—	—
910	—	46302	N/A	Inrush Harmonic Restraint Active Time	—	—
911	—	46303	N/A	Inrush Harmonic Restraint 50P	—	—
912	—	46305	N/A	Inrush Harmonic Restraint 51P	—	—
913	—	47304	N/A	LCP Pole#1 Blocking Inputs	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
914	—	46639	N/A	LCP Pole#1 Outputs	—	—
915	—	46641	N/A	LCP Pole#1 Pickup	—	—
916	—	46642	N/A	LCP Pole#1 Definite Time	—	—
917	—	47306	N/A	LCP Pole#2 Blocking Inputs	—	—
918	—	46644	N/A	LCP Pole#2 Outputs	—	—
919	—	46646	N/A	LCP Pole#2 Pickup	—	—
920	—	46647	N/A	LCP Pole#2 Definite Time	—	—
921	—	46651	N/A	HCA Pole#1 Pickup	—	—
922	—	46652	N/A	HCA Pole#1 Definite Time	—	—
923	—	46654	N/A	HCA Pole#2 Pickup	—	—
924	—	46655	N/A	HCA Pole#2 Definite Time	—	—
925	—	46430	N/A	Two Poles Ganged Op Mode Single Element	—	—
926	—	46431	N/A	Ganged Op Mode Setting F27	—	—
927	—	46432	N/A	Ganged Op Mode Setting F32	—	—
928	—	46433	N/A	Ganged Op Mode Setting F59	—	—
929	—	46434	N/A	Ganged Op Mode Setting F50	—	—
930	—	46435	N/A	Ganged Op Mode Setting F51	—	—
931	—	46437	N/A	Use Pole#1 Setting Single Element	—	—
932	—	46438	N/A	Use Pole#1 Setting F27	—	—
933	—	46439	N/A	Use Pole#1 Setting F32	—	—
934	—	46440	N/A	Use Pole#1 Setting F59	—	—
935	—	46441	N/A	Use Pole#1 Setting F50	—	—
936	—	46442	N/A	Use Pole#1 Setting F51	—	—
937	—	46452	N/A	79U Pole#1 Sequence coordinator	—	—
938	—	46453	N/A	79U Pole#1 Reset Time after Auto Reclose	—	—
939	—	46454	N/A	79U Pole#1 Reset Time from Lockout	—	—
940	—	46455	N/A	79U Pole#1 Trip Seq #1 Time Delay	—	—
941	—	46456	N/A	79U Pole#1 Trip Seq #1 51P Curve	—	—
942	—	46457	N/A	79U Pole#1 Trip Seq #1 Enable	—	—
943	—	46458	N/A	79U Pole#1 Trip Seq #2 Time Delay	—	—
944	—	46459	N/A	79U Pole#1 Trip Seq #2 51P Curve	—	—
945	—	46460	N/A	79U Pole#1 Trip Seq #2 Enable	—	—
946	—	46461	N/A	79U Pole#1 Trip Seq #3 Time Delay	—	—
947	—	46462	N/A	79U Pole#1 Trip Seq #3 51P Curve	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
948	—	46463	N/A	79U Pole#1 Trip Seq #3 Enable	—	—
949	—	46464	N/A	79U Pole#1 Trip Seq #4 Time Delay	—	—
950	—	46465	N/A	79U Pole#1 Trip Seq #4 51P Curve	—	—
951	—	46466	N/A	79U Pole#1 Trip Seq #4 Enable	—	—
952	—	46468	N/A	79U Pole#1 Trip Seq #5 51P Curve	—	—
953	—	46469	N/A	79U Pole#1 Trip Seq #5 Enable	—	—
954	—	46470	N/A	79U Pole#1 51P Pickup	—	—
955	—	46471	N/A	79U Pole#1 50P HCT Pickup	—	—
956	—	46472	N/A	79U Pole#1 50P HCT Definite Time	—	—
957	—	46473	N/A	79U Pole#1 50P HCL Pickup	—	—
958	—	46474	N/A	79U Pole#1 50P HCL Definite Time	—	—
959	—	46475	N/A	79U Pole#1 27BSVS Pickup	—	—
960	—	46476	N/A	79U Pole#1 27BSVS Definite Time	—	—
961	—	46477	N/A	79U Pole#1 IPSlogic Trip Selection	—	—
962	—	46478	N/A	79U Pole#1 Bus Supv Selection	—	—
963	—	46482	N/A	79U Pole#1 Triplogic Enable	—	—
964	—	46483	N/A	79U Pole#1 Triplogic IPS Timeout Selection	—	—
965	—	46485	N/A	79U Pole#1 Triplogic Expiry Time	—	—
966	—	46486	N/A	79U Pole#1 Skipshot Enable	—	—
967	—	46487	N/A	79U Pole#1 Skipshot IPS Selection	—	—
968	—	46488	N/A	79U Pole#1 Skipshot State	—	—
969	—	46489	N/A	79U Pole#1 Fast Curve Enable	—	—
970	—	46490	N/A	79U Pole#1 Fast Curve Selection	—	—
971	—	46491	N/A	79U Pole#1 Fast Curve Time Multiplier	—	—
972	—	46492	N/A	79U Pole#1 Fast Curve Time Adder	—	—
973	—	46493	N/A	79U Pole#1 Fast Curve Min Response Time Adder	—	—
974	—	46494	N/A	79U Pole#1 Fast Curve Elec Mech Reset	—	—
975	—	46495	N/A	79U Pole#1 Fast Curve Reset co-efficient	—	—
976	—	46497	N/A	79U Pole#1 Slow Curve Enable	—	—
977	—	46498	N/A	79U Pole#1 Slow Curve Selection	—	—
978	—	46499	N/A	79U Pole#1 Slow Curve Time Multiplier	—	—
979	—	46500	N/A	79U Pole#1 Slow Curve Time Adder	—	—
980	—	46501	N/A	79U Pole#1 Slow Curve Min Response Time Adder	—	—
981	—	46502	N/A	79U Pole#1 Slow Curve Elec Mech Reset	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
982	—	46503	N/A	79U Pole#1 Slow Curve Reset co-efficient	—	—
983	—	46505	N/A	79U Pole#1 Enable Target Reset Timer	—	—
984	—	46506	N/A	79U Pole#1 Target Reset Time	—	—
985	—	46509	N/A	79U Pole#2 Sequence coordinator	—	—
986	—	46510	N/A	79U Pole#2 Reset Time after Auto Reclose	—	—
987	—	46511	N/A	79U Pole#2 Reset Time from Lockout	—	—
988	—	46512	N/A	79U Pole#2 Trip Seq #1 Time Delay	—	—
989	—	46513	N/A	79U Pole#2 Trip Seq #1 51P Curve	—	—
990	—	46514	N/A	79U Pole#2 Trip Seq #1 Enable	—	—
991	—	46515	N/A	79U Pole#2 Trip Seq #2 Time Delay	—	—
992	—	46516	N/A	79U Pole#2 Trip Seq #2 51P Curve	—	—
993	—	46517	N/A	79U Pole#2 Trip Seq #2 Enable	—	—
994	—	46518	N/A	79U Pole#2 Trip Seq #3 Time Delay	—	—
995	—	46519	N/A	79U Pole#2 Trip Seq #3 51P Curve	—	—
996	—	46520	N/A	79U Pole#2 Trip Seq #3 Enable	—	—
997	—	46521	N/A	79U Pole#2 Trip Seq #4 Time Delay	—	—
998	—	46522	N/A	79U Pole#2 Trip Seq #4 51P Curve	—	—
999	—	46523	N/A	79U Pole#2 Trip Seq #4 Enable	—	—
1000	—	46525	N/A	79U Pole#2 Trip Seq #5 51P Curve	—	—
1001	—	46526	N/A	79U Pole#2 Trip Seq #5 Enable	—	—
1002	—	46527	N/A	79U Pole#2 51P Pickup	—	—
1003	—	46528	N/A	79U Pole#2 50P HCT Pickup	—	—
1004	—	46529	N/A	79U Pole#2 50P HCT Definite Time	—	—
1005	—	46530	N/A	79U Pole#2 50P HCL Pickup	—	—
1006	—	46531	N/A	79U Pole#2 50P HCL Definite Time	—	—
1007	—	46532	N/A	79U Pole#2 27BSVS Pickup	—	—
1008	—	46533	N/A	79U Pole#2 27BSVS Definite Time	—	—
1009	—	46534	N/A	79U Pole#2 IPSlogic Trip Selection	—	—
1010	—	46536	N/A	79U Pole#2 Bus Supv Selection	—	—
1011	—	46539	N/A	79U Pole#2 Triplogic Enable	—	—
1012	—	46540	N/A	79U Pole#2 Triplogic IPS Timeout Selection	—	—
1013	—	46542	N/A	79U Pole#2 Triplogic Expiry Time	—	—
1014	—	46543	N/A	79U Pole#2 Skipshot Enable	—	—
1015	—	46544	N/A	79U Pole#2 Skipshot IPS Selection	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
1016	—	46545	N/A	79U Pole#2 Skipshot State	—	—
1017	—	46546	N/A	79U Pole#2 Fast Curve Enable	—	—
1018	—	46547	N/A	79U Pole#2 Fast Curve Selection	—	—
1019	—	46548	N/A	79U Pole#2 Fast Curve Time Multiplier	—	—
1020	—	46549	N/A	79U Pole#2 Fast Curve Time Adder	—	—
1021	—	46550	N/A	79U Pole#2 Fast Curve Min Response Time Adder	—	—
1022	—	46551	N/A	79U Pole#2 Fast Curve Elec Mech Reset	—	—
1023	—	46552	N/A	79U Pole#2 Fast Curve Reset co-efficient	—	—
1024	—	46554	N/A	79U Pole#2 Slow Curve Enable	—	—
1025	—	46555	N/A	79U Pole#2 Slow Curve Selection	—	—
1026	—	46556	N/A	79U Pole#2 Slow Curve Time Multiplier	—	—
1027	—	46557	N/A	79U Pole#2 Slow Curve Time Adder	—	—
1028	—	46558	N/A	79U Pole#2 Slow Curve Min Response Time Adder	—	—
1029	—	46559	N/A	79U Pole#2 Slow Curve Elec Mech Reset	—	—
1030	—	46560	N/A	79U Pole#2 Slow Curve Reset co-efficient	—	—
1031	—	46562	N/A	79U Pole#2 Enable Target Reset Timer	—	—
1032	—	46563	N/A	79U Pole#2 Target Reset Time	—	—
1033	—	46292	N/A	Loop Scheme Pole#1 Blocking Inputs	—	—
1034	—	46320	N/A	Loop Scheme Pole#1 Application Type	—	—
1035	—	46321	N/A	Loop Scheme Pole#1 Action on Voltage Option	—	—
1036	—	46322	N/A	Loop Scheme Pole#1 Reset Options	—	—
1037	—	46323	N/A	Loop Scheme Pole#1 After Reset Options	—	—
1038	—	46325	N/A	Loop Scheme Pole#1 IPSLogic selection	—	—
1039	—	46331	N/A	Loop Scheme Pole#1 Profile Switch selection	—	—
1040	—	46328	N/A	Loop Scheme Pole#1 Dead Line Delay Time Y	—	—
1041	—	46333	N/A	Loop Scheme Pole#1 Dead Line Delay Time Z	—	—
1042	—	46329	N/A	Loop Scheme Pole#1 Gnd Trip Block Duration	—	—
1043	—	46330	N/A	Loop Scheme Pole#1 Reclose Block Duration	—	—
1044	—	46310	N/A	Loop Scheme Pole#1 Dead Line Vy Pickup	—	—
1045	—	46311	N/A	Loop Scheme Pole#1 Dead Line Vy Definite Time	—	—
1046	—	46312	N/A	Loop Scheme Pole#1 Dead Line Vz Pickup	—	—
1047	—	46313	N/A	Loop Scheme Pole#1 Dead Line Vz Definite Time	—	—
1048	—	46314	N/A	Loop Scheme Pole#1 Live Line Sensing Vy Pickup	—	—
1049	—	46315	N/A	Loop Scheme Pole#1 Live Line Sensing Vy Definite Time	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
1050	—	46316	N/A	Loop Scheme Pole#1 Live Line Sensing Vz Pickup	—	—
1051	—	46317	N/A	Loop Scheme Pole#1 Live Line Sensing Vz Definite Time	—	—
1052	—	46326	N/A	Loop Scheme Pole#1 Live Line Window Delay Time	—	—
1053	—	46334	N/A	Loop Scheme Pole#1 Restore Vy Delay Time	—	—
1054	—	46343	N/A	Loop Scheme Pole#1 Restore Vz Delay Time	—	—
1055	—	47294	N/A	Loop Scheme Pole#2 Blocking Inputs	—	—
1056	—	46402	N/A	Loop Scheme Pole#2 Application Type	—	—
1057	—	46403	N/A	Loop Scheme Pole#2 Action on Voltage Option	—	—
1058	—	46404	N/A	Loop Scheme Pole#2 Reset Options	—	—
1059	—	46405	N/A	Loop Scheme Pole#2 After Reset Options	—	—
1060	—	46407	N/A	Loop Scheme Pole#2 IPSLogic selection	—	—
1061	—	46414	N/A	Loop Scheme Pole#2 Profile Switch selection	—	—
1062	—	46410	N/A	Loop Scheme Pole#2 Dead Line Delay Time Y	—	—
1063	—	46411	N/A	Loop Scheme Pole#2 Dead Line Delay Time Z	—	—
1064	—	46412	N/A	Loop Scheme Pole#2 Gnd Trip Block Duration	—	—
1065	—	46413	N/A	Loop Scheme Pole#2 Reclose Block Duration	—	—
1066	—	46416	N/A	Loop Scheme Pole#2 Dead Line Vy Pickup	—	—
1067	—	46417	N/A	Loop Scheme Pole#2 Dead Line Vy Definite Time	—	—
1068	—	46418	N/A	Loop Scheme Pole#2 Dead Line Vz Pickup	—	—
1069	—	46419	N/A	Loop Scheme Pole#2 Dead Line Vz Definite Time	—	—
1070	—	46420	N/A	Loop Scheme Pole#2 Live Line Sensing Vy Pickup	—	—
1071	—	46421	N/A	Loop Scheme Pole#2 Live Line Sensing Vy Definite Time	—	—
1072	—	46422	N/A	Loop Scheme Pole#2 Live Line Sensing Vz Pickup	—	—
1073	—	46423	N/A	Loop Scheme Pole#2 Live Line Sensing Vz Definite Time	—	—
1074	—	46408	N/A	Loop Scheme Pole#2 Live Line Window Delay Time	—	—
1075	—	46424	N/A	Loop Scheme Pole#2 Restore Vy Delay Time	—	—
1076	—	46425	N/A	Loop Scheme Pole#2 Restore Vz Delay Time	—	—
1077	—	47308	N/A	Sect Unbalanced Pole#1 Inputs	—	—
1078	—	46658	N/A	Sect Unbalanced Pole#1 Trigger Options	—	—
1079	—	46659	N/A	Sect Unbalanced Pole#1 Max Trips	—	—
1080	—	46660	N/A	Sect Unbalanced Pole#1 Reset Timer	—	—
1081	—	46661	N/A	Sect Unbalanced Pole#1 Voltage Loss Delay	—	—
1082	—	46662	N/A	Sect Unbalanced Pole#1 Phase Duration	—	—
1083	—	46663	N/A	Sect Unbalanced Pole#1 Phase Multiplier	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
1084	—	46664	N/A	Sect Unbalanced Pole#1 27 Pickup	—	—
1085	—	46665	N/A	Sect Unbalanced Pole#1 27 Definite Time	—	—
1086	—	46666	N/A	Sect Unbalanced Pole#1 50 Pickup	—	—
1087	—	46667	N/A	Sect Unbalanced Pole#1 50 Definite Time	—	—
1088	—	46668	N/A	Sect Unbalanced Pole#1 59 Pickup	—	—
1089	—	46669	N/A	Sect Unbalanced Pole#1 59 Definite Time	—	—
1090	—	46670	N/A	Sect Unbalanced Pole#1 51 Pickup	—	—
1091	—	46671	N/A	Sect Unbalanced Pole#1 51 Curve	—	—
1092	—	46672	N/A	Sect Unbalanced Pole#1 51 Time Multiplier	—	—
1093	—	46673	N/A	Sect Unbalanced Pole#1 51 Elec Mech Reset	—	—
1094	—	46675	N/A	Sect Unbalanced Pole#1 51 Time Adder	—	—
1095	—	46676	N/A	Sect Unbalanced Pole#1 51 Min response Time Adder	—	—
1096	—	46678	N/A	Sect Unbalanced Pole#1 51 Reset co-efficient	—	—
1097	—	47310	N/A	Sect Unbalanced Pole#2 Inputs	—	—
1098	—	46684	N/A	Sect Unbalanced Pole#2 Trigger Options	—	—
1099	—	46685	N/A	Sect Unbalanced Pole#2 Max Trips	—	—
1100	—	46686	N/A	Sect Unbalanced Pole#2 Reset Timer	—	—
1101	—	46687	N/A	Sect Unbalanced Pole#2 Voltage Loss Delay	—	—
1102	—	46688	N/A	Sect Unbalanced Pole#2 Phase Duration	—	—
1103	—	46689	N/A	Sect Unbalanced Pole#2 Phase Multiplier	—	—
1104	—	46690	N/A	Sect Unbalanced Pole#2 27 Pickup	—	—
1105	—	46691	N/A	Sect Unbalanced Pole#2 27 Definite Time	—	—
1106	—	46692	N/A	Sect Unbalanced Pole#2 50 Pickup	—	—
1107	—	46693	N/A	Sect Unbalanced Pole#2 50 Definite Time	—	—
1108	—	46694	N/A	Sect Unbalanced Pole#2 59 Pickup	—	—
1109	—	46695	N/A	Sect Unbalanced Pole#2 59 Definite Time	—	—
1110	—	46696	N/A	Sect Unbalanced Pole#2 51 Pickup	—	—
1111	—	46697	N/A	Sect Unbalanced Pole#2 51 Curve	—	—
1112	—	46698	N/A	Sect Unbalanced Pole#2 51 Time Multiplier	—	—
1113	—	46699	N/A	Sect Unbalanced Pole#2 51 Elec Mech Reset	—	—
1114	—	46701	N/A	Sect Unbalanced Pole#2 51 Time Adder	—	—
1115	—	46702	N/A	Sect Unbalanced Pole#2 51 Min response Time Adder	—	—
1116	—	46704	N/A	Sect Unbalanced Pole#2 51 Reset co-efficient	—	—
1117	—	65081	N/A	LOS Pole#1 Under voltage Definite Time	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
1118	—	65082	N/A	LOS Pole#1 Over voltage Definite Time	—	—
1119	—	65085	N/A	LOS Pole#2 Under voltage Definite Time	—	—
1120	—	65086	N/A	LOS Pole#2 Over voltage Definite Time	—	—
1121	—	4844	N/A	Phase of Pole#1	—	—
1122	—	4845	N/A	Phase of Pole#2	—	—
1123	—	7569	N/A	Pole#1 51 MTT Active Profile	—	—
1124	—	7569	N/A	Pole#1 51 MTT Profile 1	—	—
1125	—	9869	N/A	Pole#1 51 MTT Profile 2	—	—
1126	—	12169	N/A	Pole#1 51 MTT Profile 3	—	—
1127	—	14469	N/A	Pole#1 51 MTT Profile 4	—	—
1128	—	16769	N/A	Pole#1 51 MTT Profile 5	—	—
1129	—	19069	N/A	Pole#1 51 MTT Profile 6	—	—
1130	—	21369	N/A	Pole#1 51 MTT Profile 7	—	—
1131	—	23669	N/A	Pole#1 51 MTT Profile 8	—	—
1132	—	7582	N/A	Pole#2 51 MTT Active Profile	—	—
1133	—	7582	N/A	Pole#2 51 MTT Profile 1	—	—
1134	—	9882	N/A	Pole#2 51 MTT Profile 2	—	—
1135	—	12182	N/A	Pole#2 51 MTT Profile 3	—	—
1136	—	14482	N/A	Pole#2 51 MTT Profile 4	—	—
1137	—	16782	N/A	Pole#2 51 MTT Profile 5	—	—
1138	—	19082	N/A	Pole#2 51 MTT Profile 6	—	—
1139	—	21382	N/A	Pole#2 51 MTT Profile 7	—	—
1140	—	23682	N/A	Pole#2 51 MTT Profile 8	—	—
1141	—	7305	N/A	Pole#1 50 MTT Active Profile	—	—
1142	—	7305	N/A	Pole#1 50 MTT Profile 1	—	—
1143	—	9605	N/A	Pole#1 50 MTT Profile 2	—	—
1144	—	11905	N/A	Pole#1 50 MTT Profile 3	—	—
1145	—	14205	N/A	Pole#1 50 MTT Profile 4	—	—
1146	—	16505	N/A	Pole#1 50 MTT Profile 5	—	—
1147	—	18805	N/A	Pole#1 50 MTT Profile 6	—	—
1148	—	21105	N/A	Pole#1 50 MTT Profile 7	—	—
1149	—	23405	N/A	Pole#1 50 MTT Profile 8	—	—
1150	—	7310	N/A	Pole#2 50 MTT Active Profile	—	—
1151	—	7310	N/A	Pole#2 50 MTT Profile 1	—	—

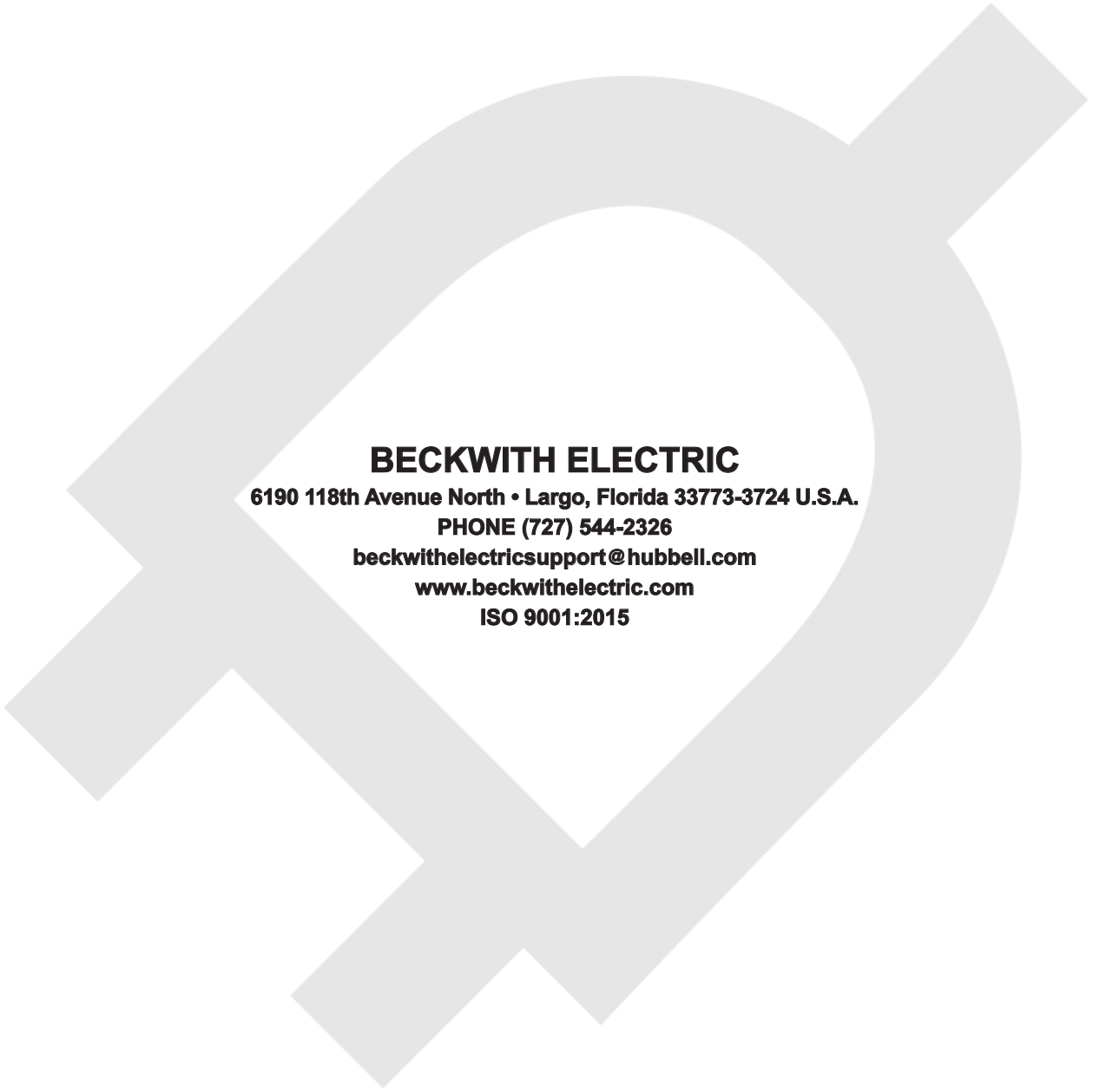
Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Analog Input List (Status) (Pole 1 & Pole 2)						
Data Points	Scale	Register Number	Unit	Description	Range Value	Request Function
1152	—	9610	N/A	Pole#2 50 MTT Profile 2	—	—
1153	—	11910	N/A	Pole#2 50 MTT Profile 3	—	—
1154	—	14210	N/A	Pole#2 50 MTT Profile 4	—	—
1155	—	16510	N/A	Pole#2 50 MTT Profile 5	—	—
1156	—	18810	N/A	Pole#2 50 MTT Profile 6	—	—
1157	—	21110	N/A	Pole#2 50 MTT Profile 7	—	—
1158	—	23410	N/A	Pole#2 50 MTT Profile 8	—	—
1159	—	4885	N/A	FBM percent life used Pole#1	—	—
1160	—	4886	N/A	FBM percent life used Pole#2	—	—
1161	—	4888	N/A	FBM percent life Remaining Pole#1	—	—
1162	—	4889	N/A	FBM percent life Remaining Pole#2	—	—
1163	—	5028	N/A	Power Target #1 Threshold Secondary	—	—
1164	—	5030	N/A	Power Target #2 Threshold Secondary	—	—
1165	—	5032	N/A	Power Target #3 Threshold Secondary	—	—
1166	—	5034	N/A	Power Target #4 Threshold Secondary	—	—
1167	—	5036	N/A	Power Target #5 Threshold Secondary	—	—
1168	—	5038	N/A	Power Target #6 Threshold Secondary	—	—
1169	—	5040	N/A	Power Target #7 Threshold Secondary	—	—
1170	—	5042	N/A	Power Target #8 Threshold Secondary	—	—
1171	—	5044	N/A	Power Target #9 Threshold Secondary	—	—
1172	—	5046	N/A	Power Target #10 Threshold Secondary	—	—
1173	—	5048	N/A	Power Target #11 Threshold Secondary	—	—
1174	—	5050	N/A	Power Target #12 Threshold Secondary	—	—
1175	—	1166	N/A	Operations Counter Pole#1	—	—
1176	—	1168	N/A	Operations Counter Pole#2	—	—
1177	—	3914	N/A	Serial Number	—	—
1178	—	3775	N/A	Control Temperature	—	—
1179	—	4998	N/A	Peak Demand Current Magnitude Pole#1	—	—
1180	—	5052	N/A	Peak Demand Current Magnitude Pole#2	—	—

Table 8 Analog Input List (Status) (Pole 1 & Pole 2)

Binary Counter Lists (Pole 1 & Pole 2)						
Data Point	Scale	Register Number	Unit	Description	Range Value	Request Function
0	–	1166	N/A	Operation Counters Pole#1	–	–
1	–	1168	N/A	Operation Counters Pole#2	–	–
2	–	1150	N/A	Phase OC Pickup Counter Pole#1	–	–
3	–	1154	N/A	Phase OC Pickup Counter Pole#2	–	–
4	–	1152	N/A	Phase OC Trip Counter Pole#1	–	–
5	–	1156	N/A	Phase OC Trip Counter Pole#2	–	–
6	–	1174	N/A	IPSlogic Counter #1	–	–
7	–	1175	N/A	IPSlogic Counter #2	–	–
8	–	1176	N/A	IPSlogic Counter #3	–	–
9	–	1177	N/A	IPSlogic Counter #4	–	–
10	–	1178	N/A	IPSlogic Counter #5	–	–
11	–	1179	N/A	IPSlogic Counter #6	–	–
12	–	1180	N/A	IPSlogic Counter #7	–	–
13	–	1081	N/A	IPSlogic Counter #8	–	–
14	–	1195	N/A	CBEMA Event Counter Pole#1	–	–
15	–	1196	N/A	CBEMA Event Counter Pole#2	–	–
16	–	2006	N/A	Sectionalizer Counter Pole#1	–	–
17	–	2007	N/A	Sectionalizer Counter Pole#2	–	–
18	–	1100	N/A	GlobalLogic Counter #1	–	–
19	–	1101	N/A	GlobalLogic Counter #2	–	–
20	–	1102	N/A	GlobalLogic Counter #3	–	–
21	–	1103	N/A	GlobalLogic Counter #4	–	–
22	–	1104	N/A	GlobalLogic Counter #5	–	–
23	–	1105	N/A	GlobalLogic Counter #6	–	–
24	–	1106	N/A	GlobalLogic Counter #7	–	–
25	–	1107	N/A	GlobalLogic Counter #8	–	–

Table 9 Binary Counter Lists (Pole 1 & Pole 2)



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