

Block Diagram for 700-DI ≤5kVA HIPOTRONICS Regulator (\leq 5kVA) Device Under Test + Embedded HV Tank -----(DUT) Controller Ethernet 11 Ethernet DI-FO Hub #1 MCF Coupling Capacitor + qPD DI-FO-xx Fiber Line Item E-Stop DI-FO Hub #2 **DI-REM-SFTW Option PD** Detector Ethernet Ethernet PC **PD** Detection Apparatus

(May be included in regulator if All-Inclusive System)

(customer-supplied)

Interlock Integration

HIPOTRONICS

All 700-Series systems are supplied with a 4-pin XLR plug to integrate with an external interlock circuit as a standard feature.

The Interlock circuit contains two separate channels of Command-Out/Status-In pairs. The plug is supplied with a jumper wire between Pins 1&4, and between Pins 2&3. To connect either channel to an external interlock, remove the jumper and connect the channel to a set of contacts. A wiring example is shown in the table.

When the controller detects 24Vdc on Pins 3 and 4, it interprets "Interlock Safe!" and allows HV test to take place. If at any time the controller detects 0V on Pin 3 OR Pin 4, it interprets "Interlock Danger!" and disables the HV circuit.



EXAMPLE INTERLOCK WIRING		
PIN #	SIGNAL	CONNECT TO
1	Channel 1 Command (24Vdc OUT)	Safety Fence Norm. Open
2	Channel 2 Command (24Vdc OUT)	Foot Switch Norm. Open
3	Channel 2 Status (24Vdc IN)	Foot Switch Common
4	Channel 1 Status (24Vdc IN)	Safety Fence Common

